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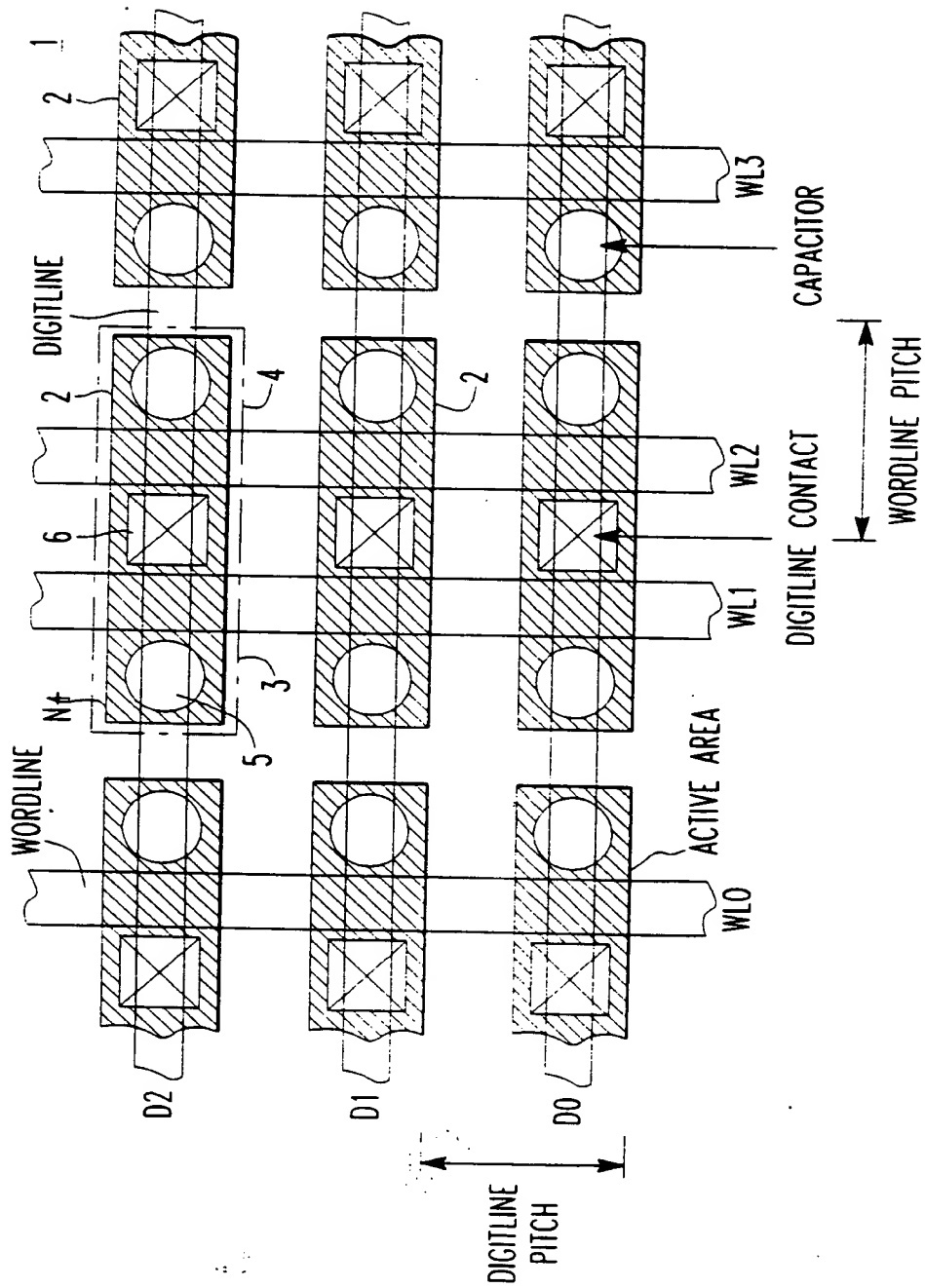
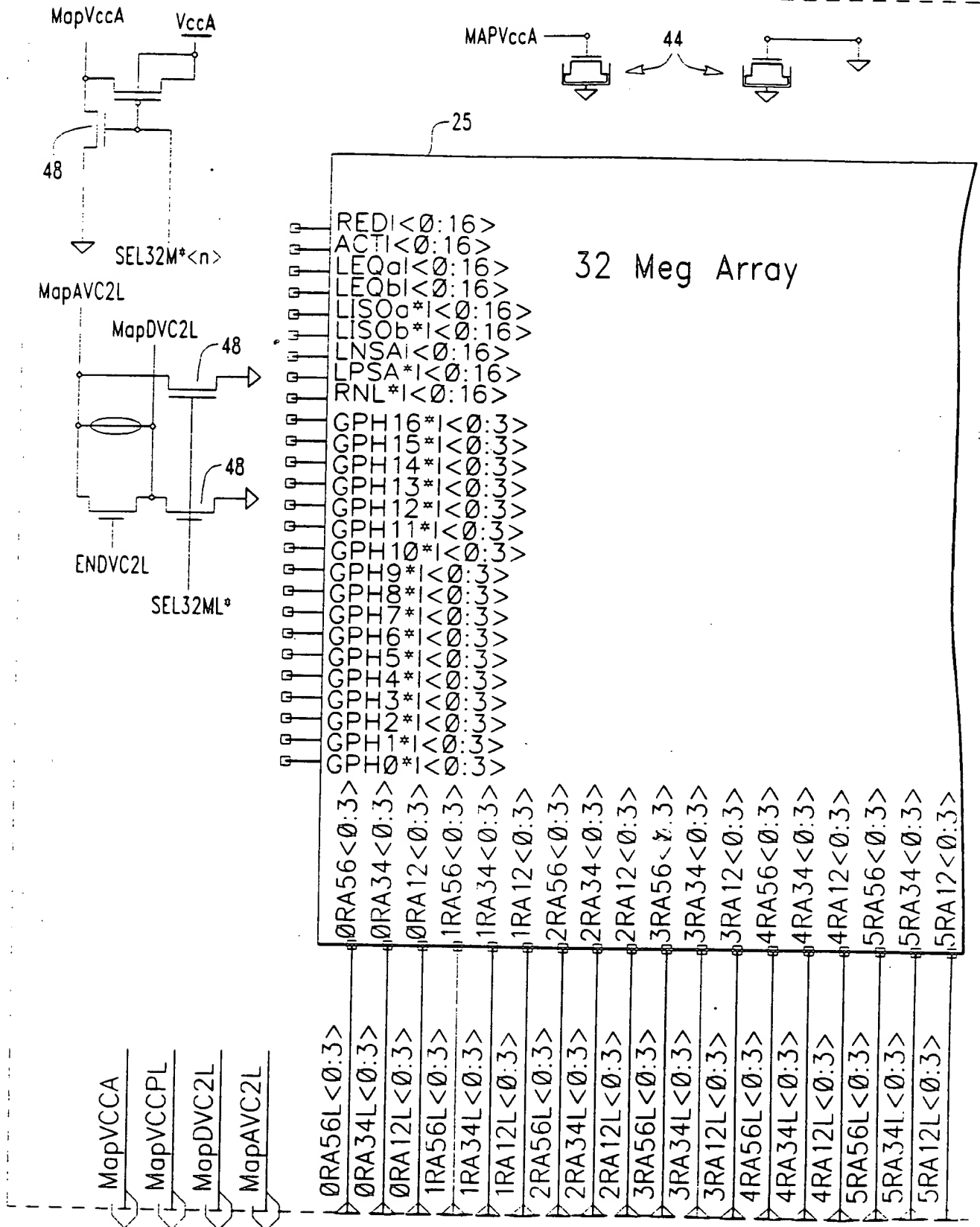


FIG. 1



FIG. 3A

3,367



32 Meg Array

```

REDr<0:16>
ACTr<0:16>
LEQar<0:16>
LEQbr<0:16>
LISOa*r<0:16>
LISOb*r<0:16>
LNSAr<0:16>
LPSA*r<0:16>
RNL*r<0:16>

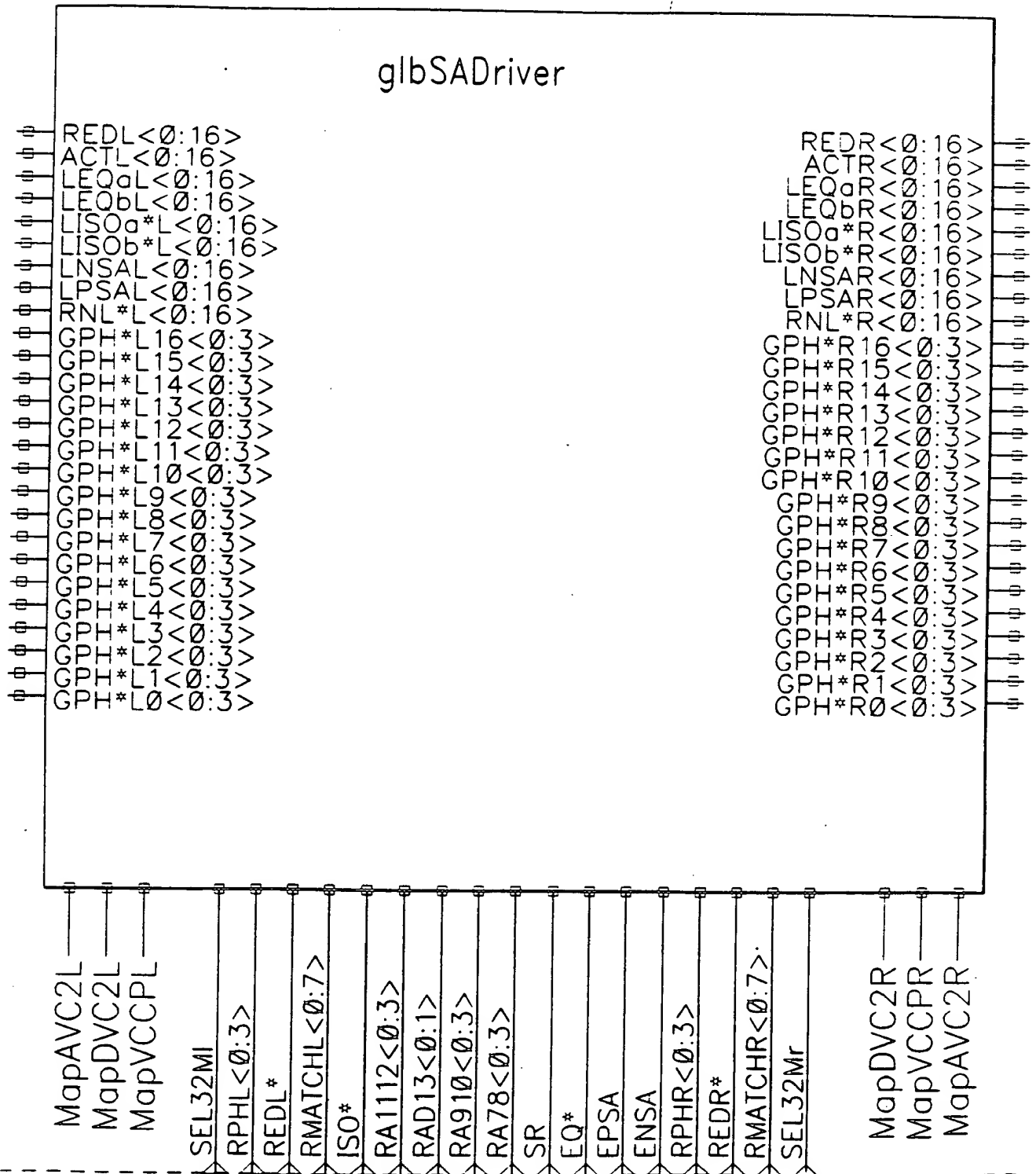
GPH16*r<0:3>
GPH15*r<0:3>
GPH14*r<0:3>
GPH13*r<0:3>
GPH12*r<0:3>
GPH11*r<0:3>
GPH10*r<0:3>
GPH9*r<0:3>
GPH8*r<0:3>
GPH7*r<0:3>
GPH6*r<0:3>
GPH5*r<0:3>
GPH4*r<0:3>
GPH3*r<0:3>
GPH2*r<0:3>
GPH1*r<0:3>
GPH0*r<0:3>

```

6RA56L<0:3>	6RA56<0:3>
6RA34L<0:3>	6RA34<0:3>
6RA12L<0:3>	6RA12<0:3>
7RA56L<0:3>	7RA56<0:3>
7RA34L<0:3>	7RA34<0:3>
7RA12L<0:3>	7RA12<0:3>
8RA56L<0:3>	8RA56<0:3>
8RA34L<0:3>	8RA34<0:3>
8RA12L<0:3>	8RA12<0:3>
CSELL<0:1055>	CSEL<0:1055>
DLaL<0:7>	DLa<0:7>
DLaL*<0:7>	DLa*<0:7>
DLbL<0:7>	DLb<0:7>
DLbL*<0:7>	DLb*<0:7>
DLcL<0:7>	DLc<0:7>
DLcL*<0:7>	DLc*<0:7>
DLdL<0:7>	DLd<0:7>
DLdL*<0:7>	DLd*<0:7>

64 Meg Array

- 29



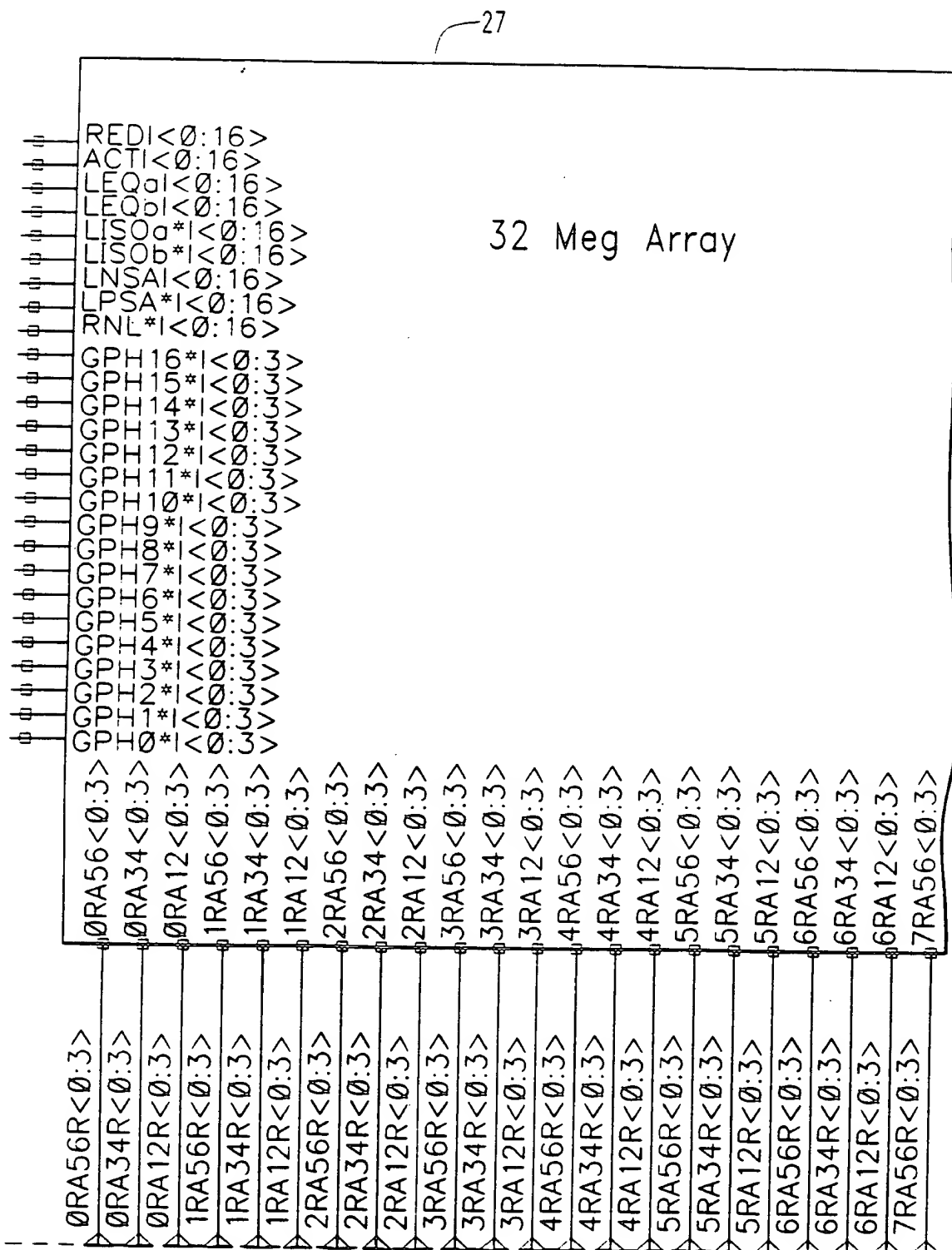
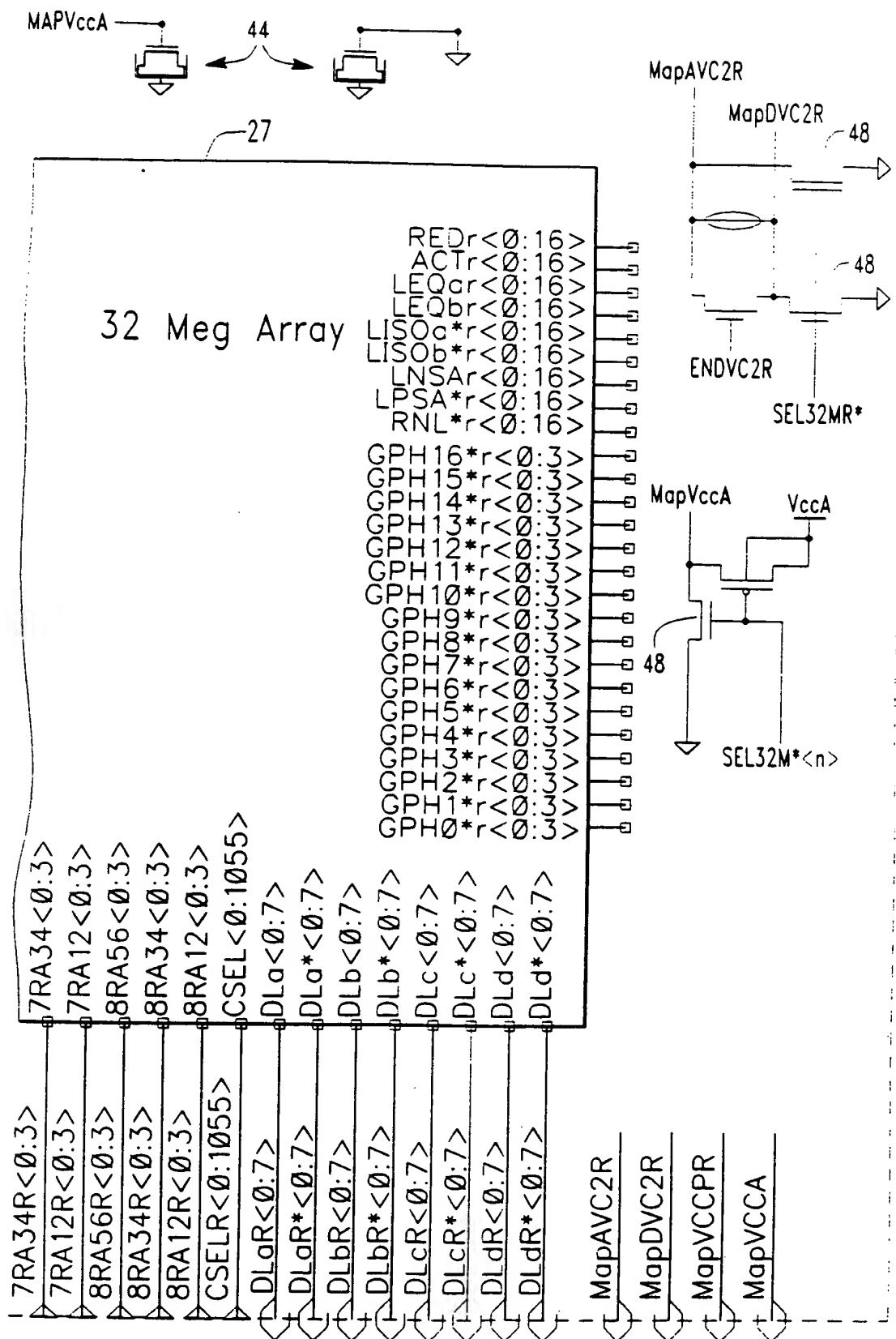


FIG. 3E

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32MEG ARRAY

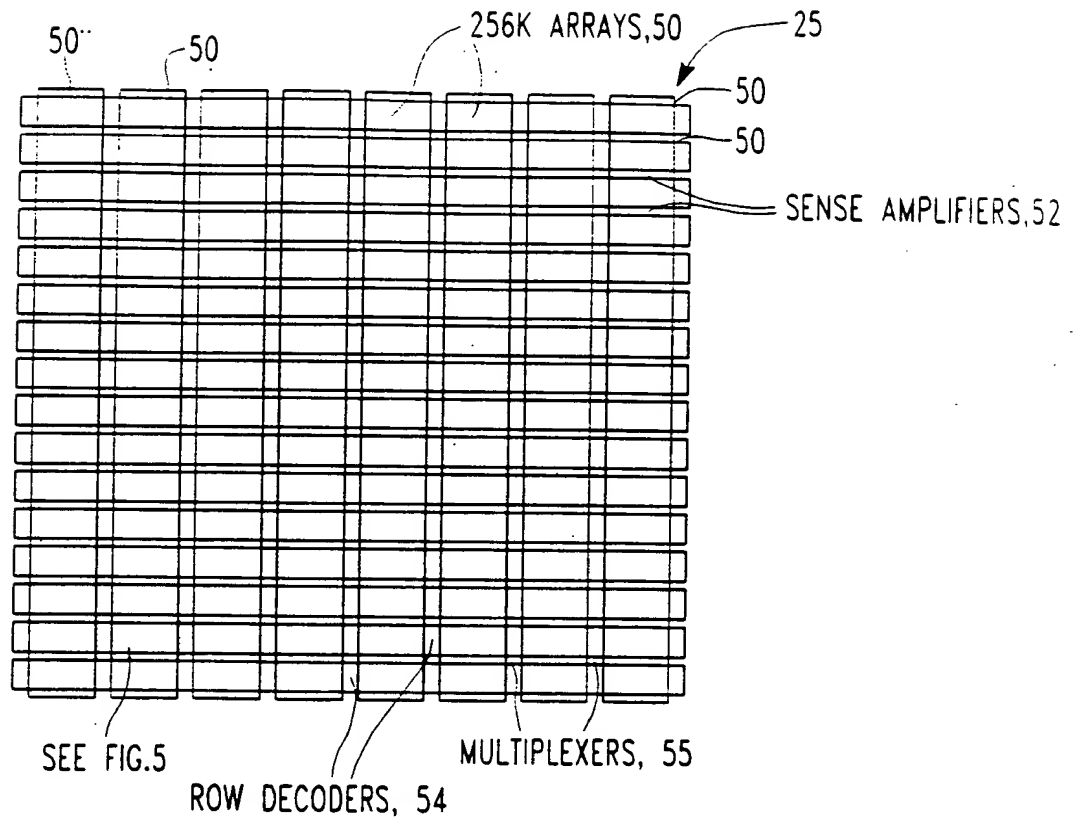


FIG. 4

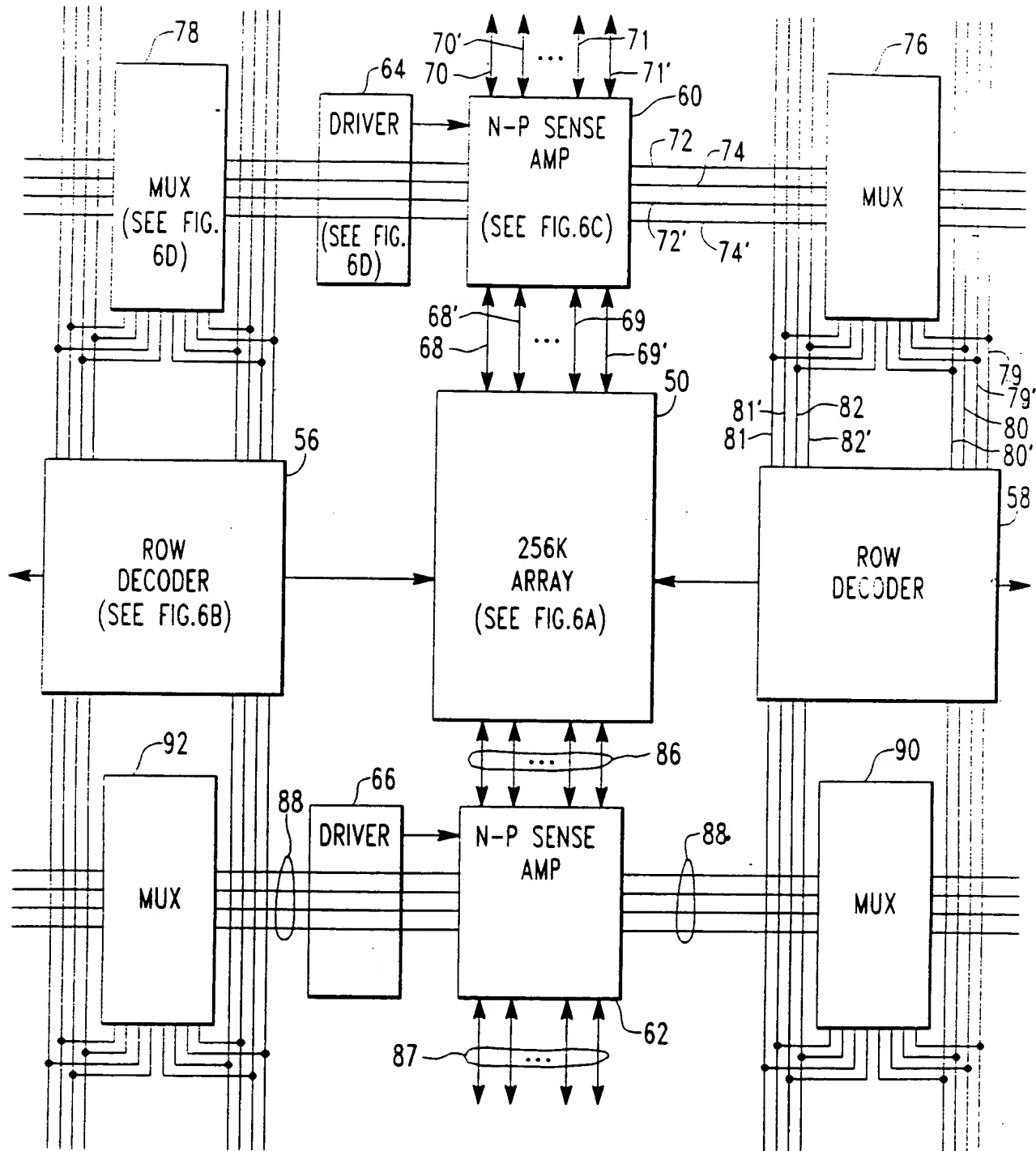


FIG. 5

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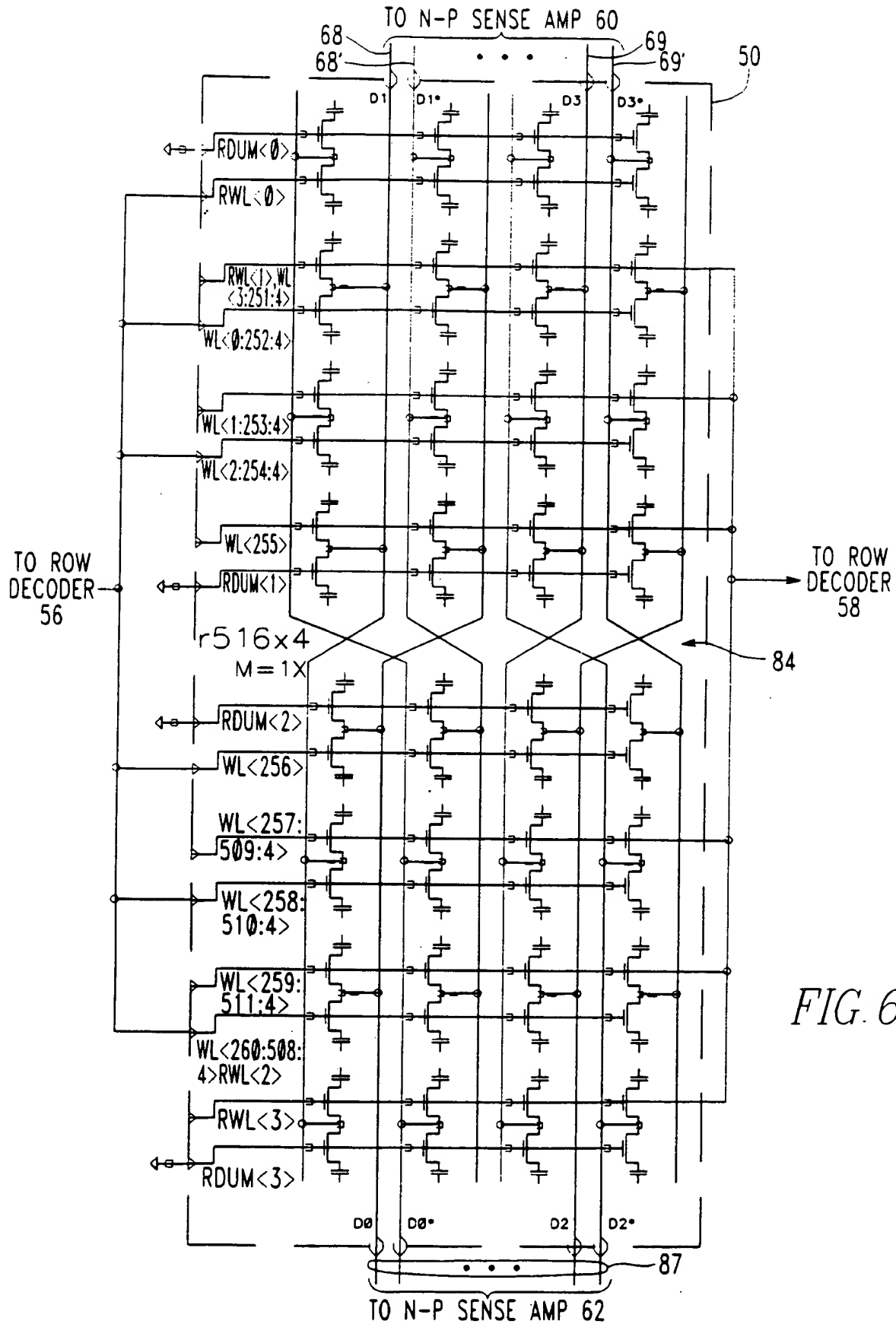
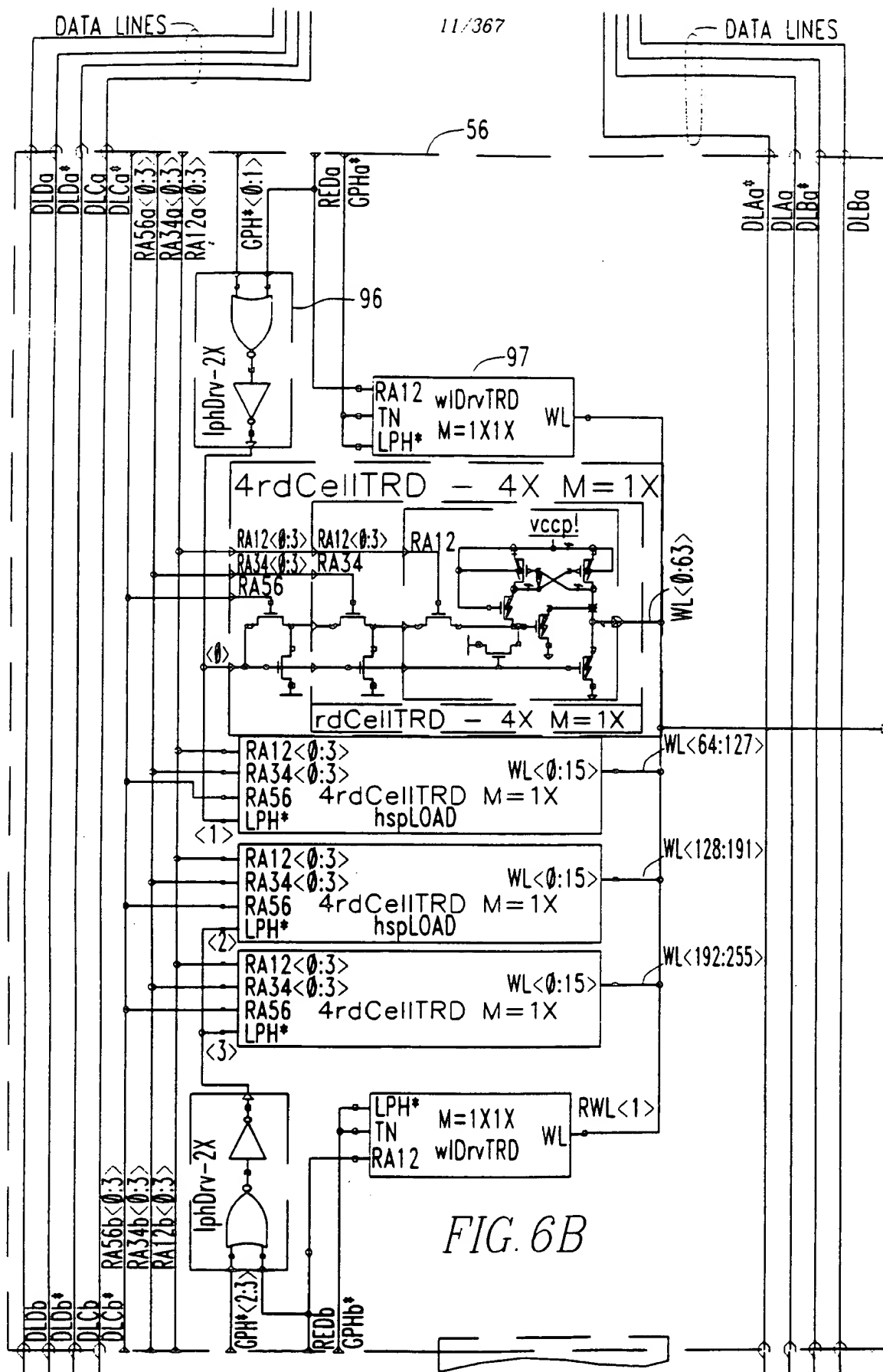


FIG. 6A

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Connections of odd/even
columns to IOa and IOb
alternates with odd/even
column select lines:
CA01* D1(even) D2(odd)

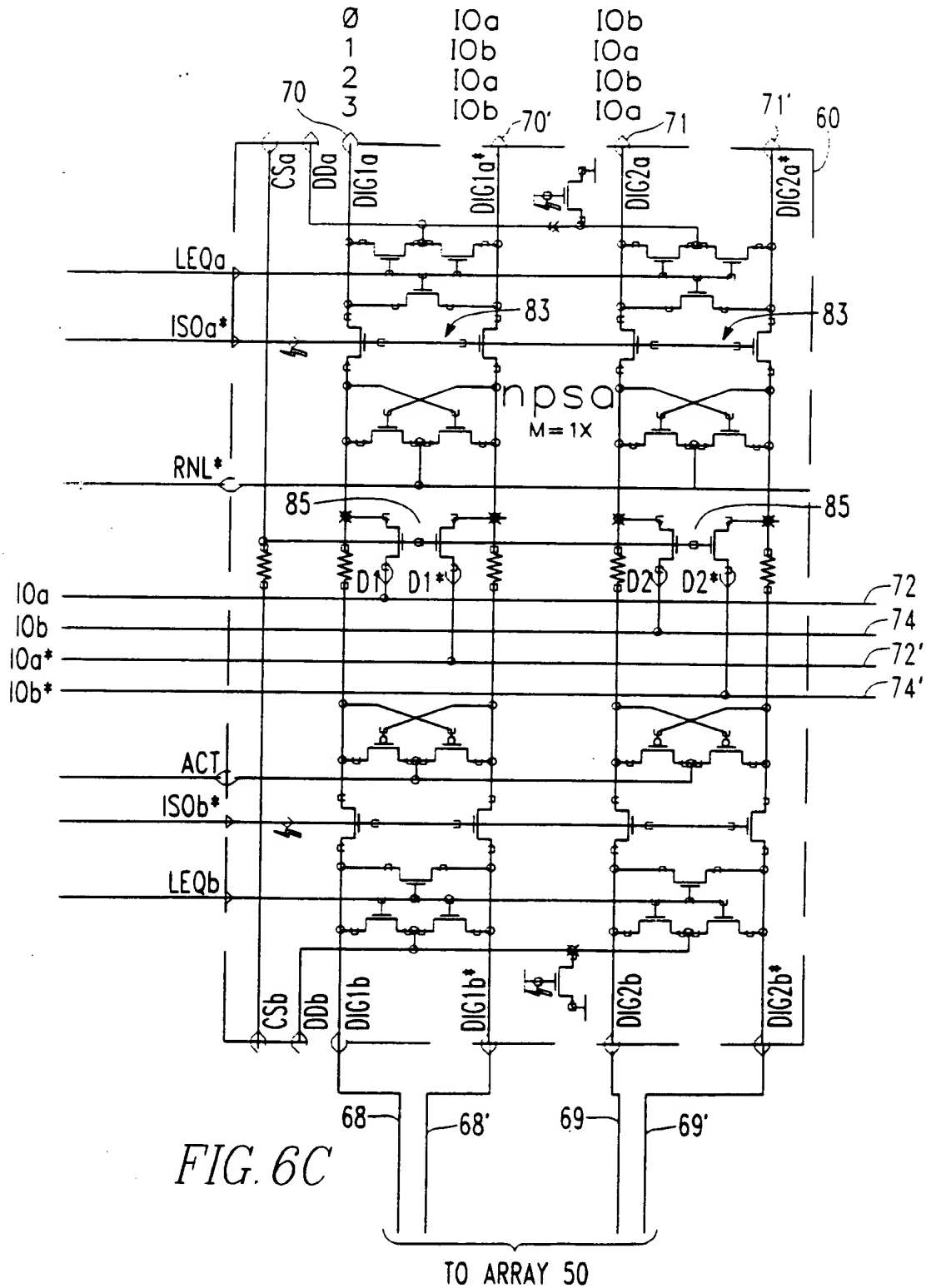


FIG. 6C

13,367

78

LNSA

LNSA

DATA
LINES

64

LEQa

LEQa

ISOa*

RNL*

IOab

IObb

IOab*

IObb*

IOat

IObt

IOat*

IObt*

IOa

IOb

IOa*

IOb*

63

AC

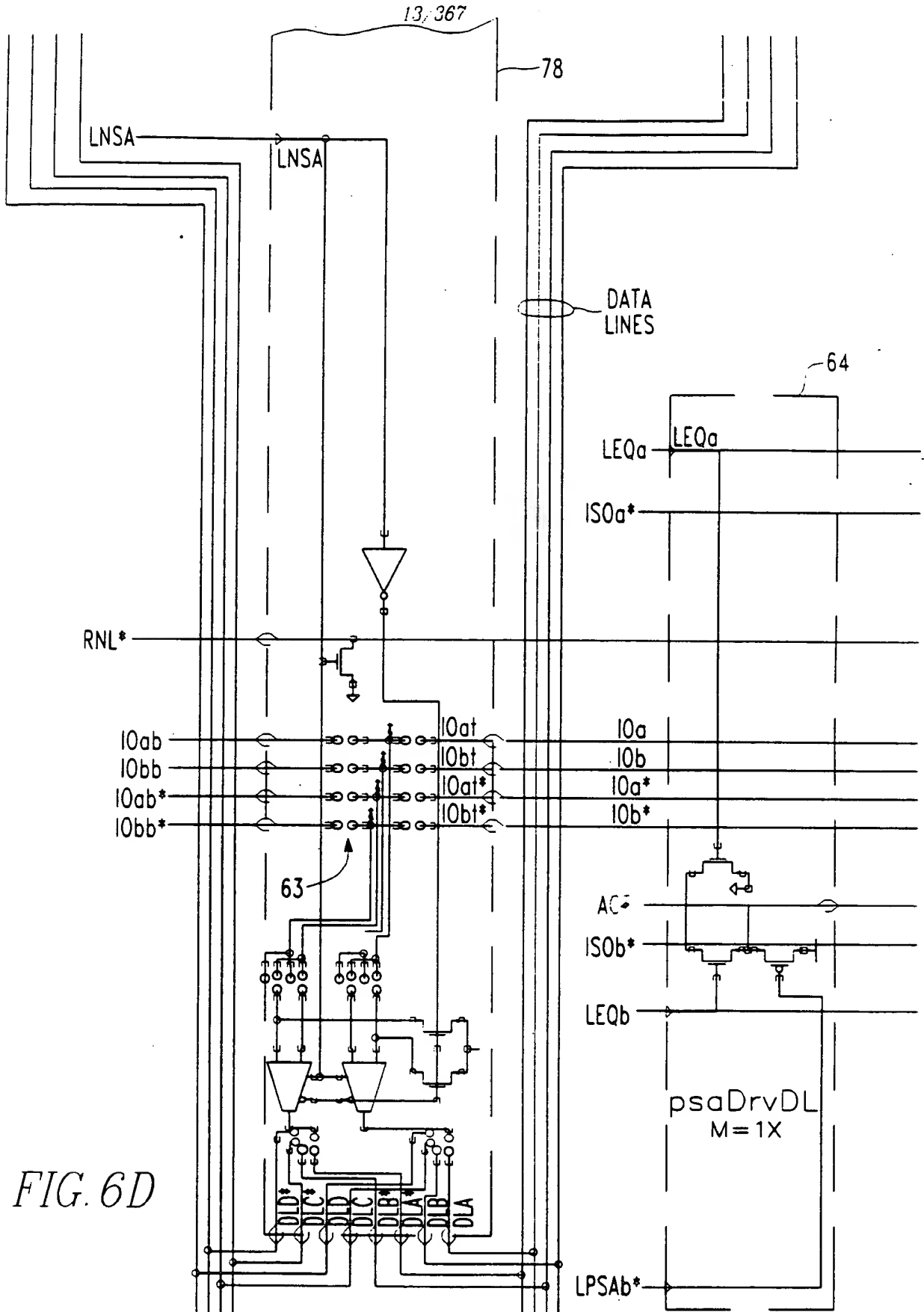
ISOb*

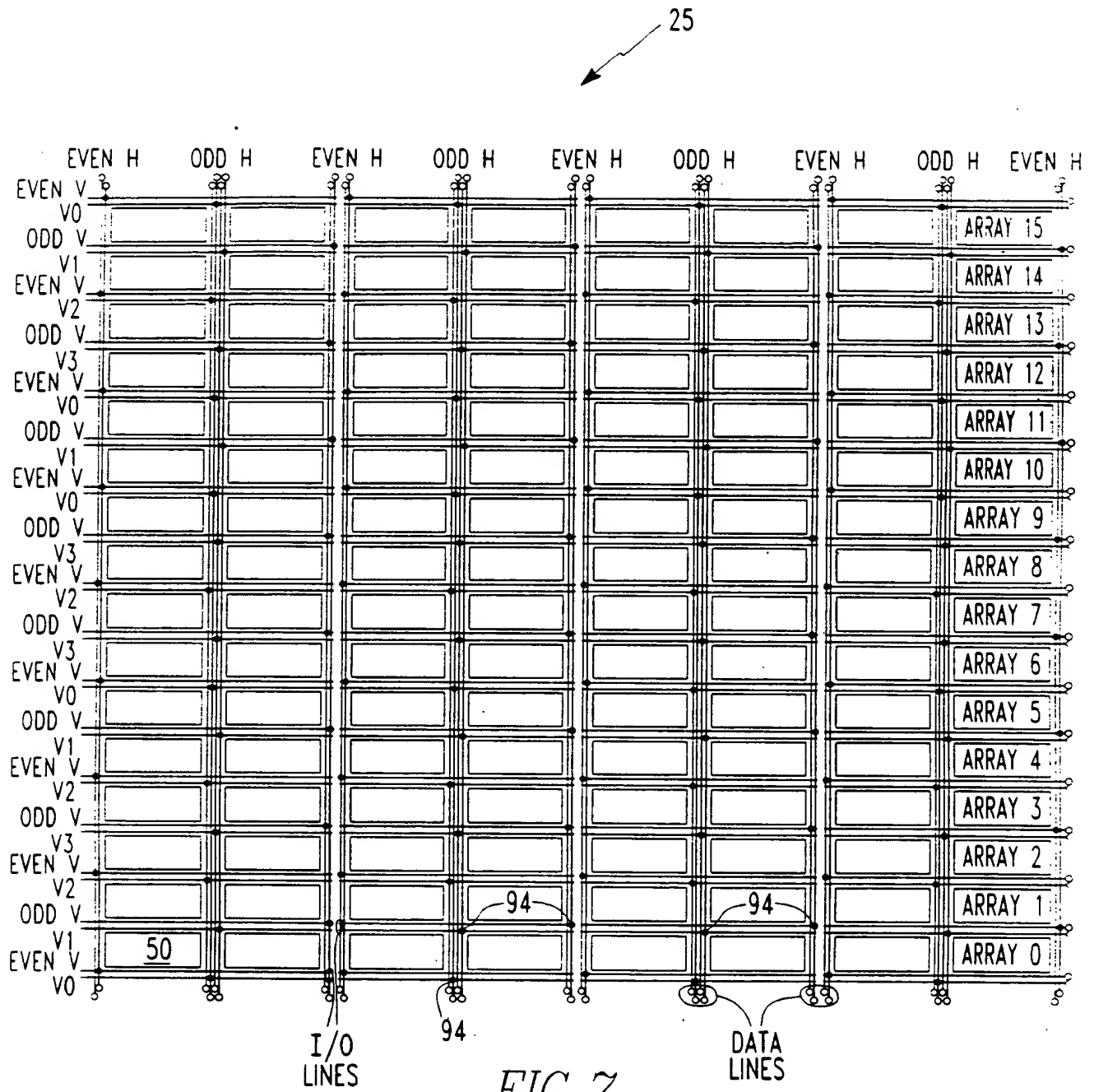
LEQb

psaDrvDL
M=1X

LPSAb*

FIG. 6D





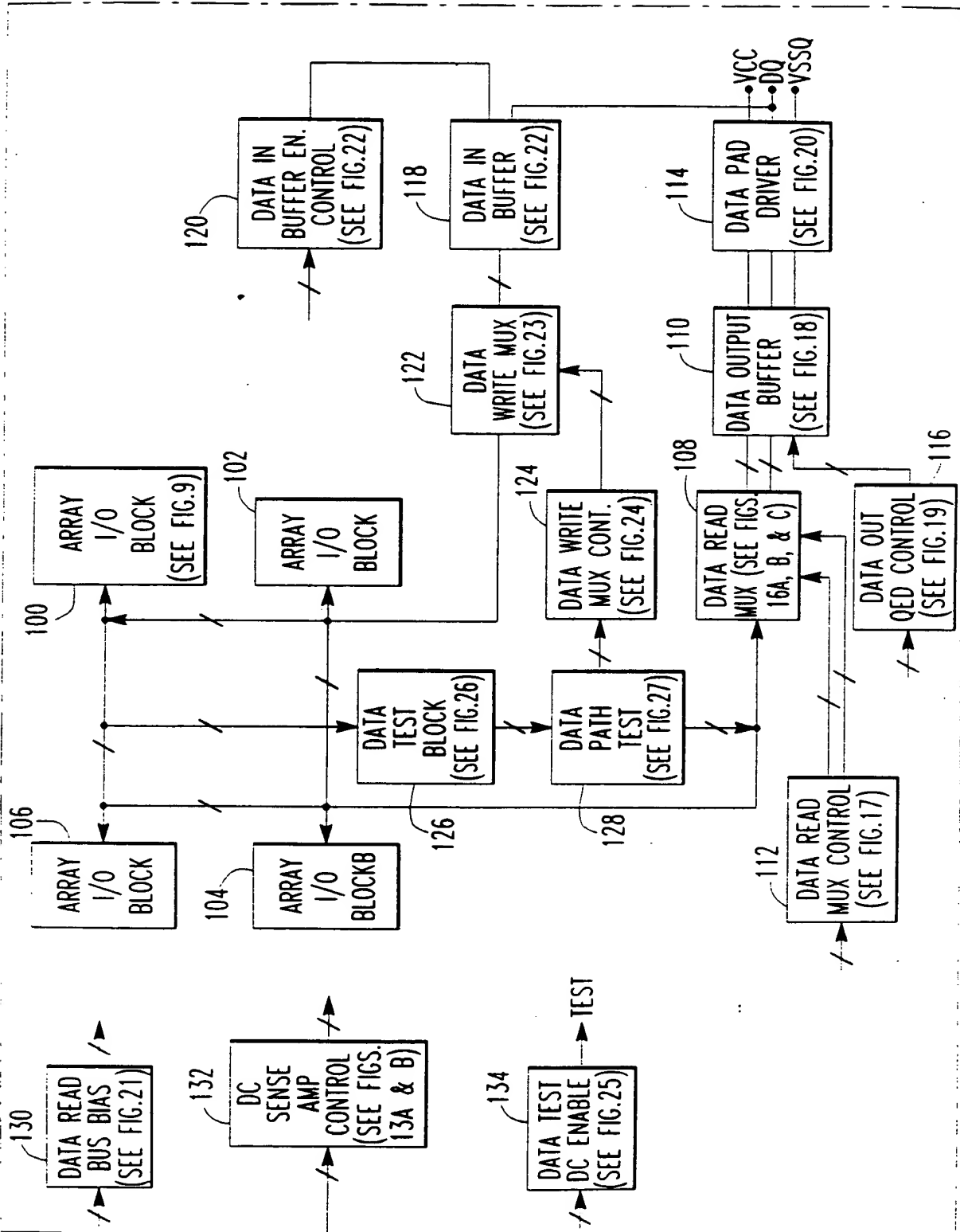


FIG. 8

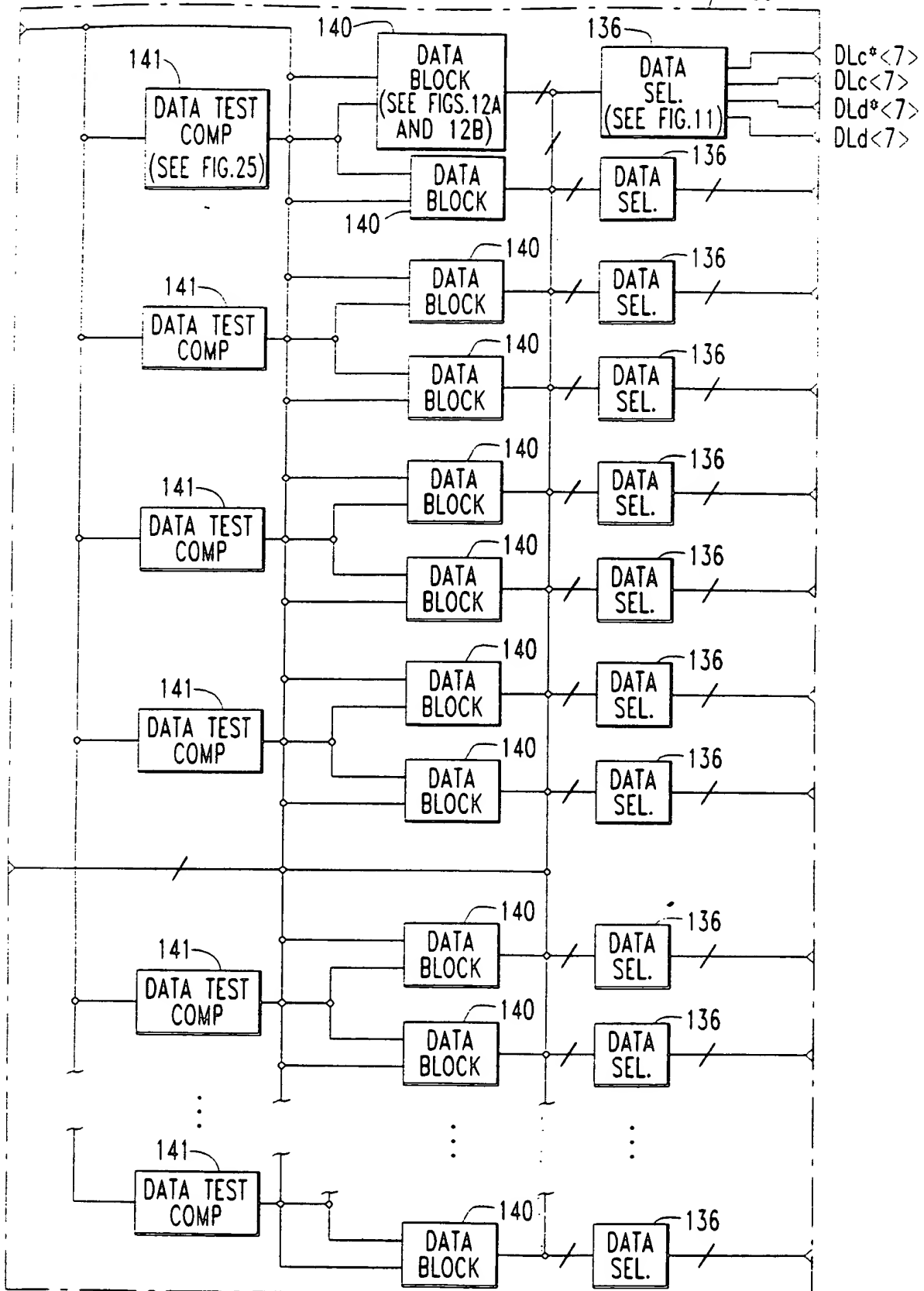


FIG. 9

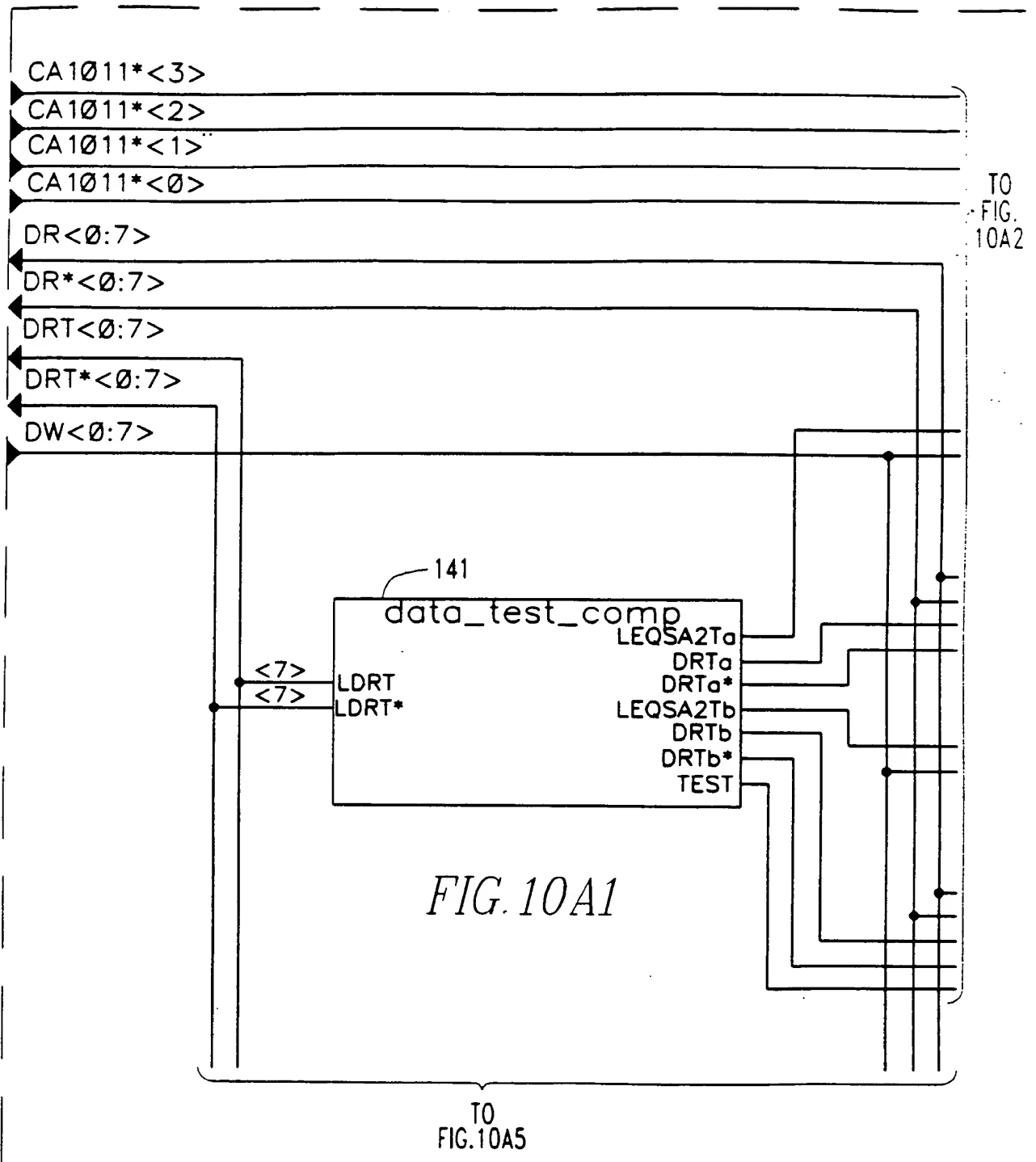


FIG. 10A2

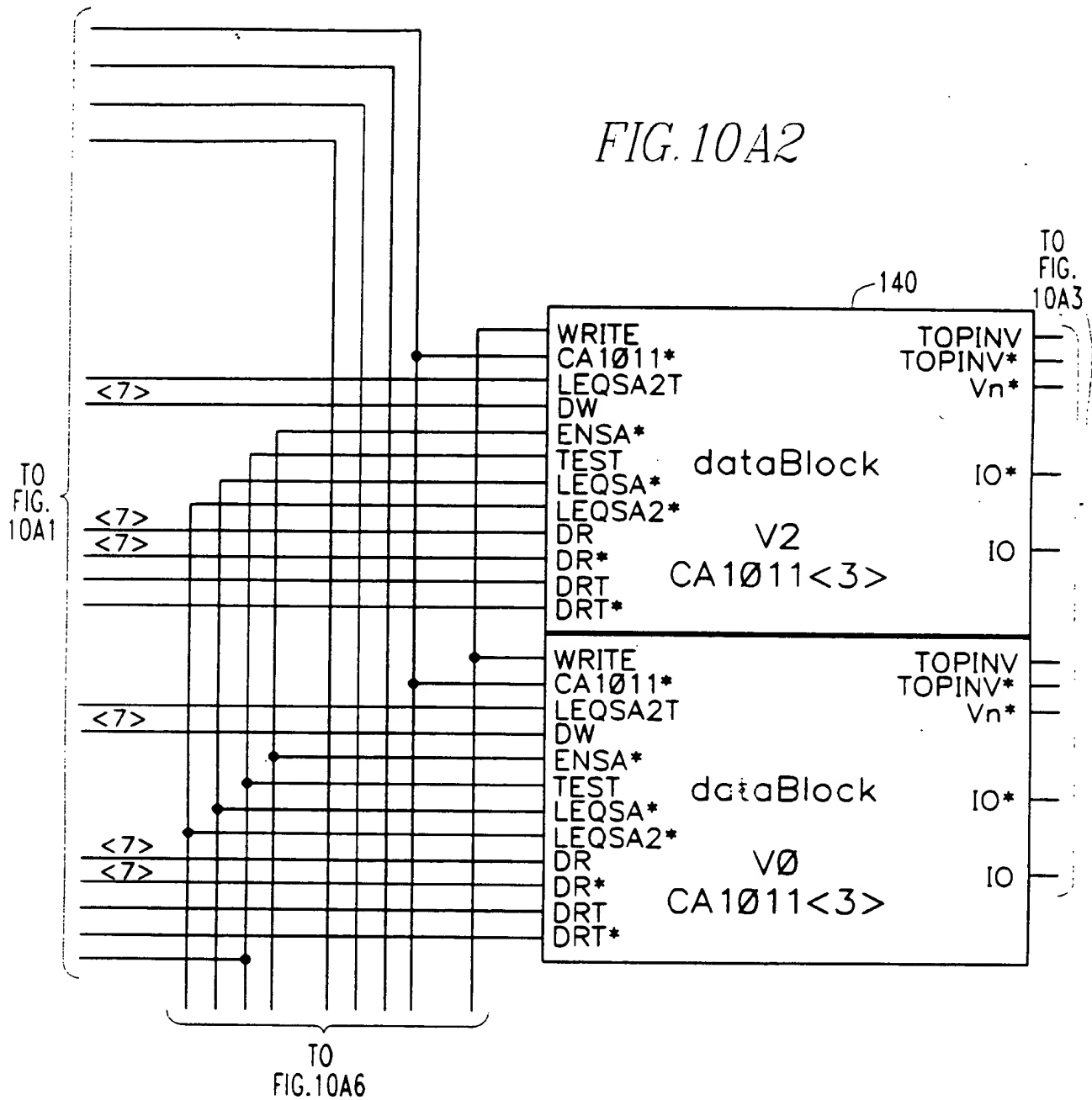
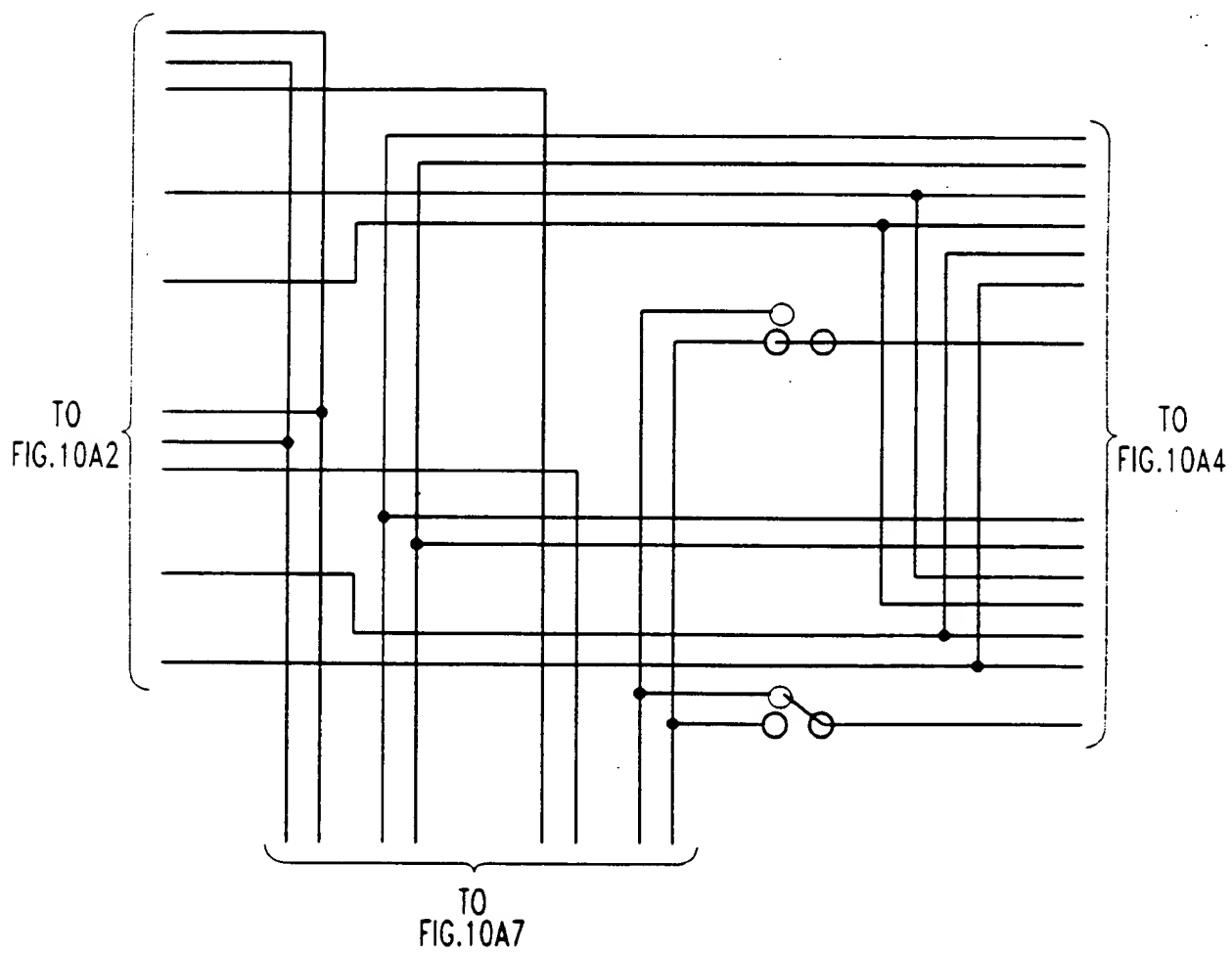


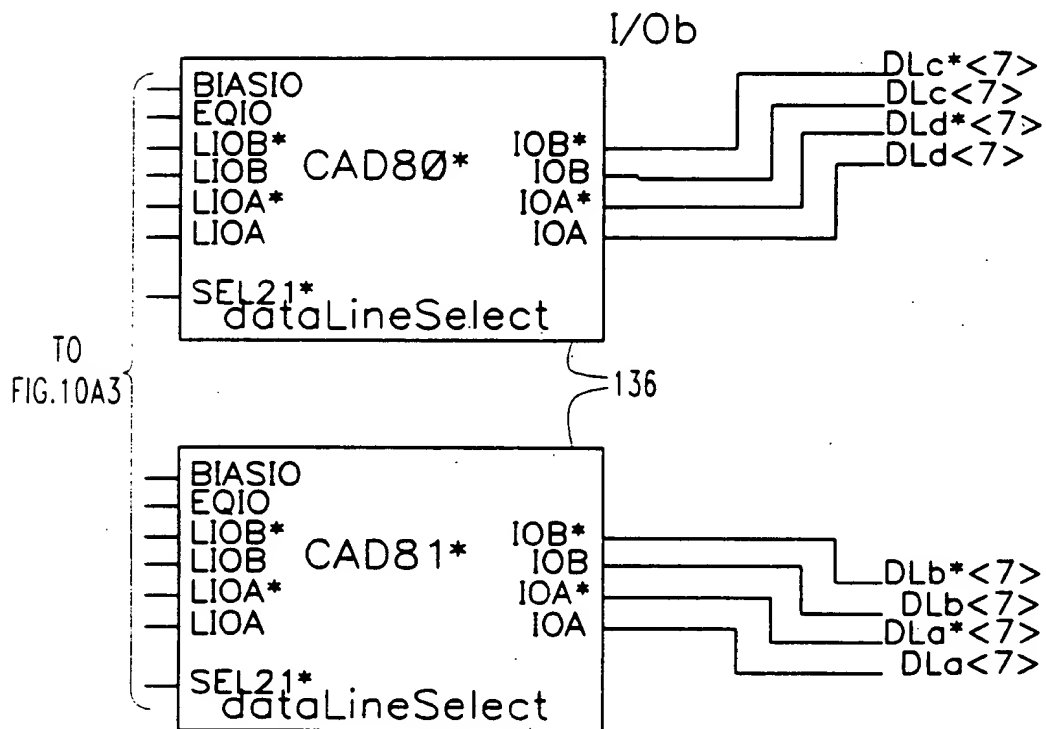
FIG. 10A3



arrayIOBlock

100

FIG. 10A4

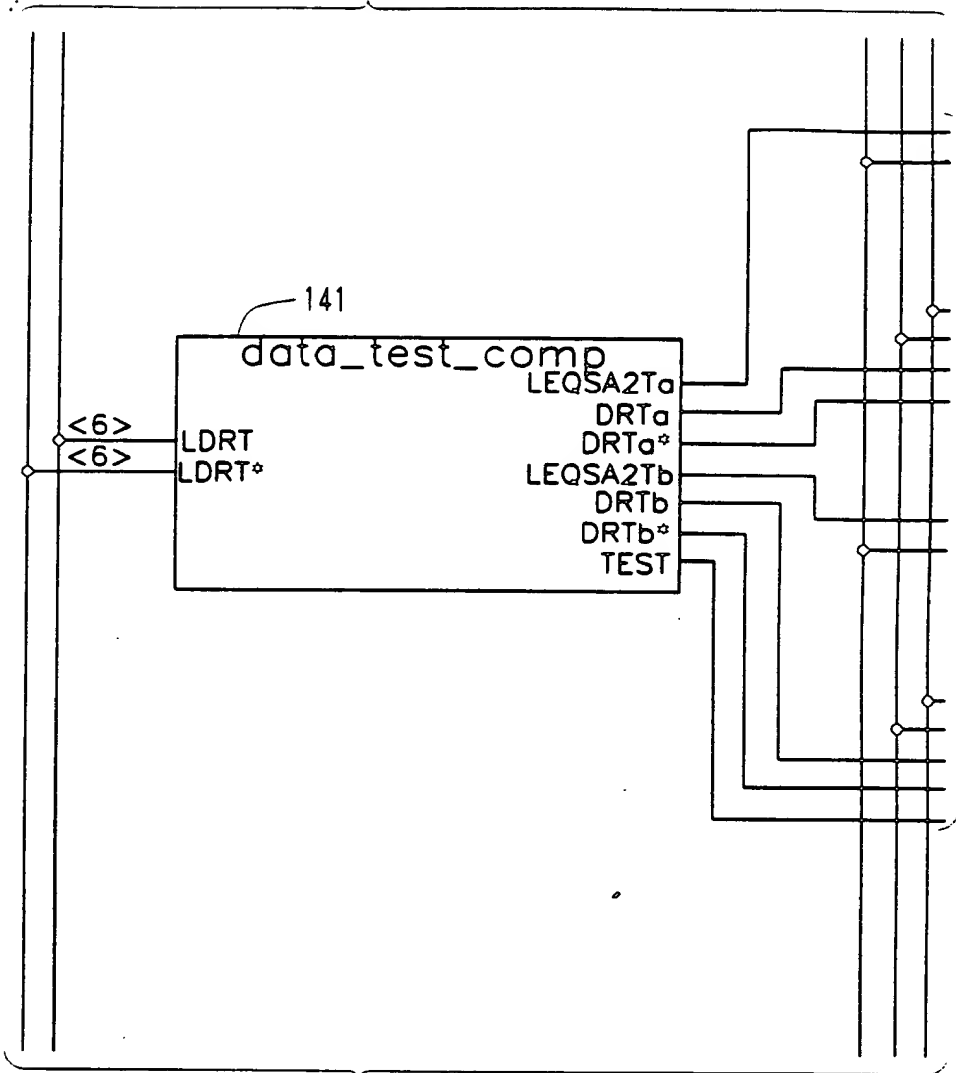


TO
FIG.10A1

TO
FIG.
10A6

TO
FIG.10B1

FIG.10A5



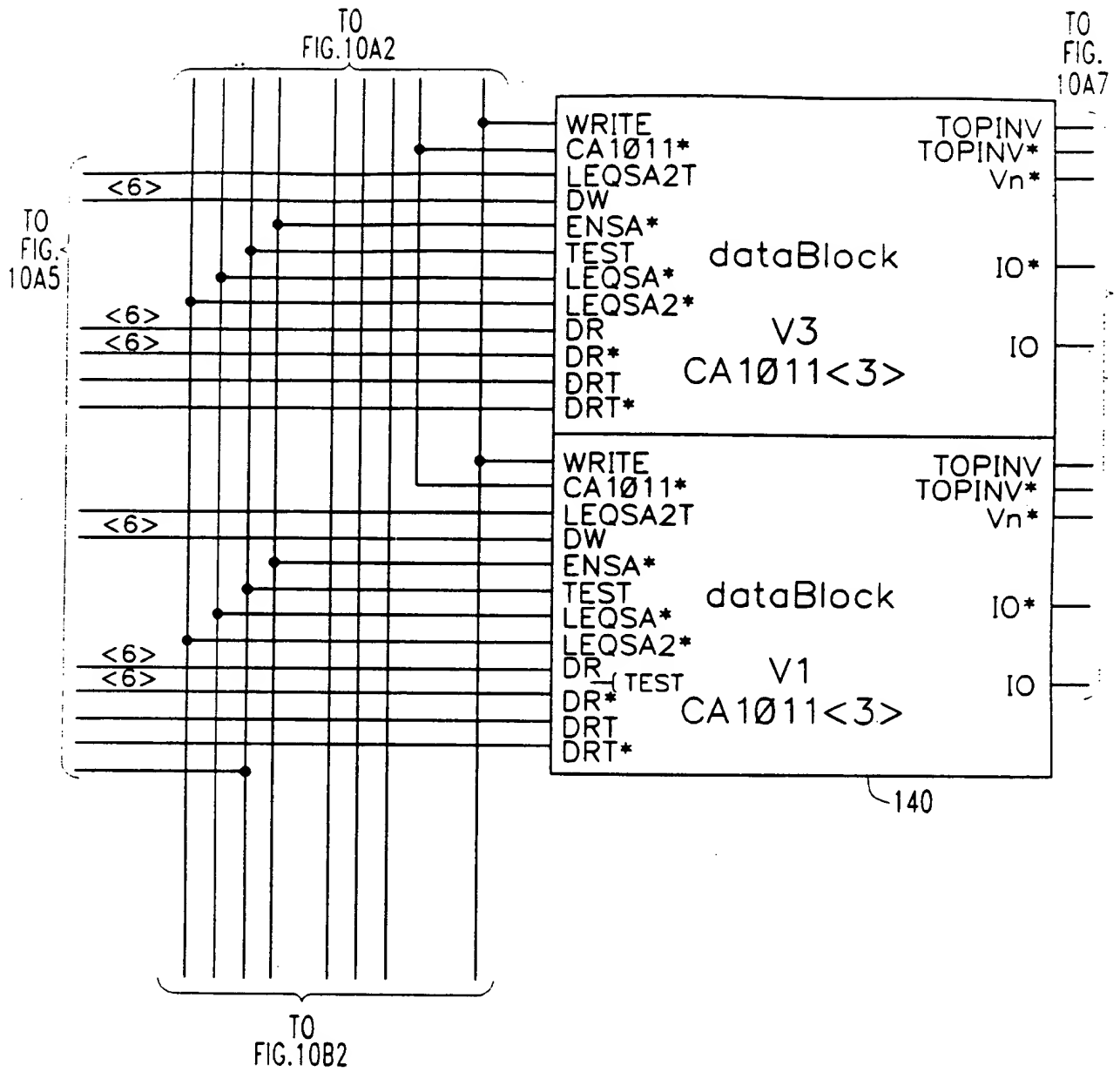
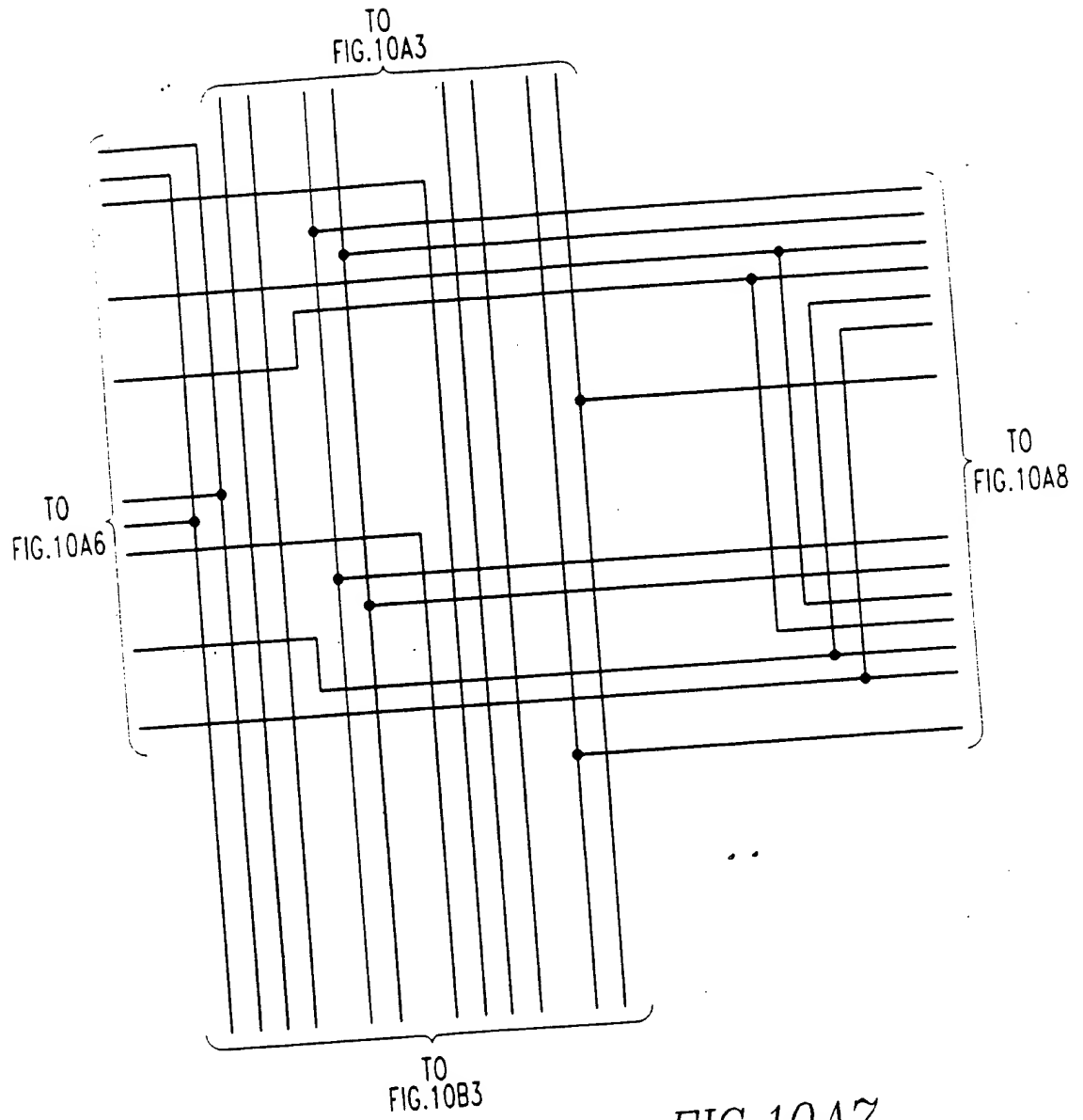


FIG. 10A6



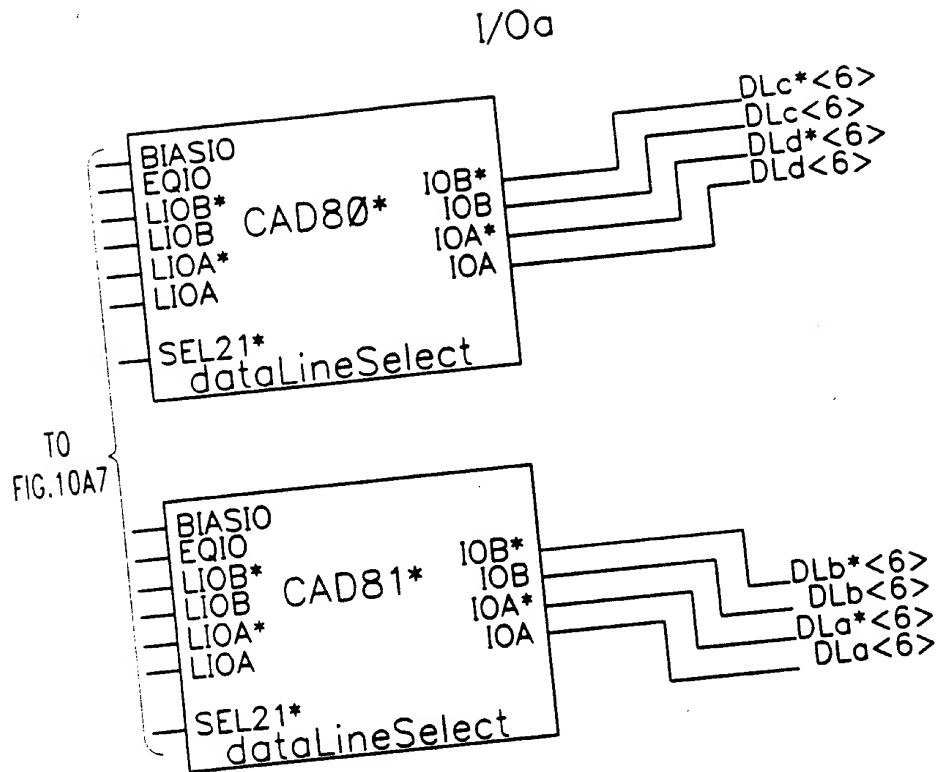


FIG. 10A8

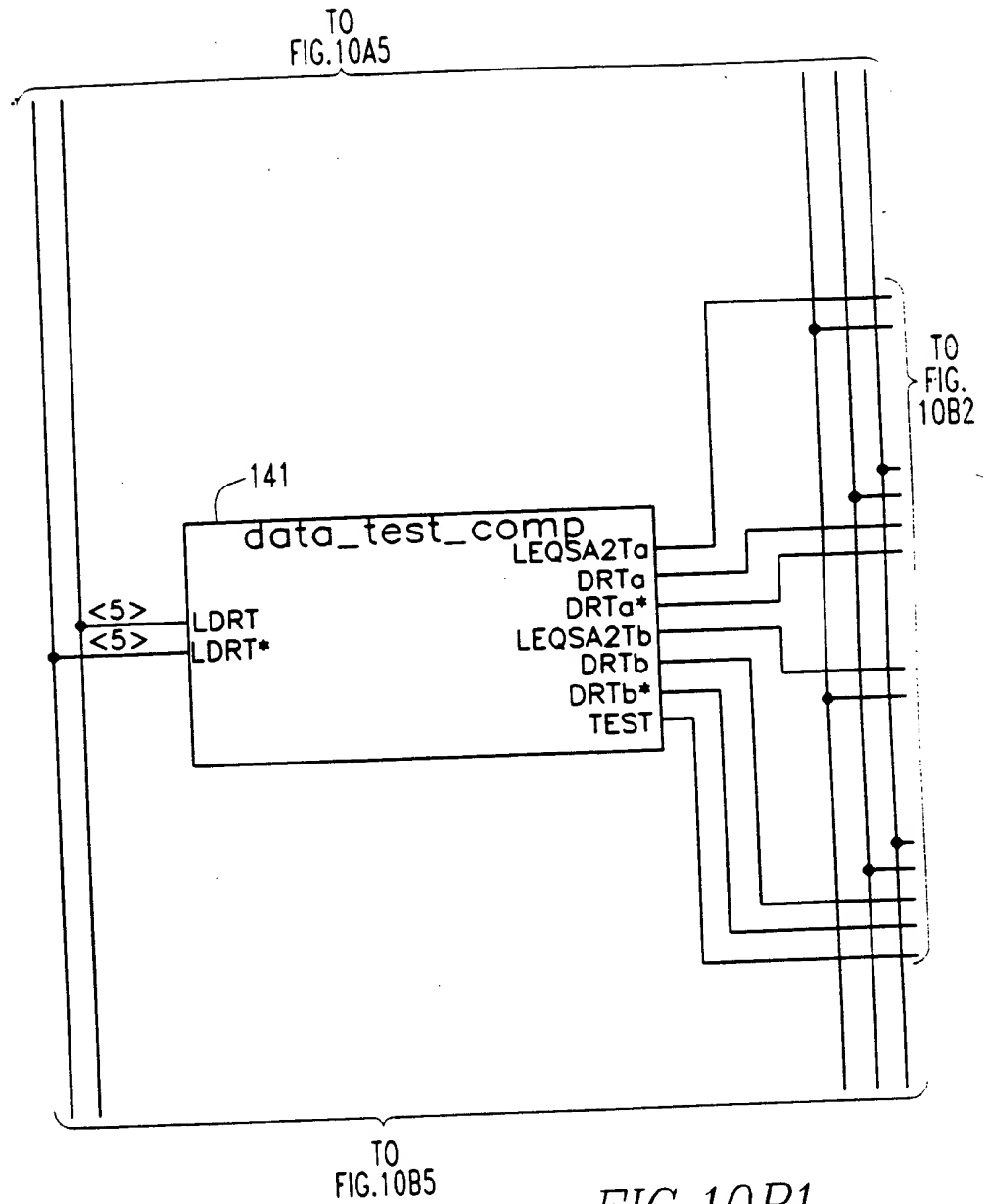


FIG. 10B1

4350 000000

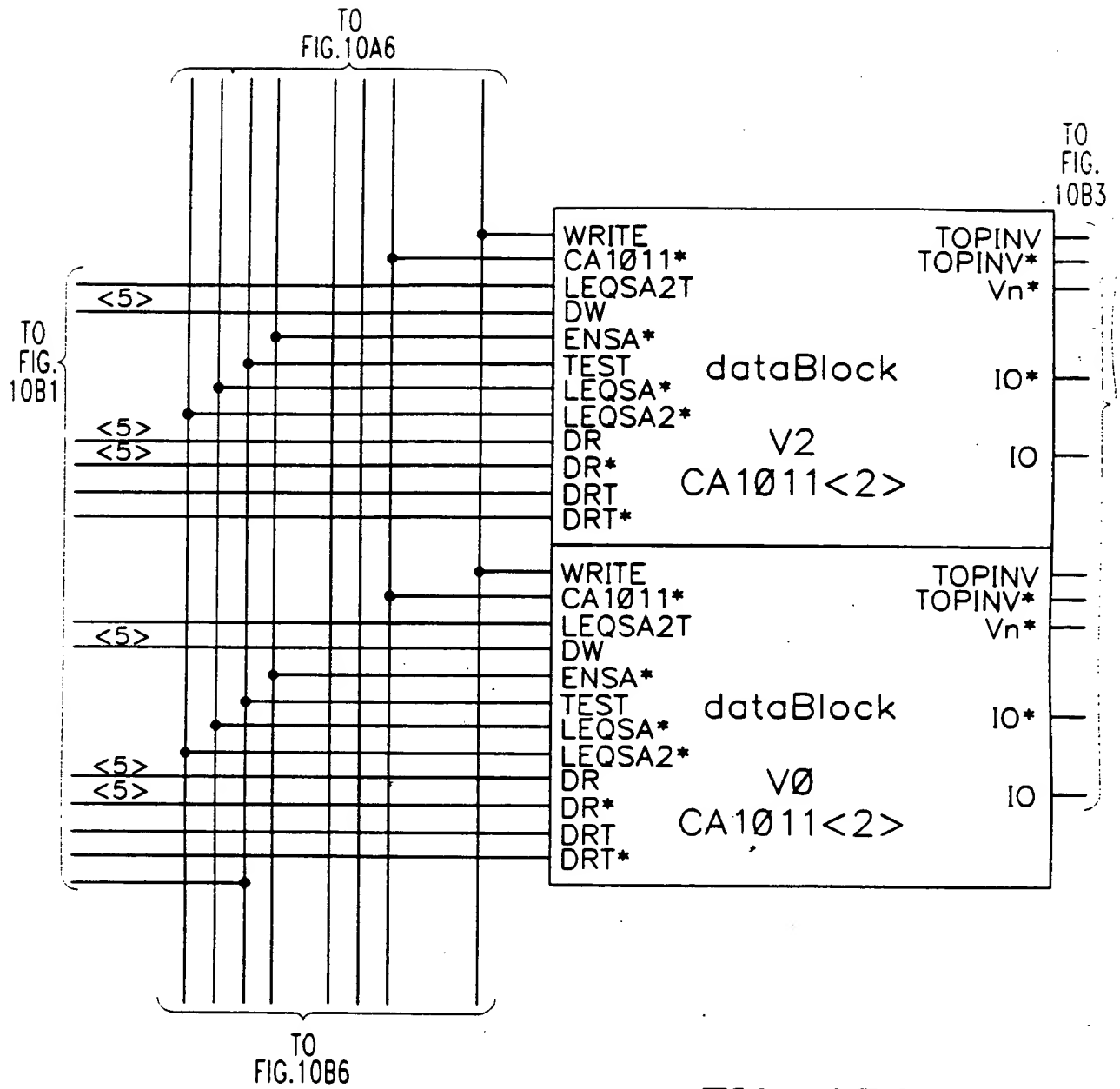


FIG. 10B2

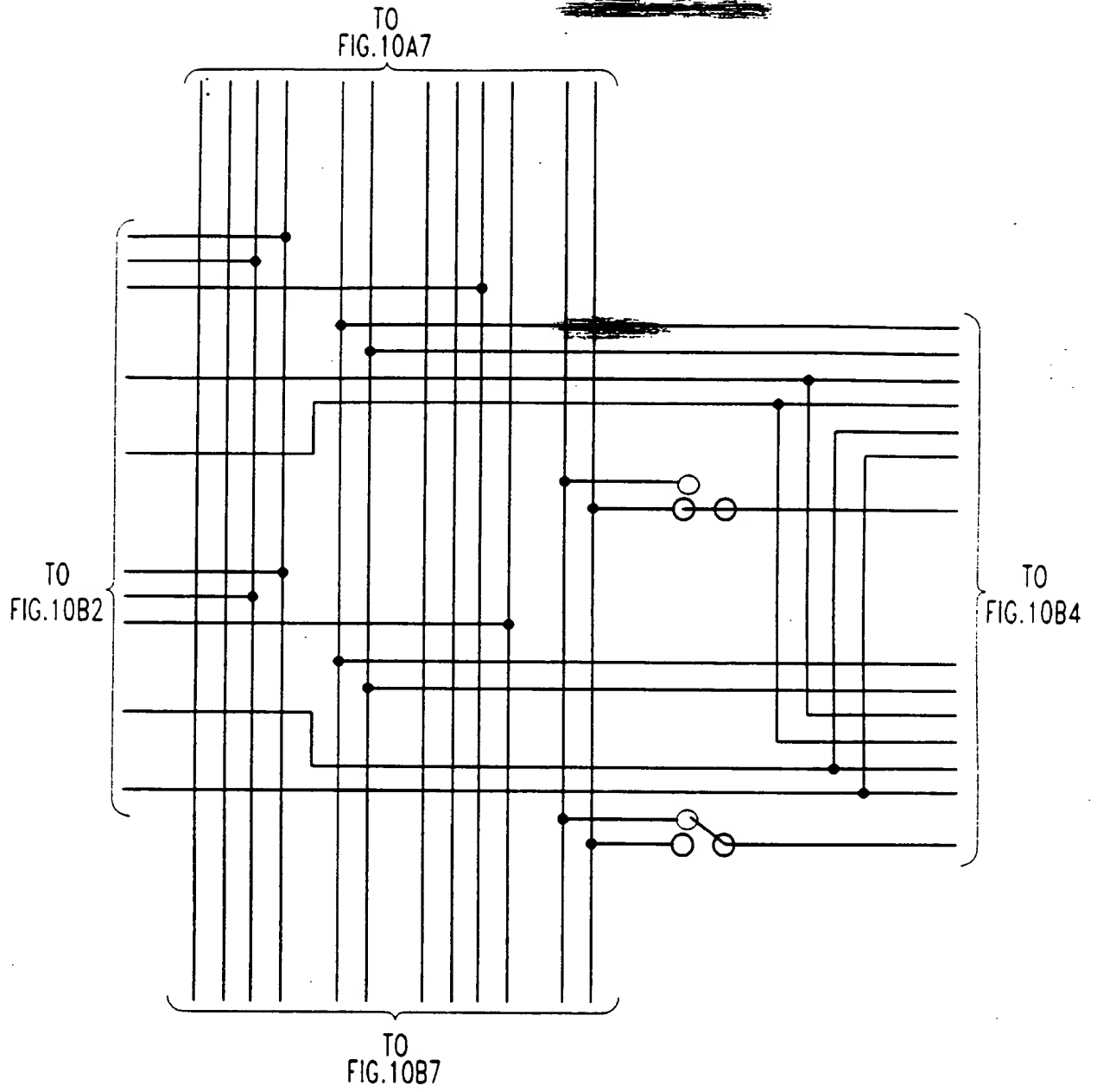


FIG. 10B3

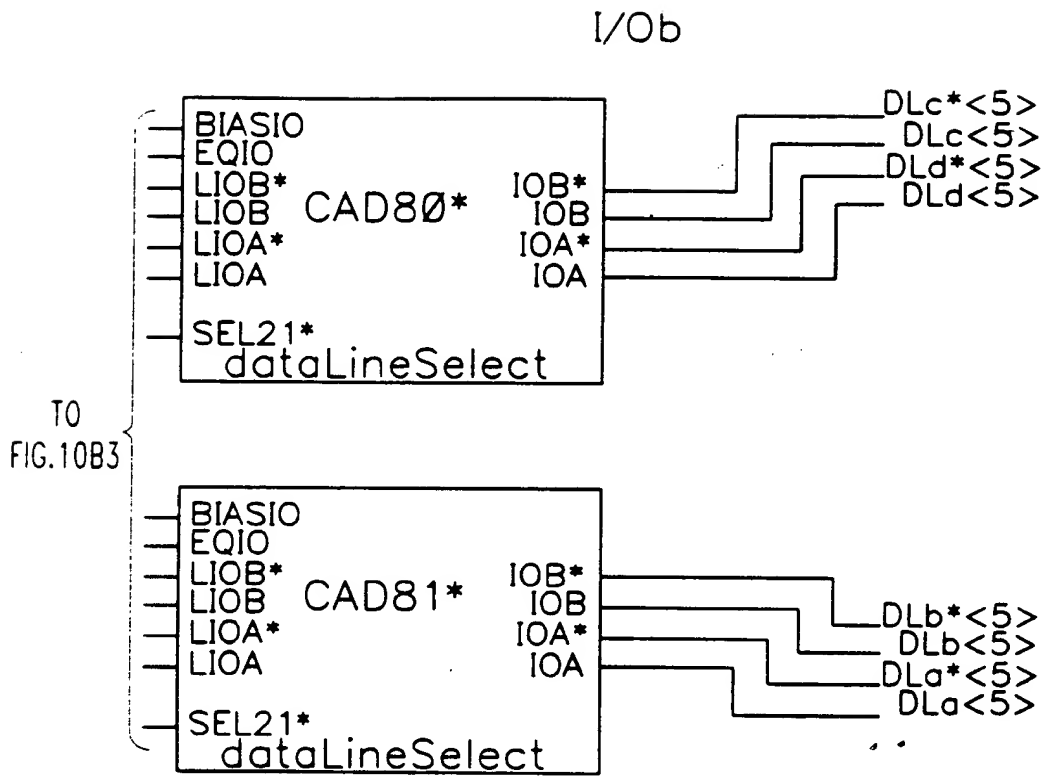


FIG.10B4

FIG. 10B5

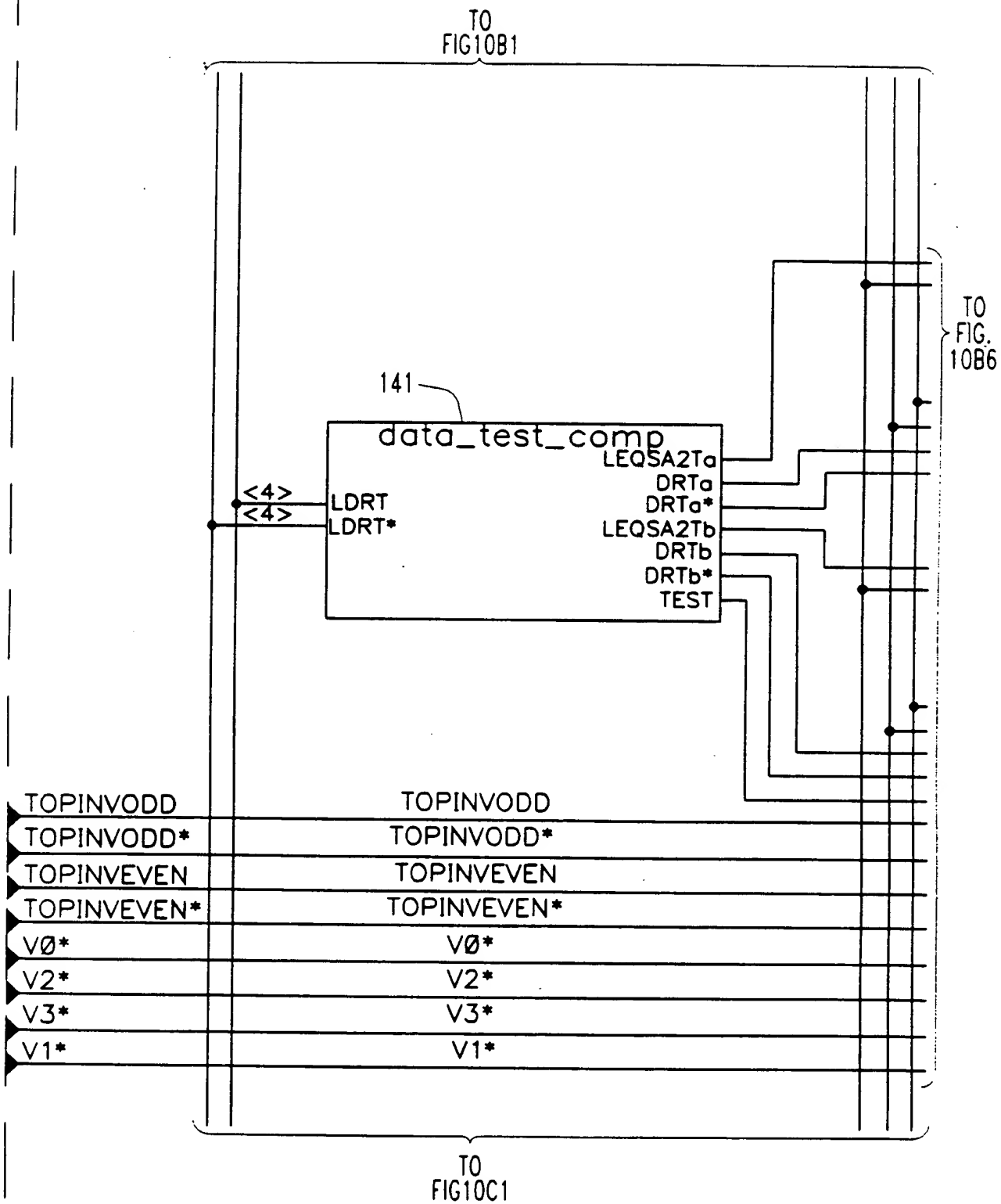


FIG. 10B6

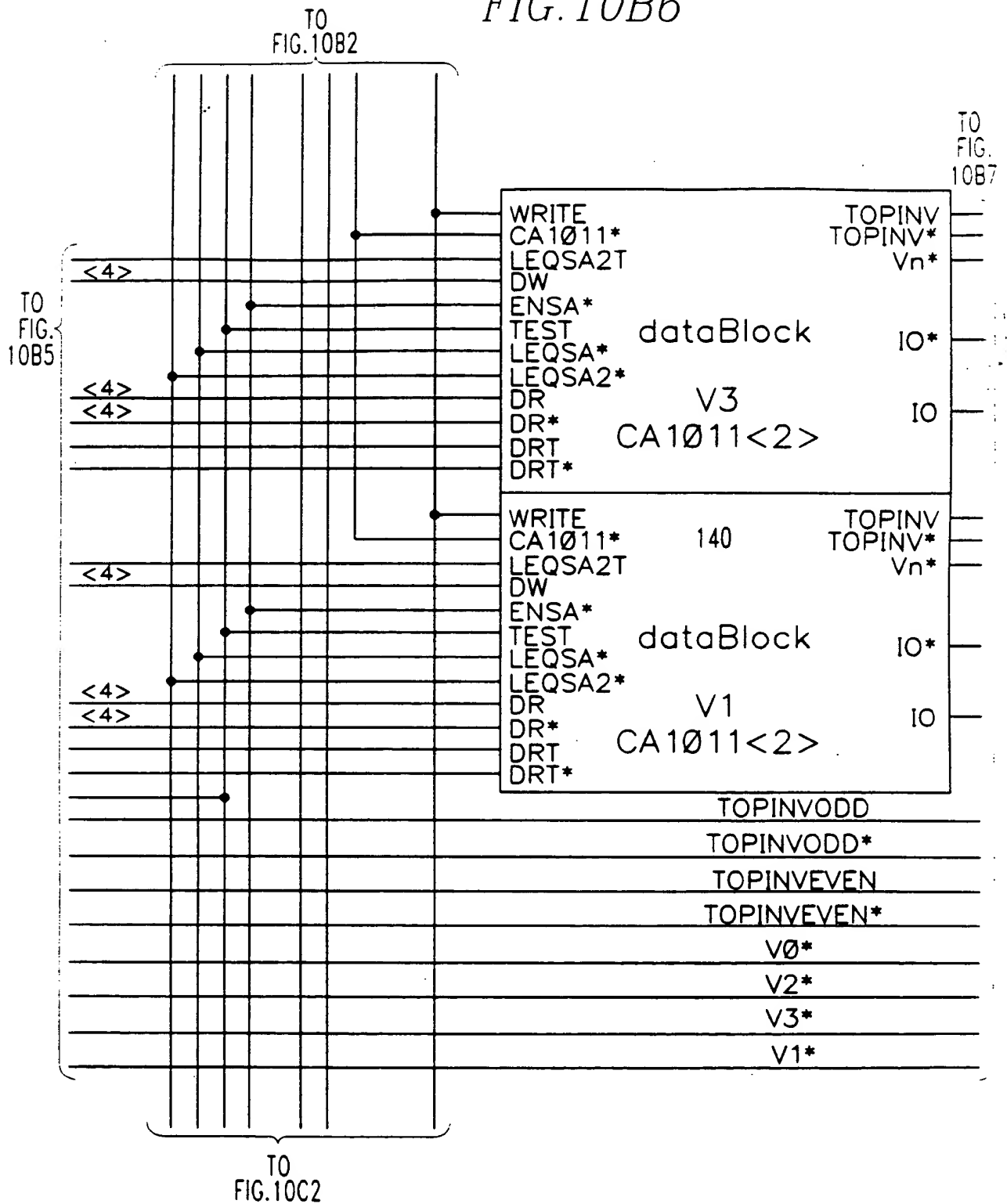


FIG. 10B7

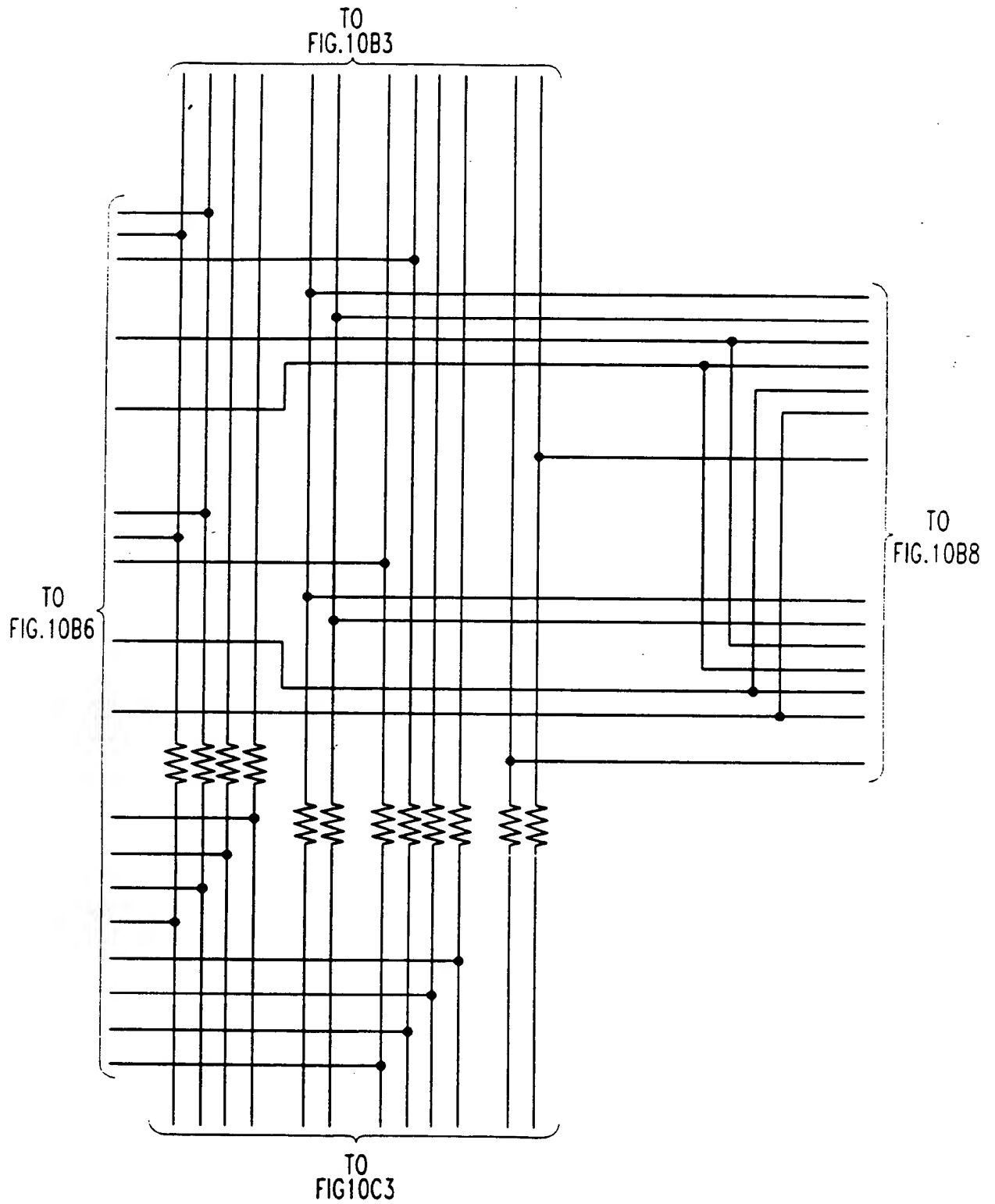
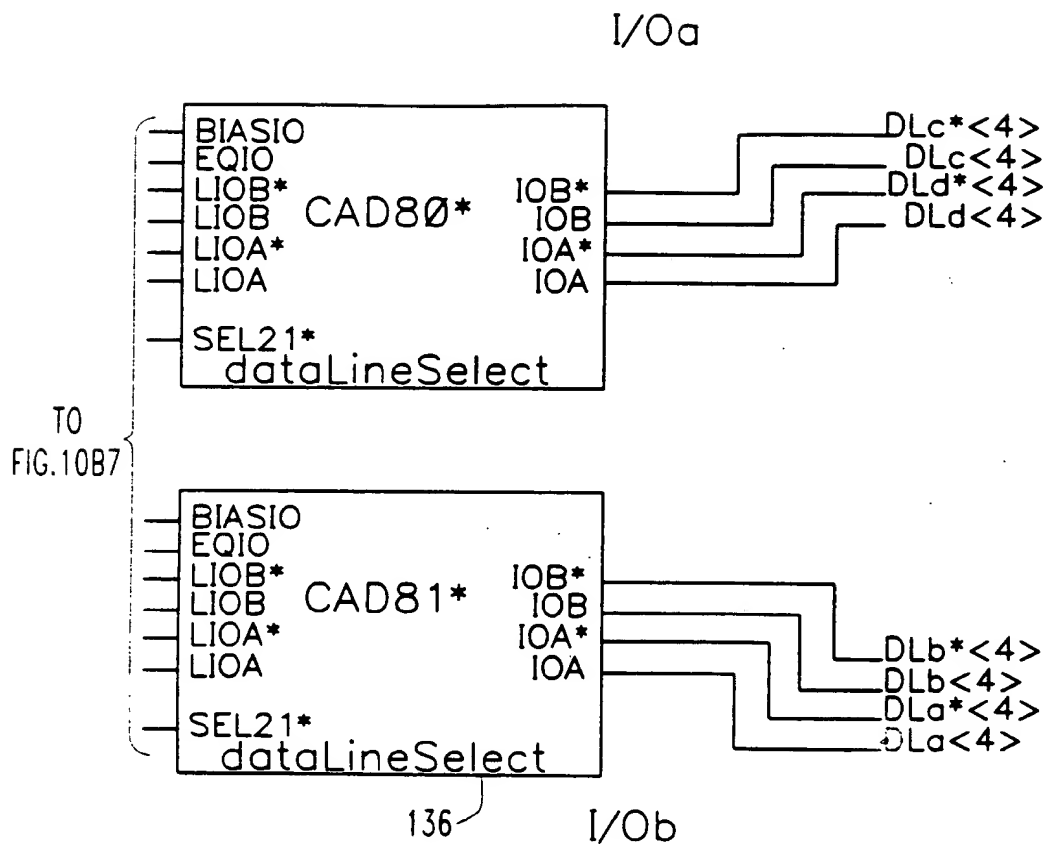


FIG. 10B8



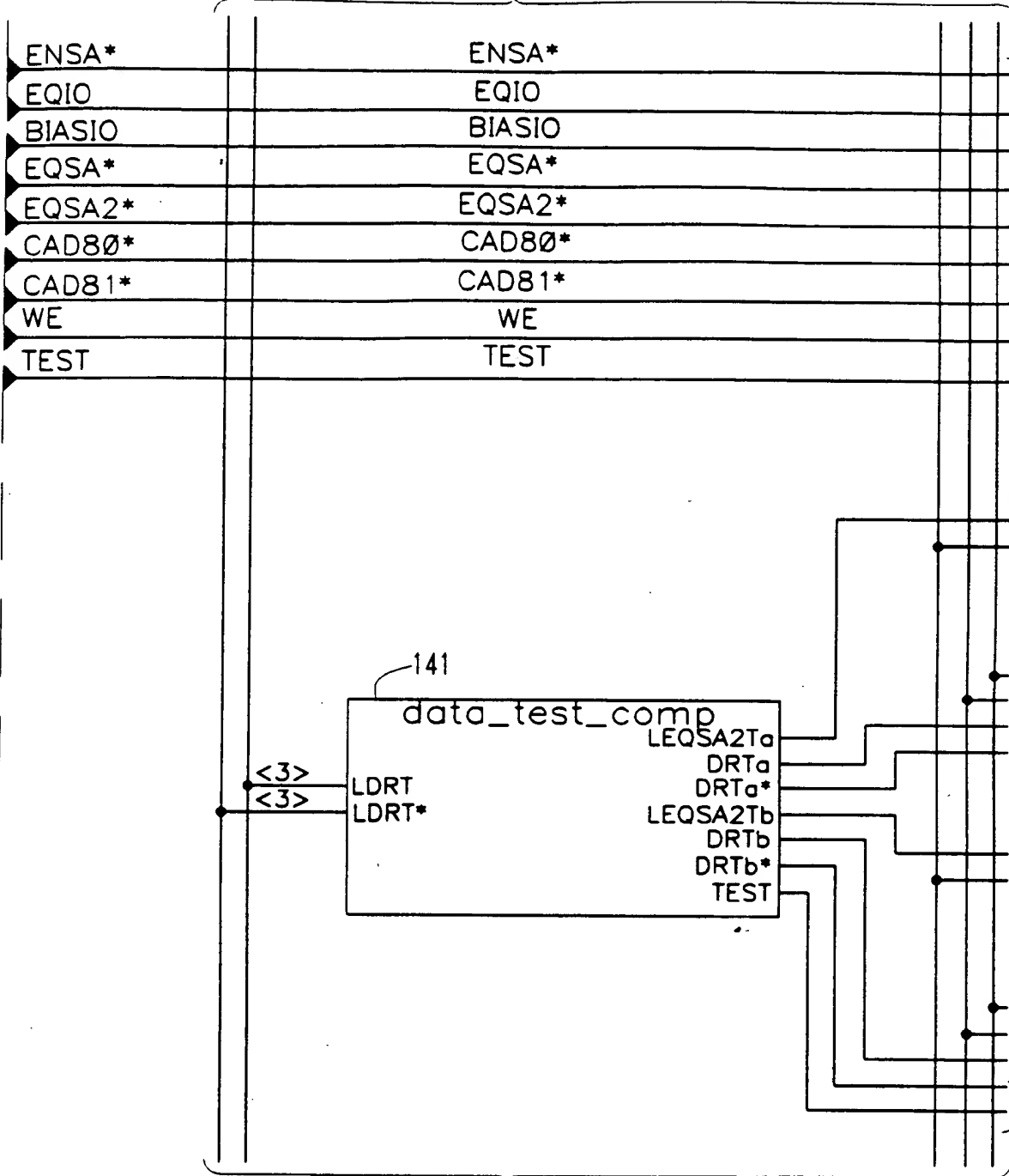
TO
FIG.10B5TO
FIG.
10C2TO
FIG.10C5

FIG. 10C1

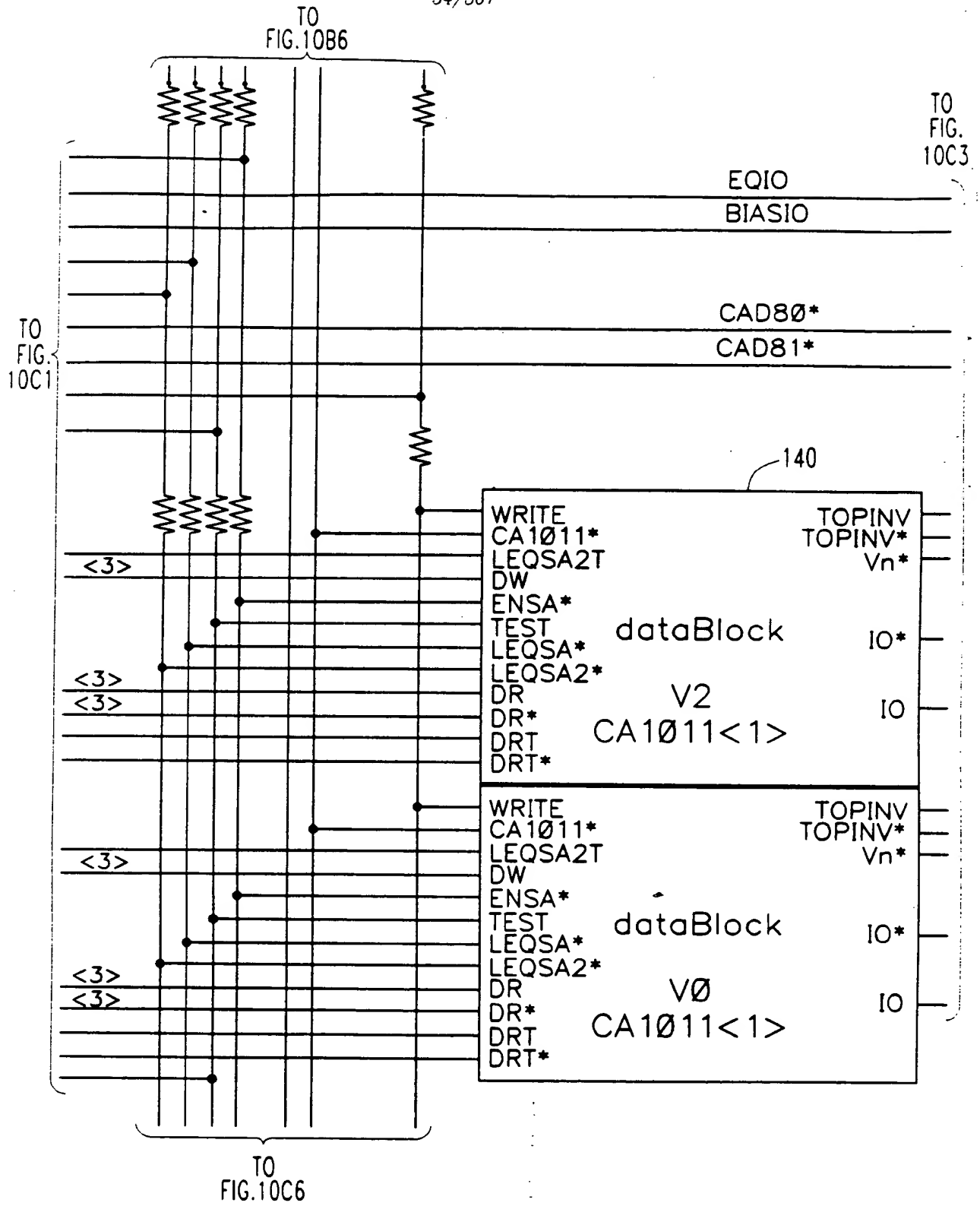


FIG. 10C2

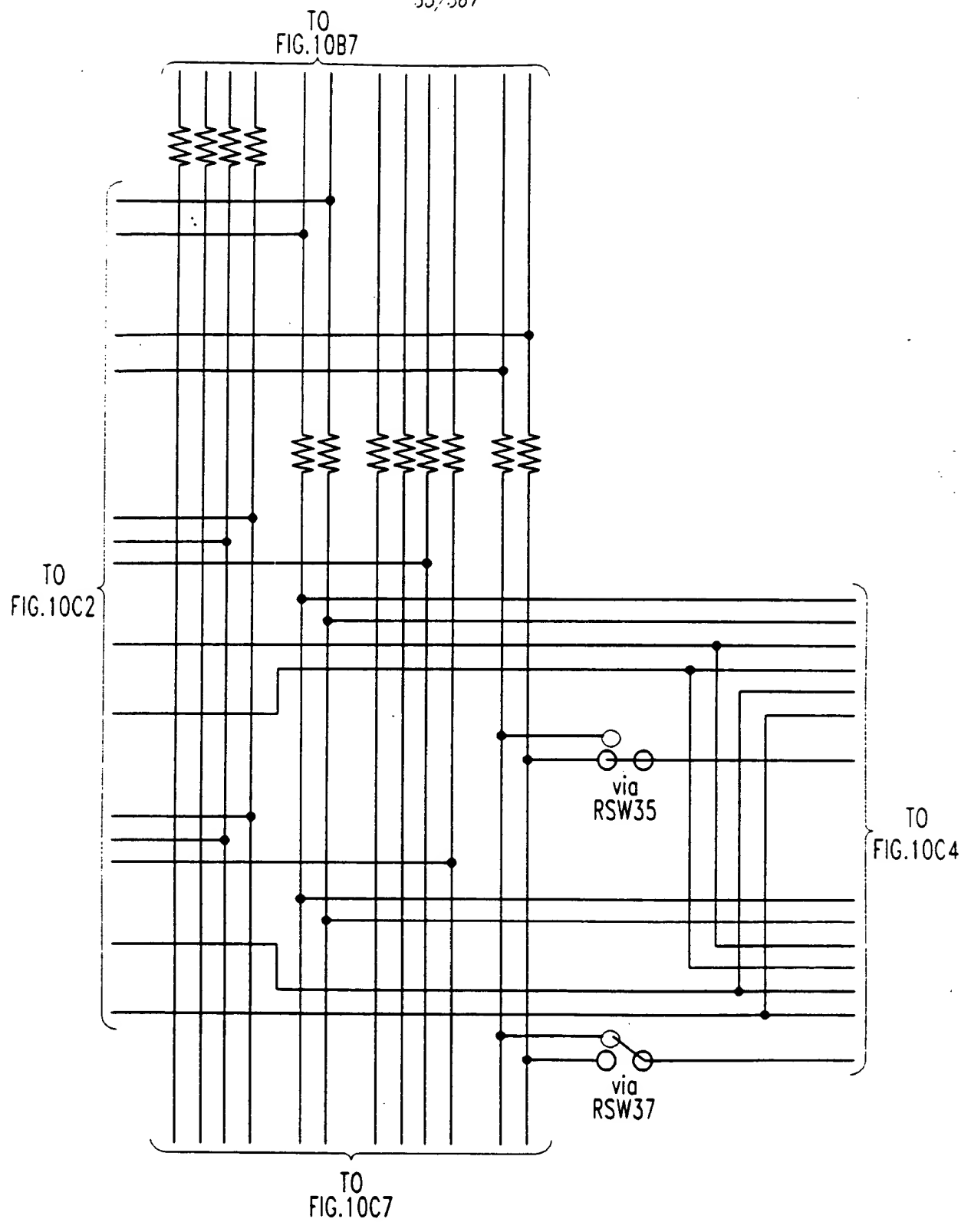
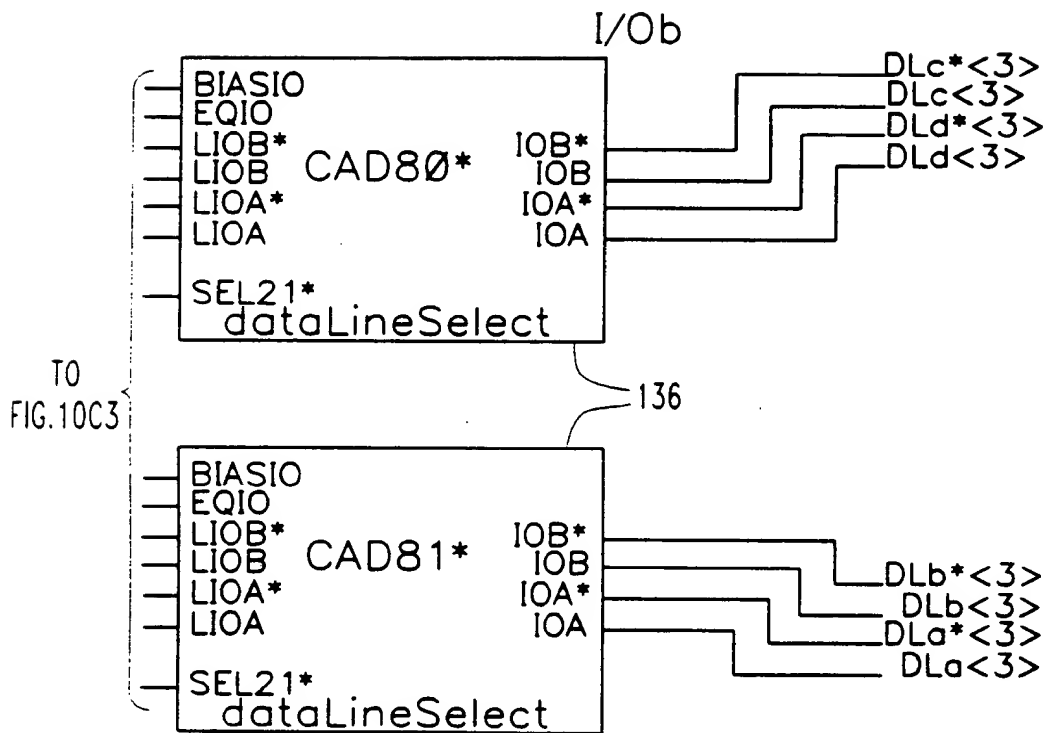


FIG. 10C3

FIG. 10C4



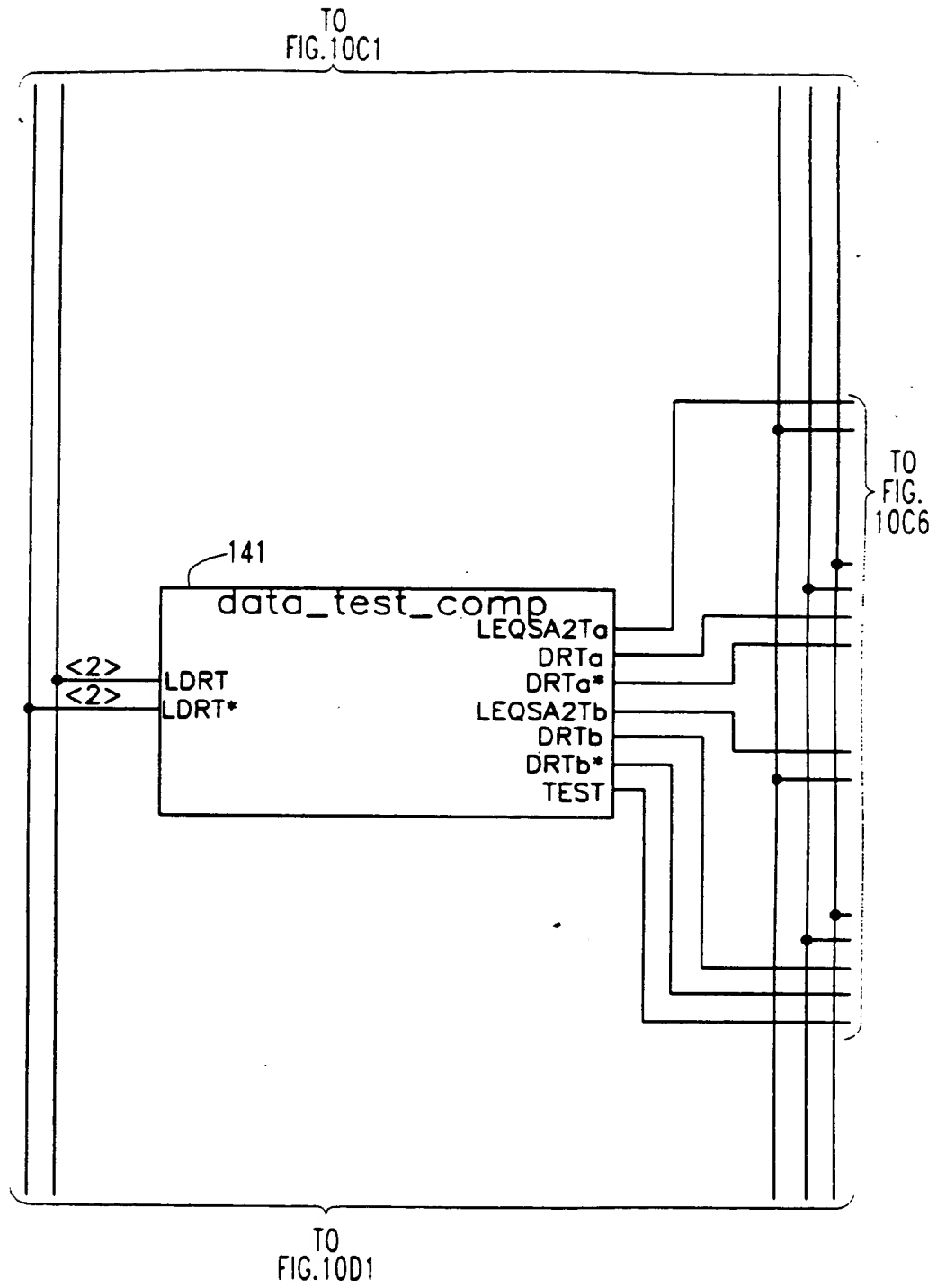


FIG. 10C5

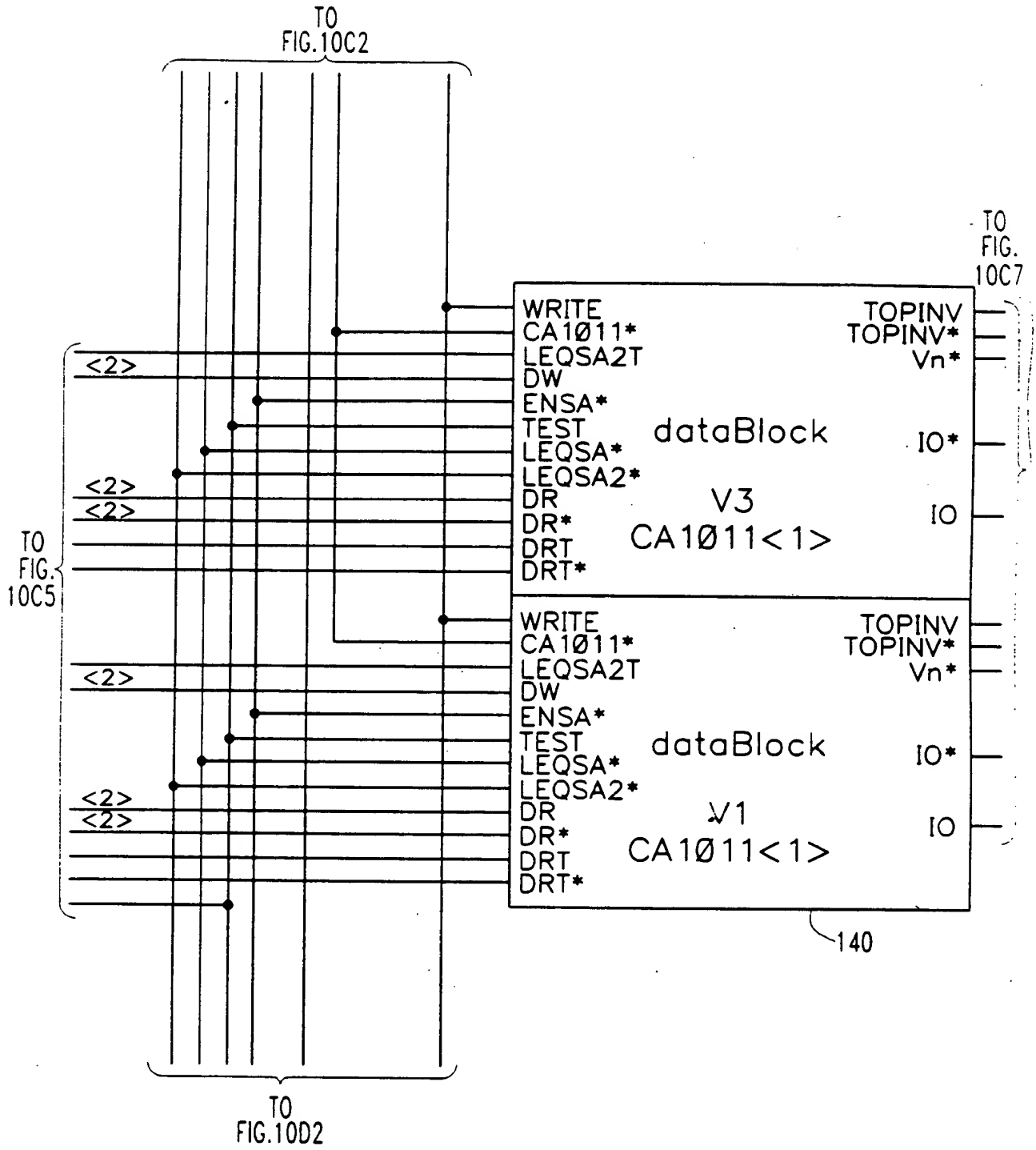


FIG. 10C6

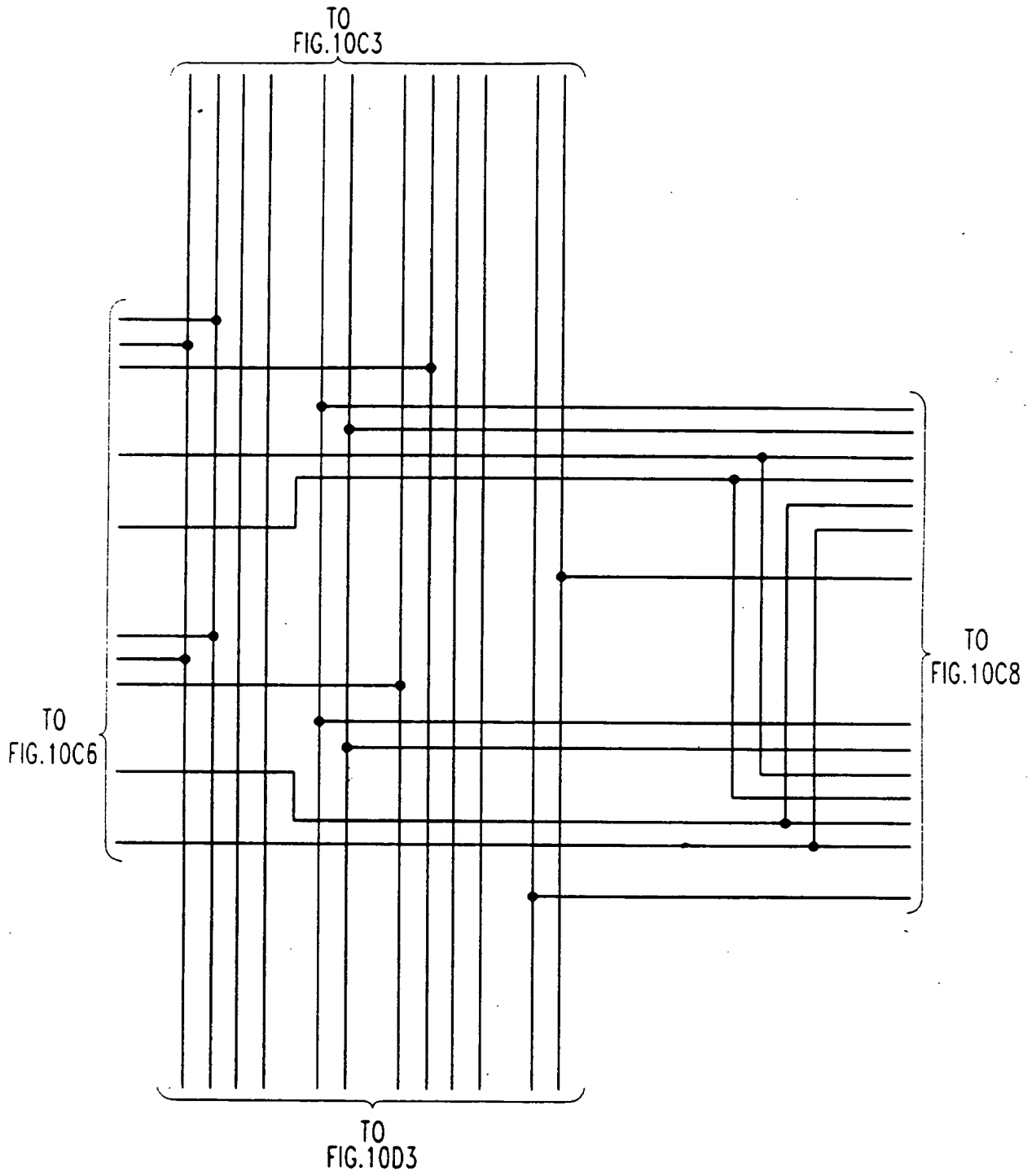


FIG. 10C7

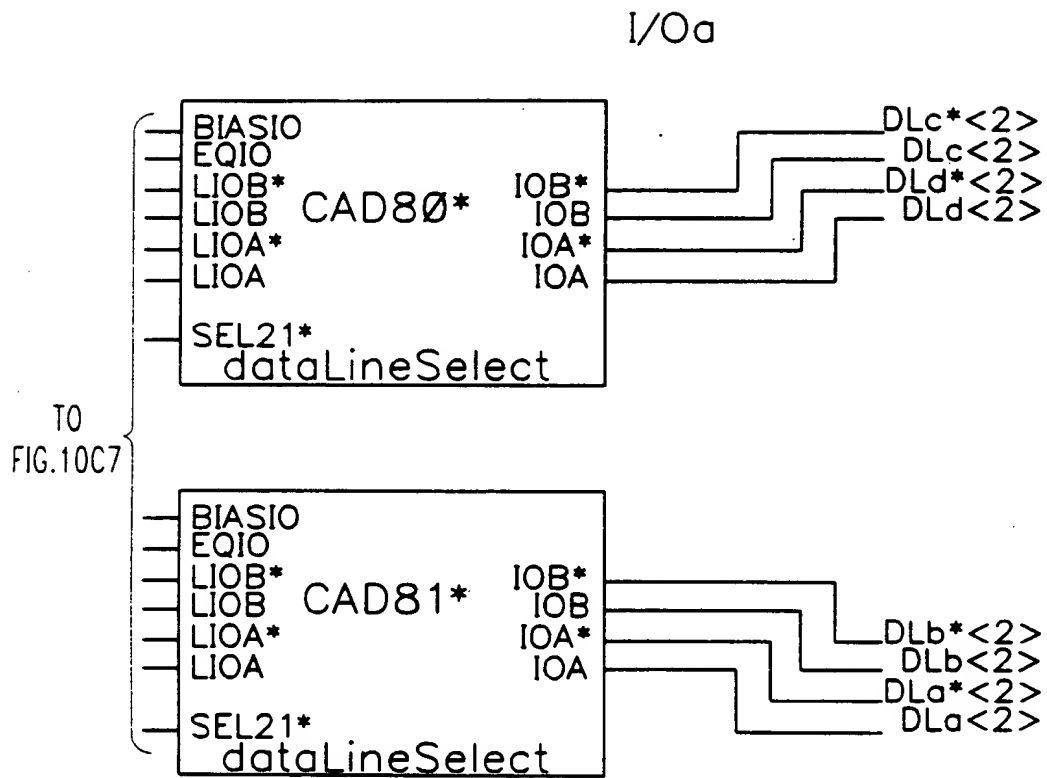


FIG. 10C8

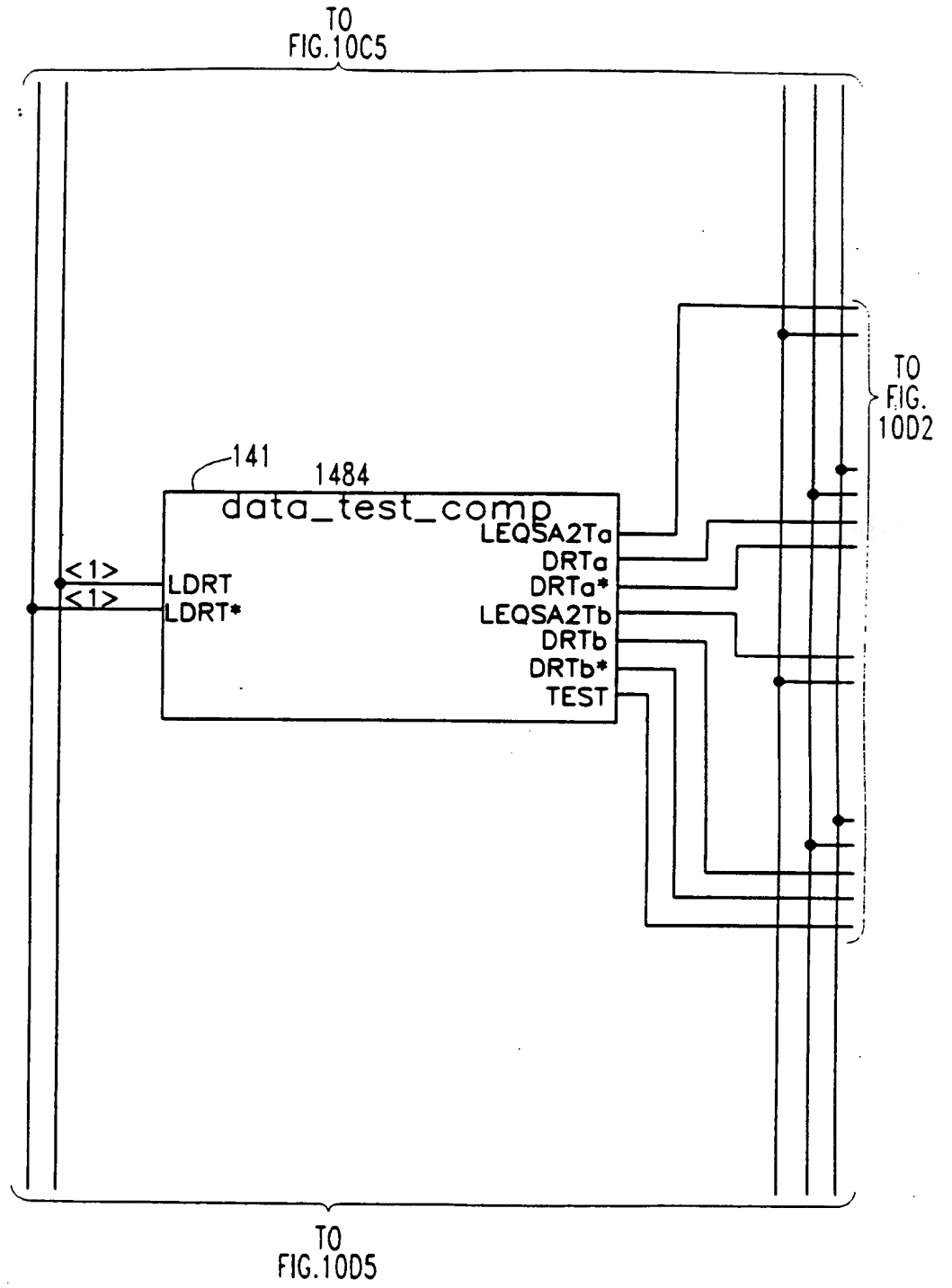


FIG. 10D1

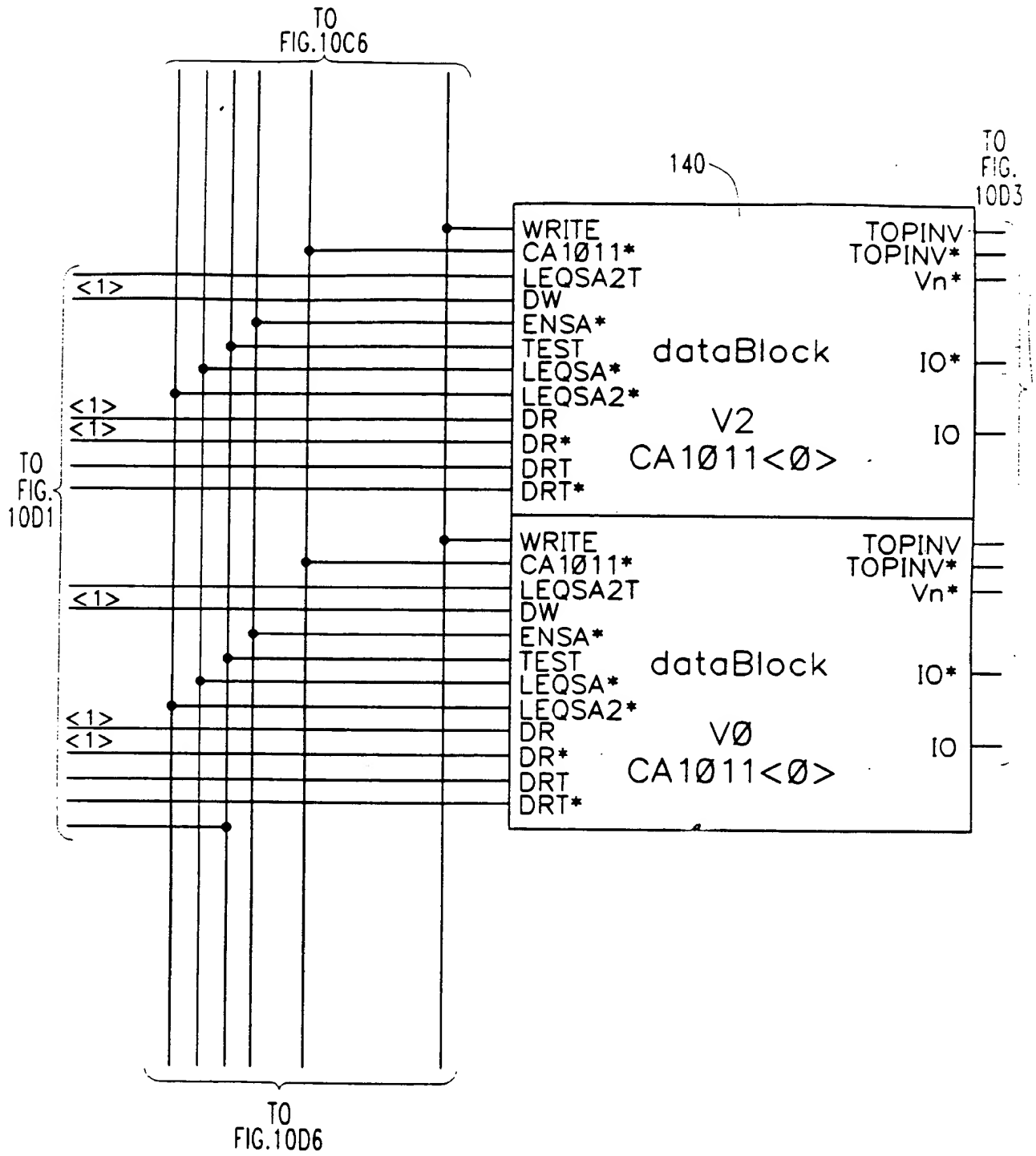
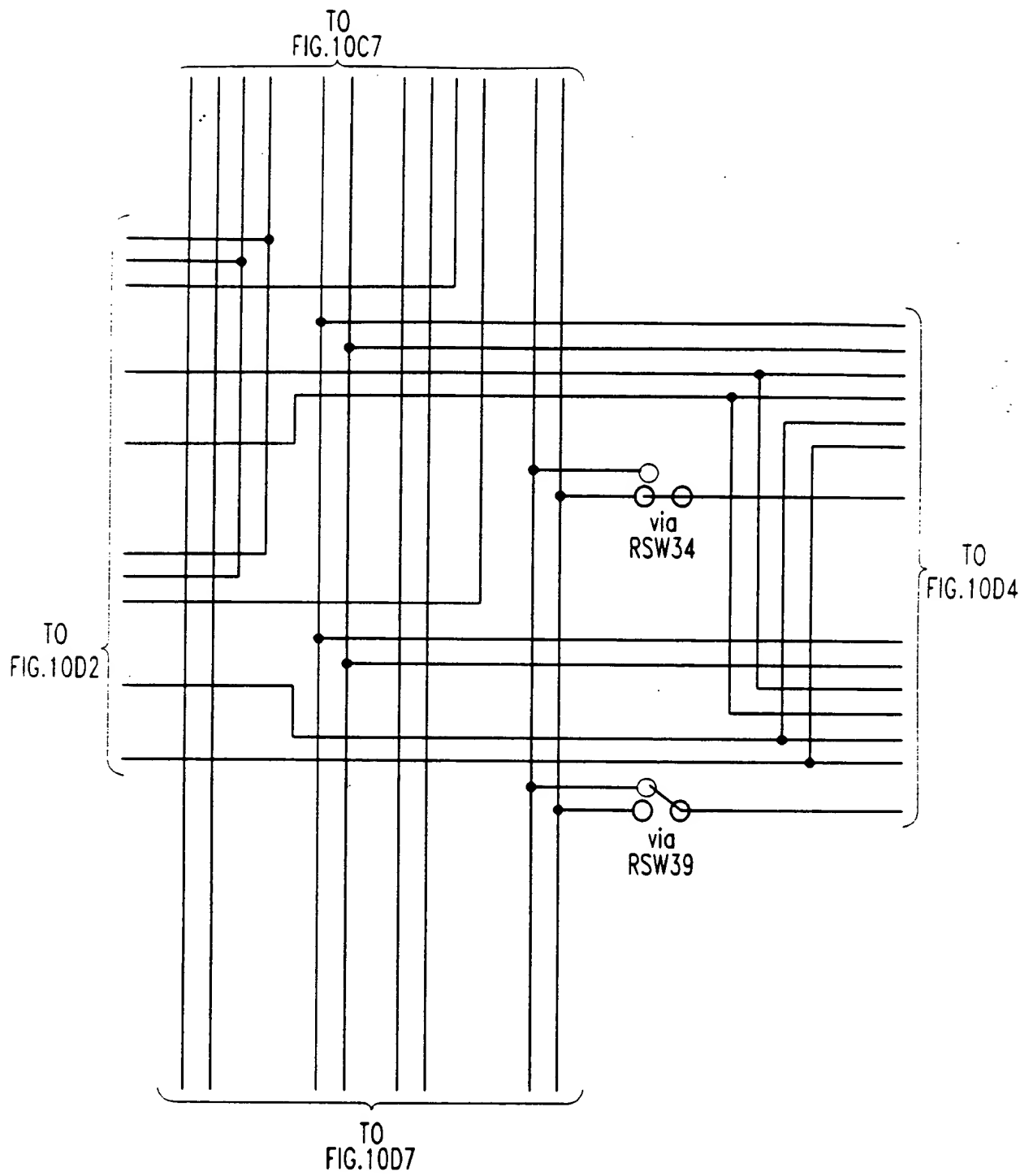


FIG. 10D2

*FIG. 10D3*

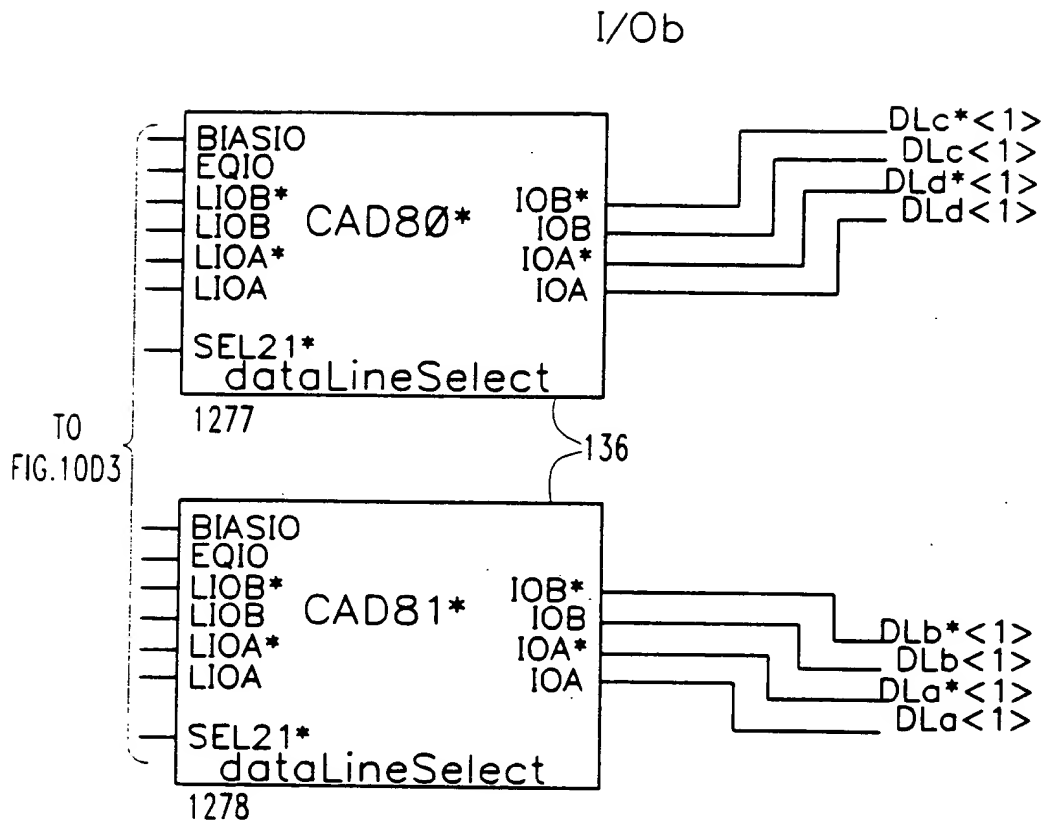


FIG. 10D4

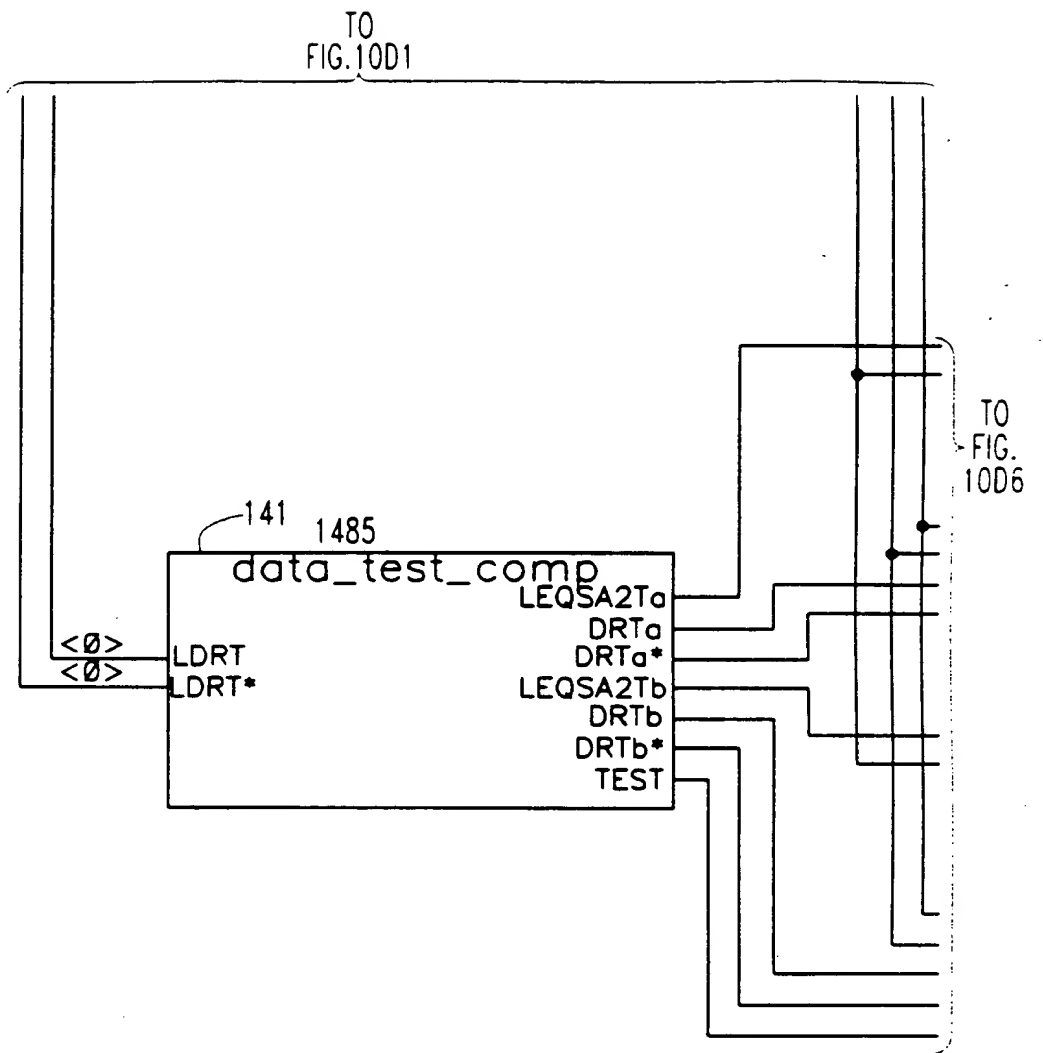


FIG. 10D5

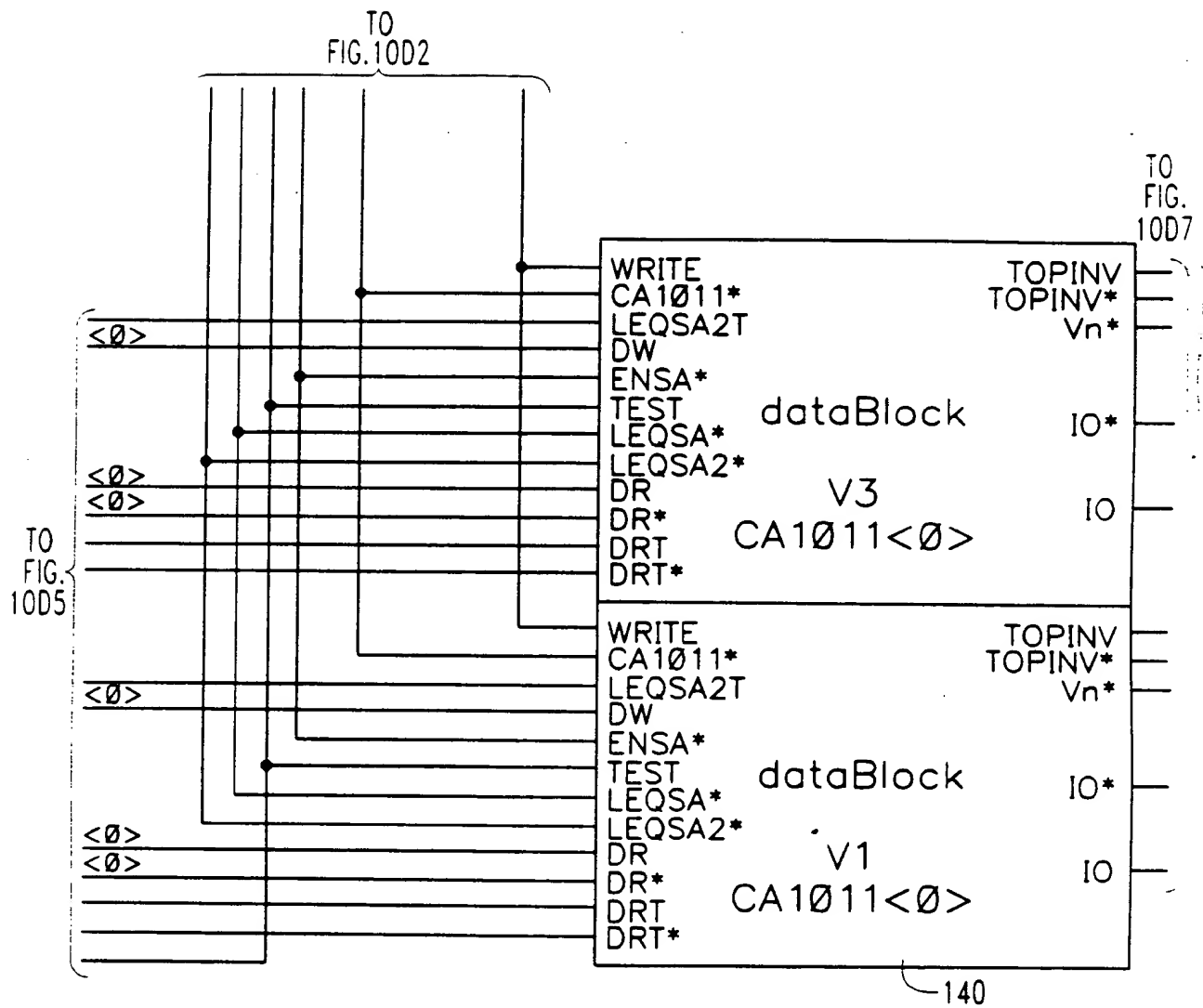


FIG. 10D6

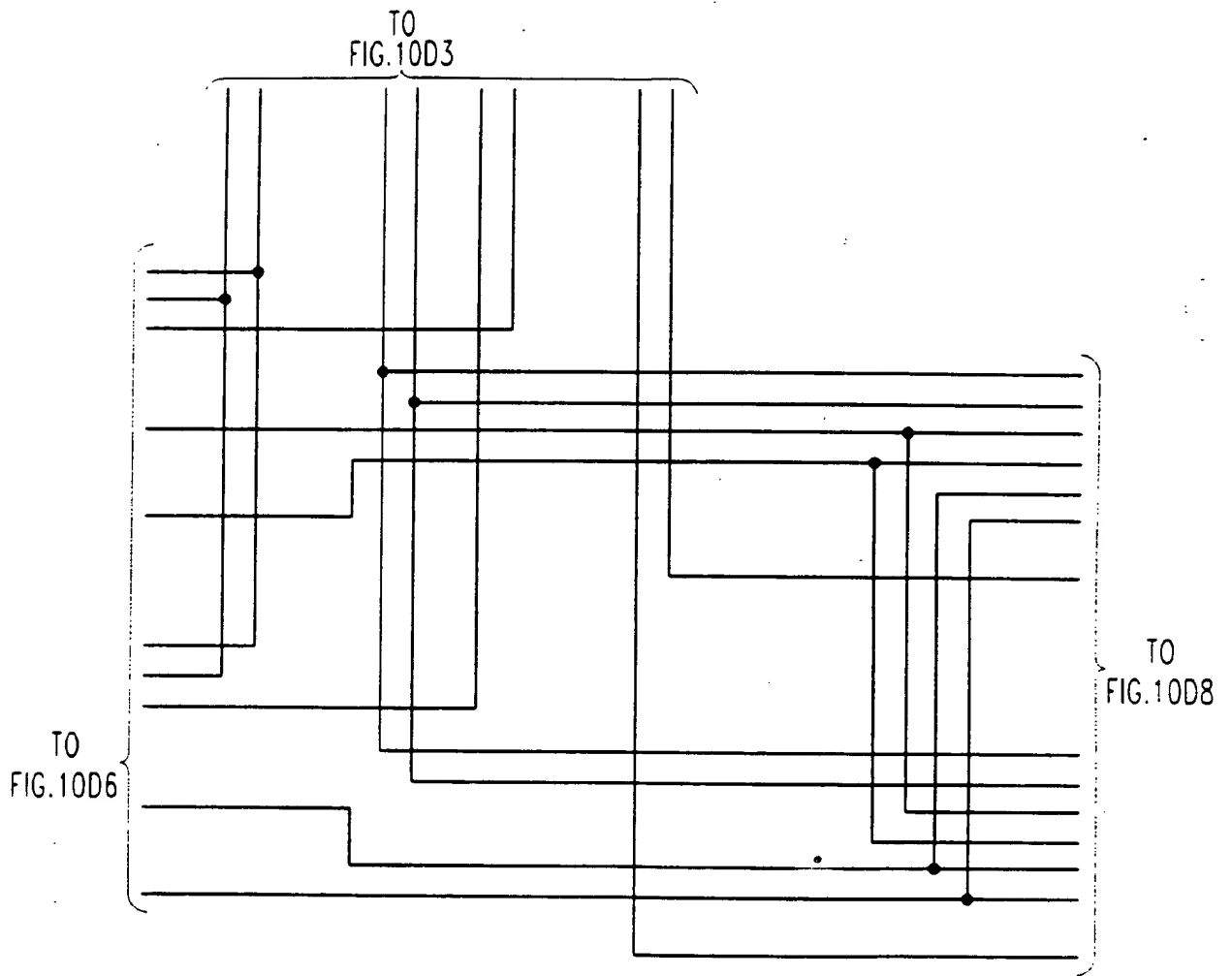


FIG. 10D7

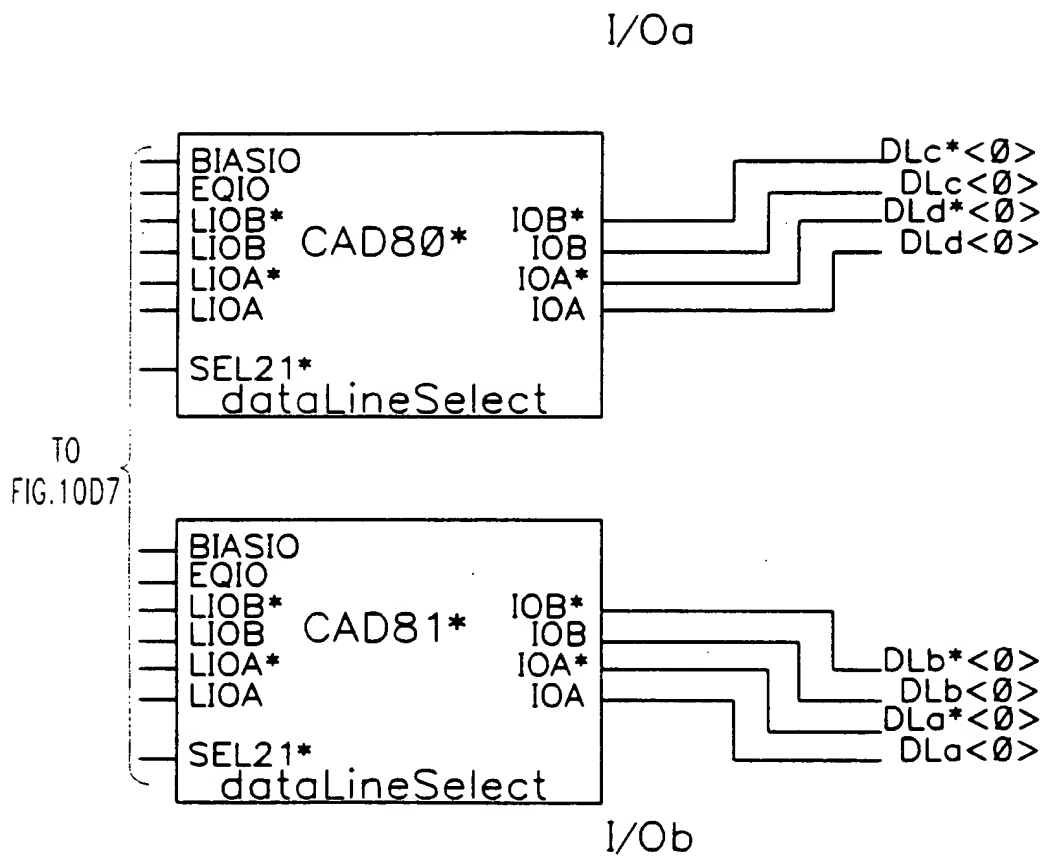
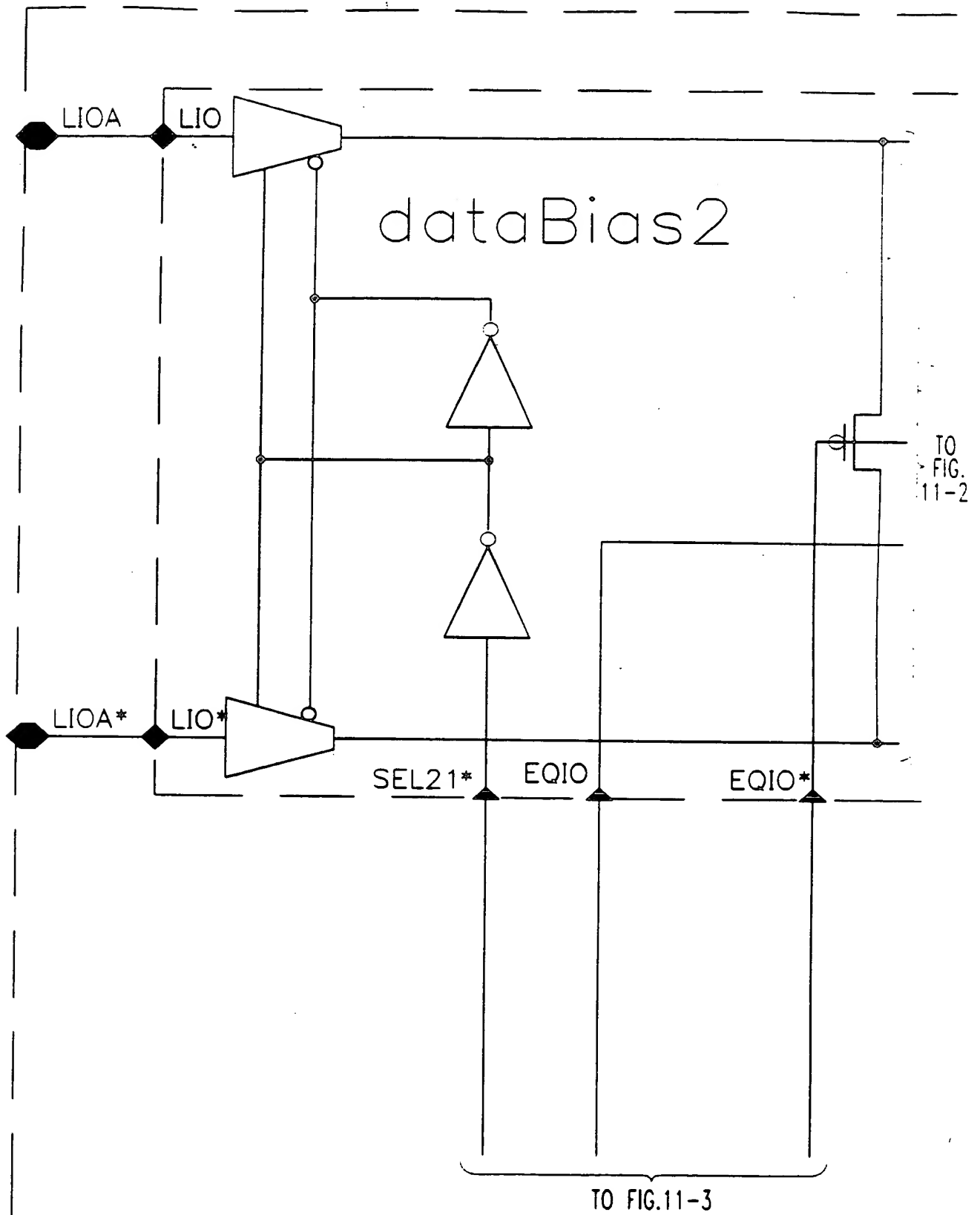
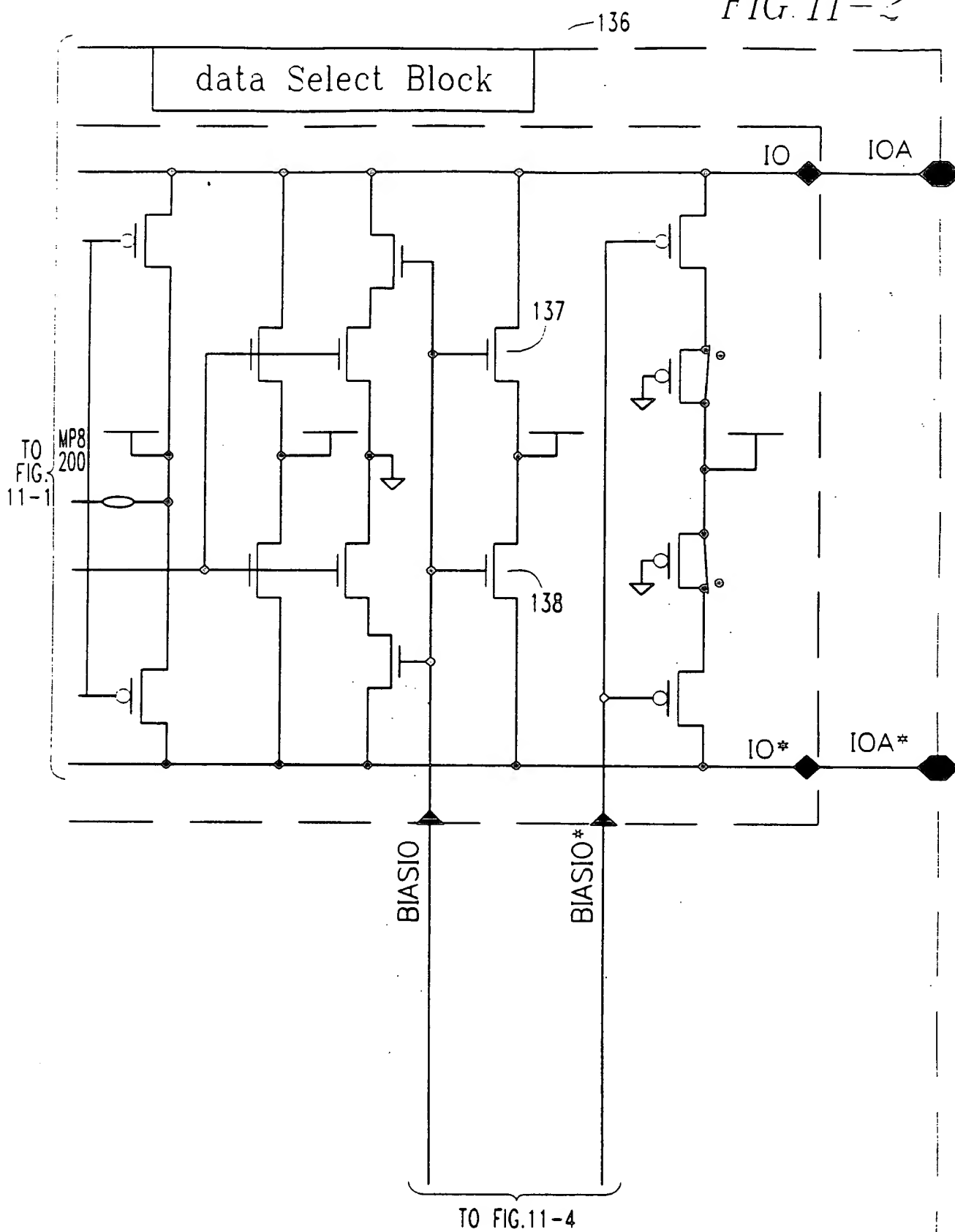


FIG.10D8





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TO FIG. 11-2

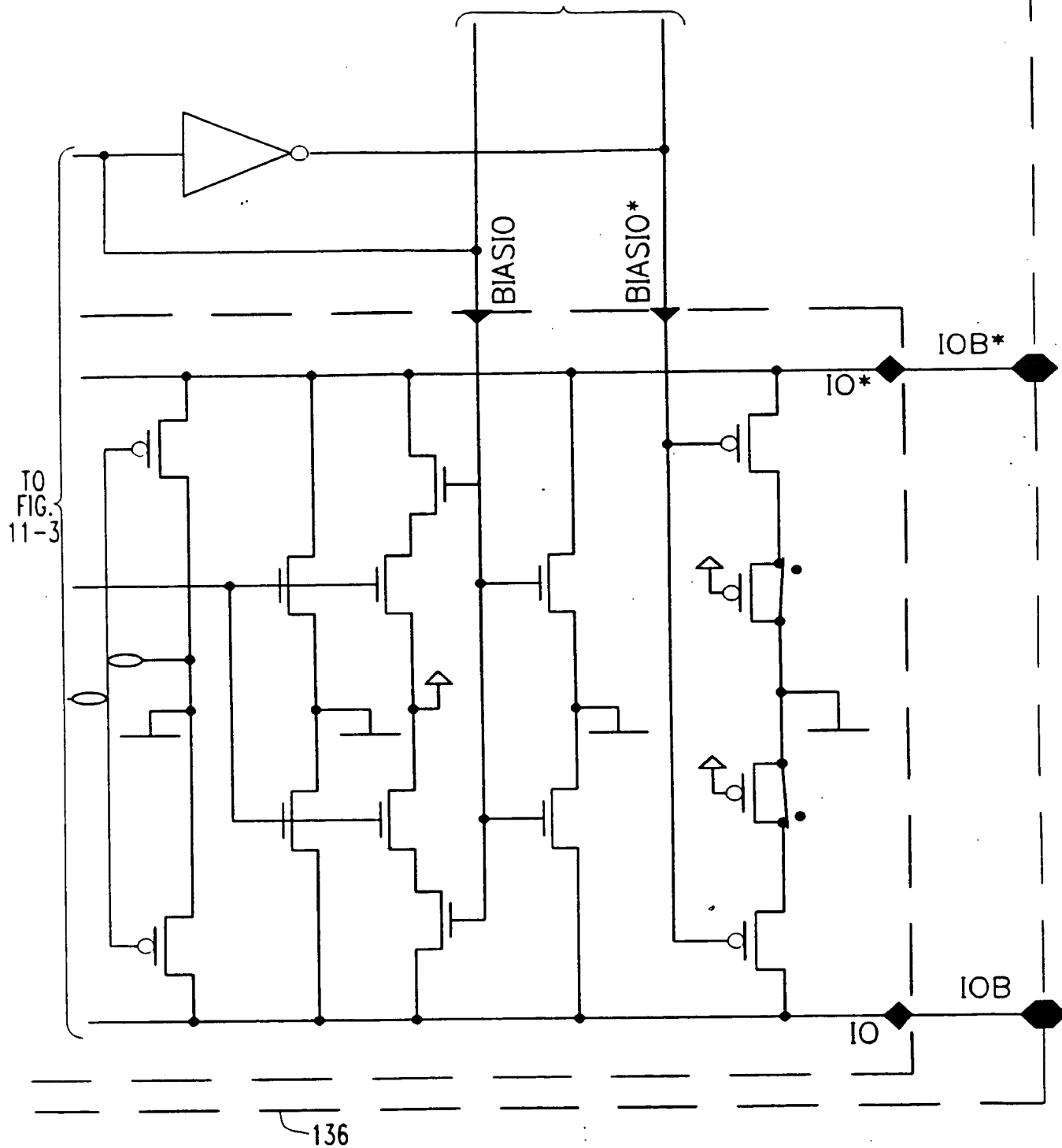


FIG. 11-4

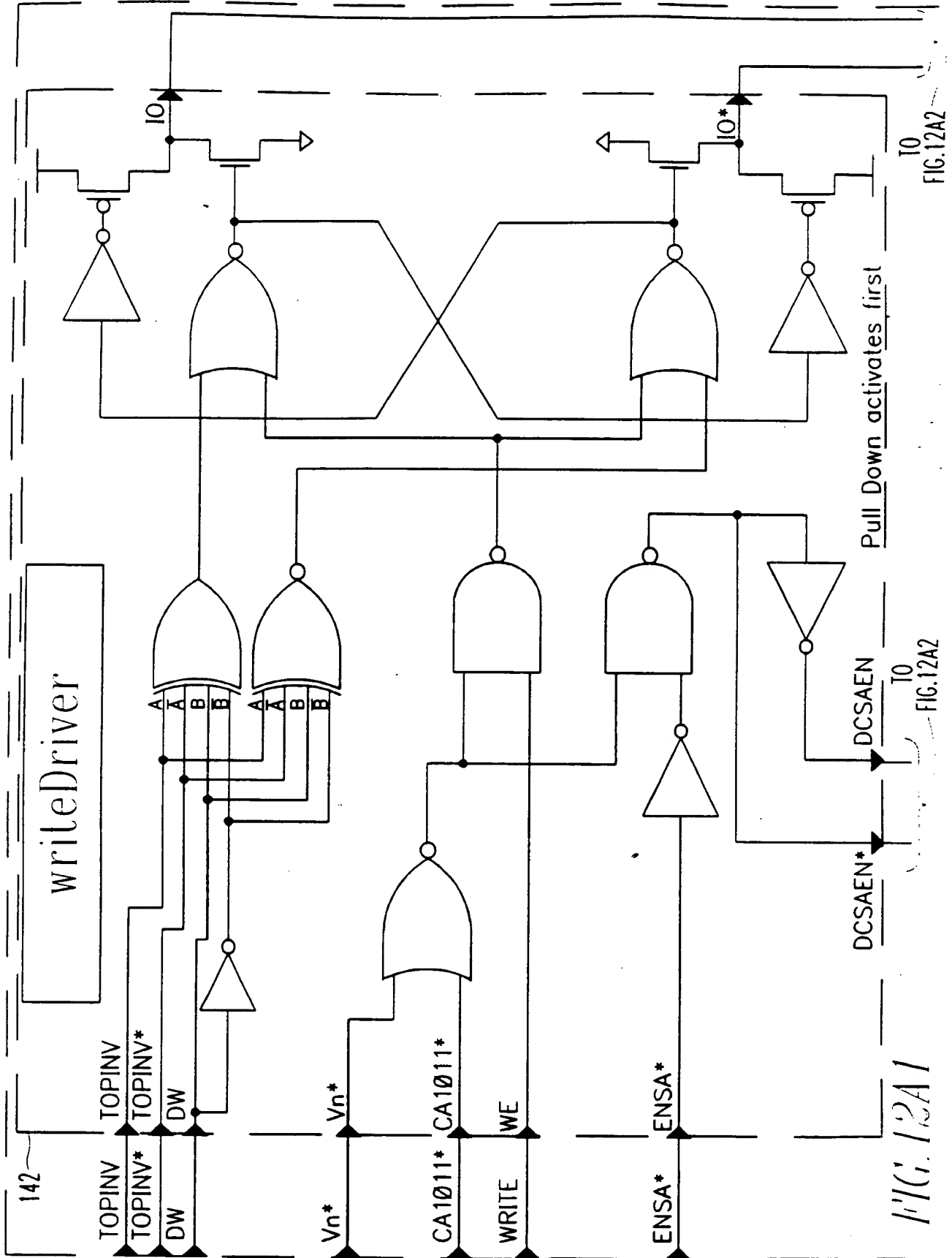


FIG. 12A1

FIG. 12A2

Pull Down activates first

FIG. 12A2

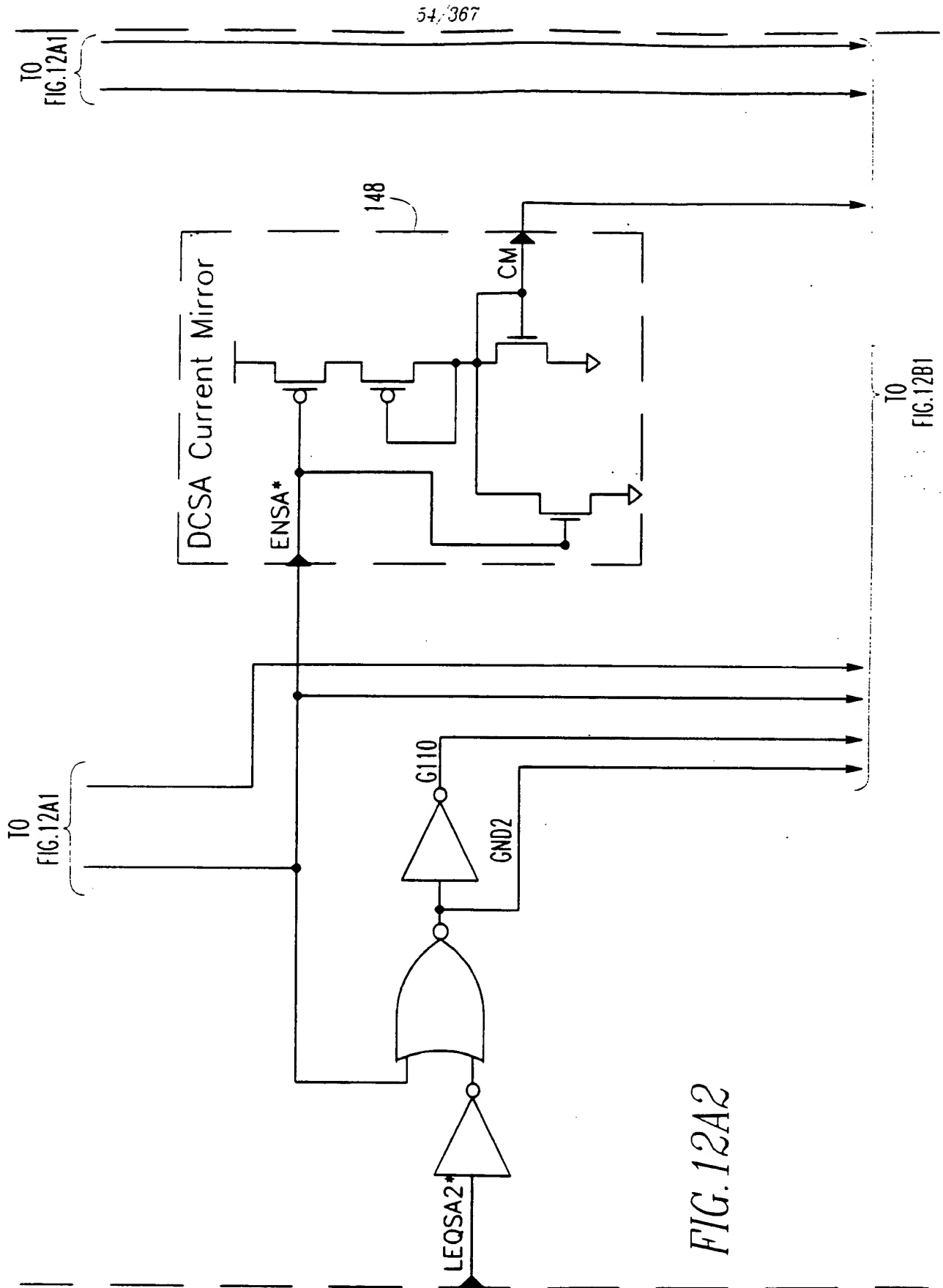
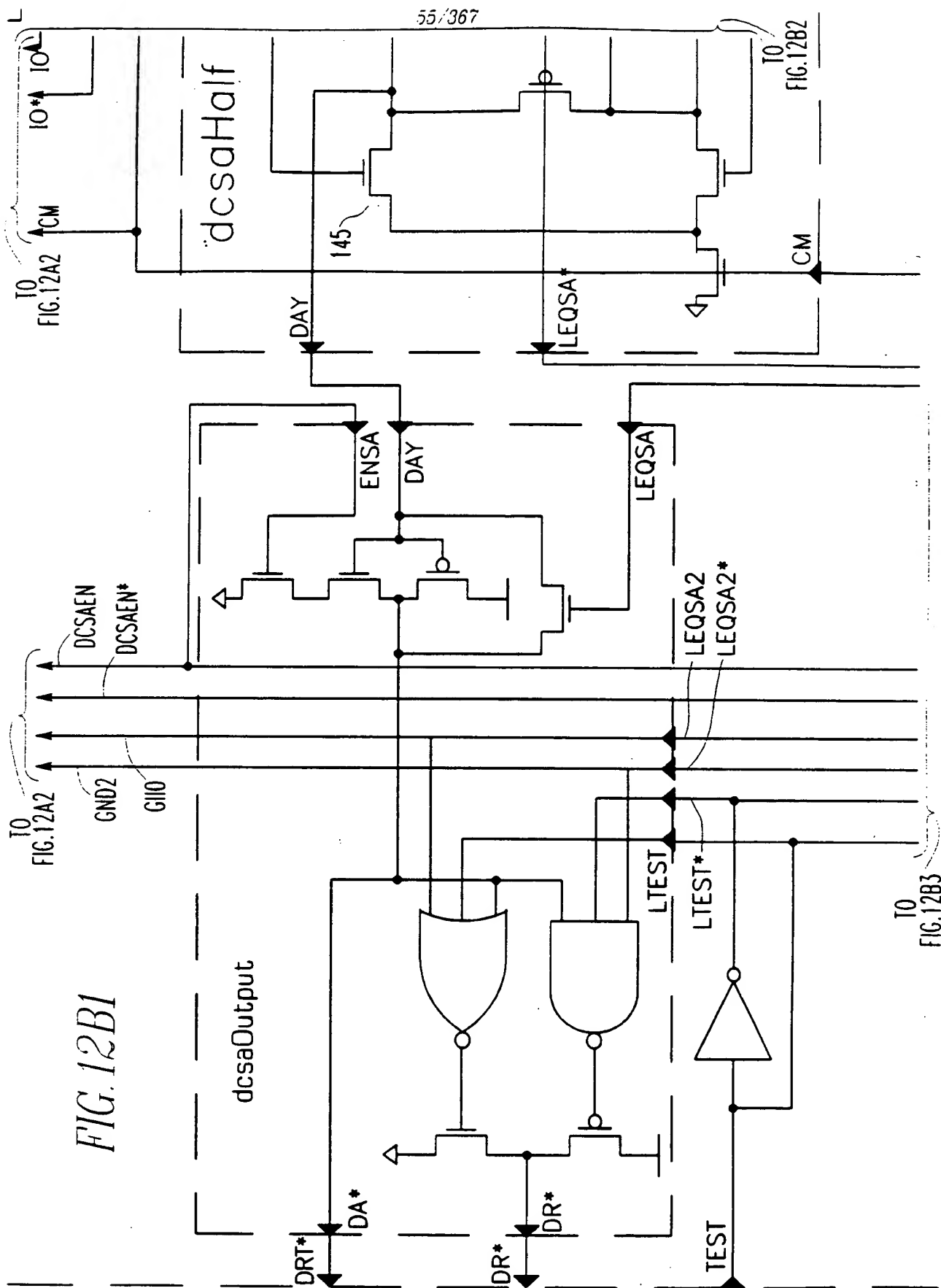


FIG. 12A2



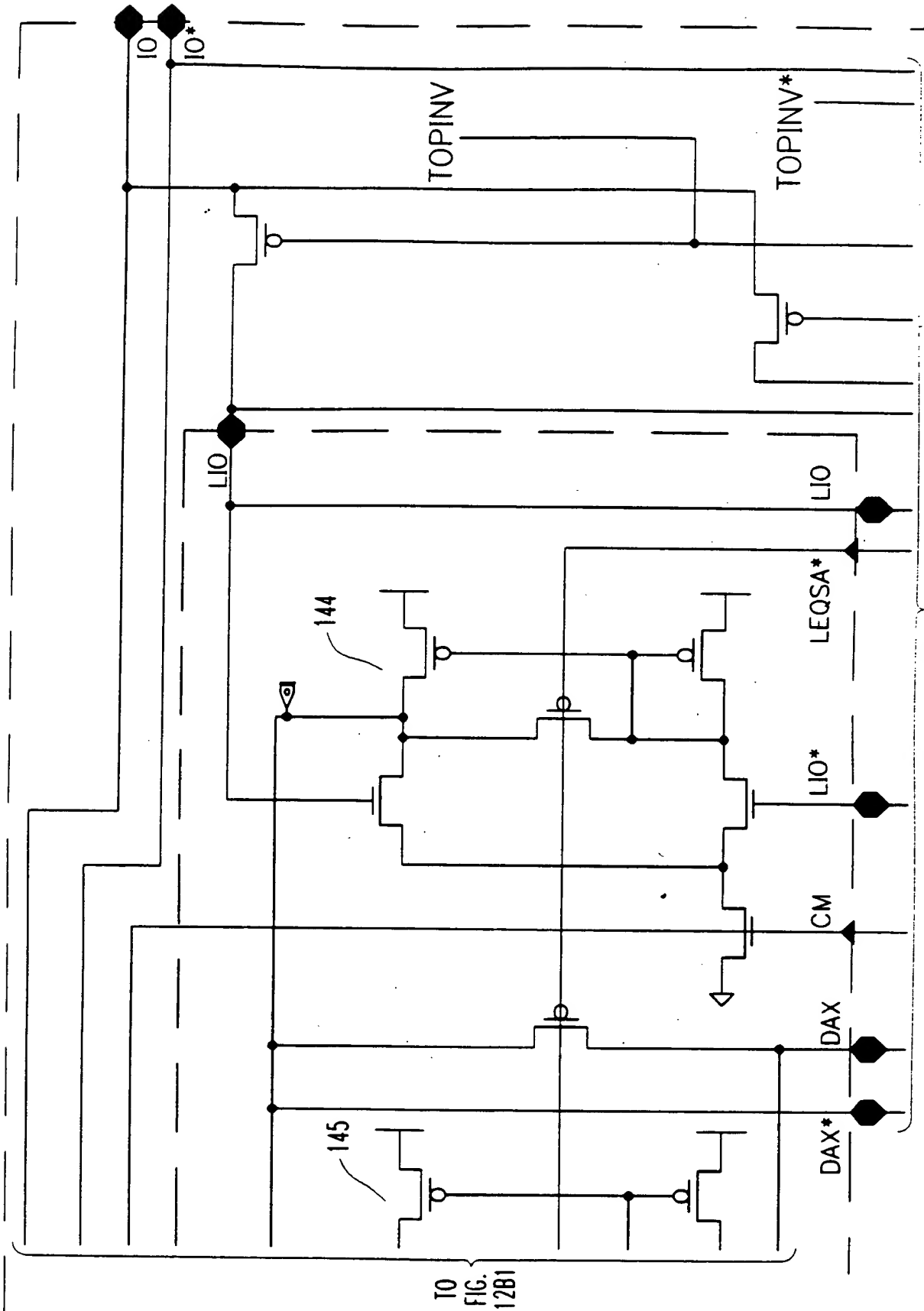
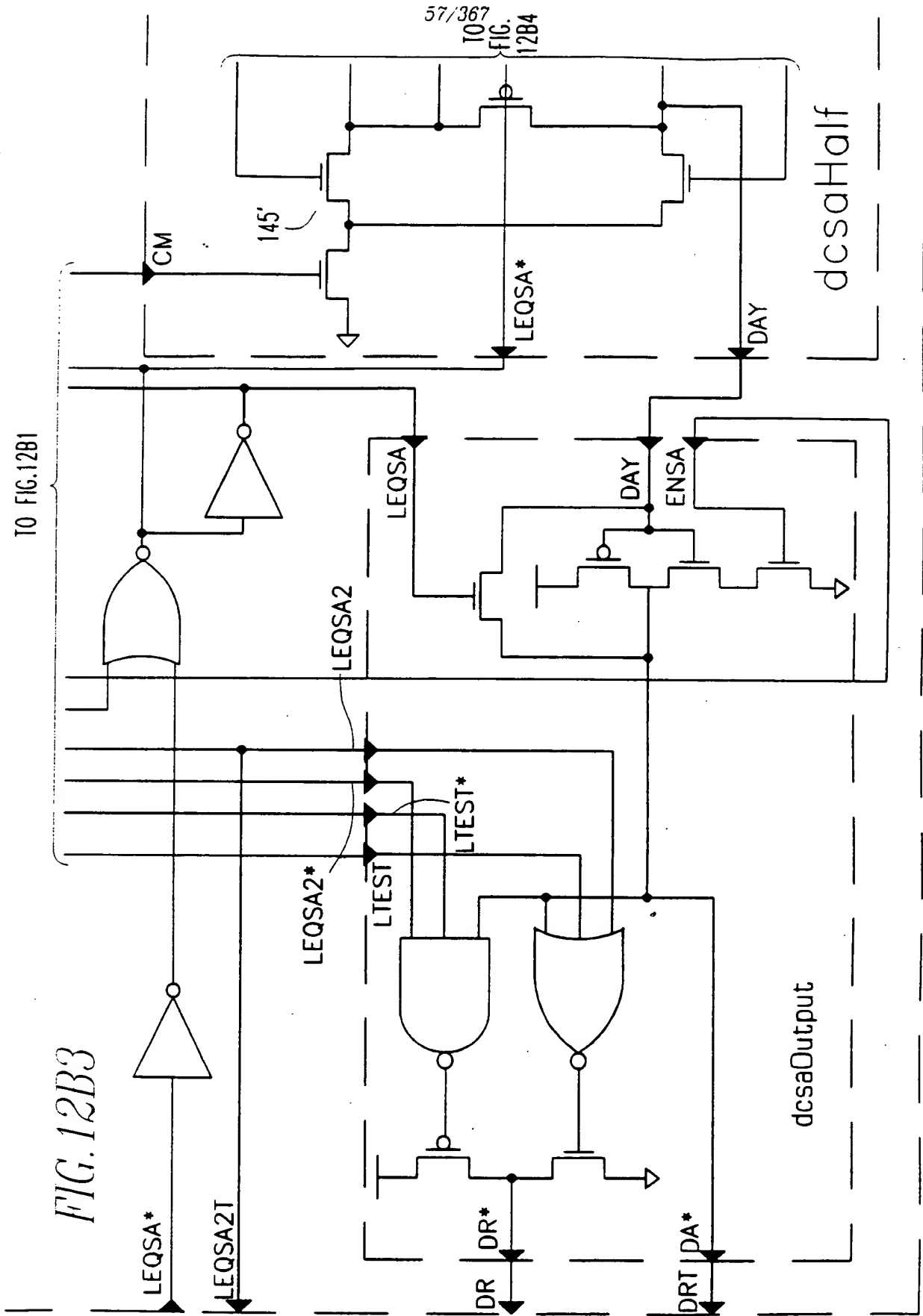


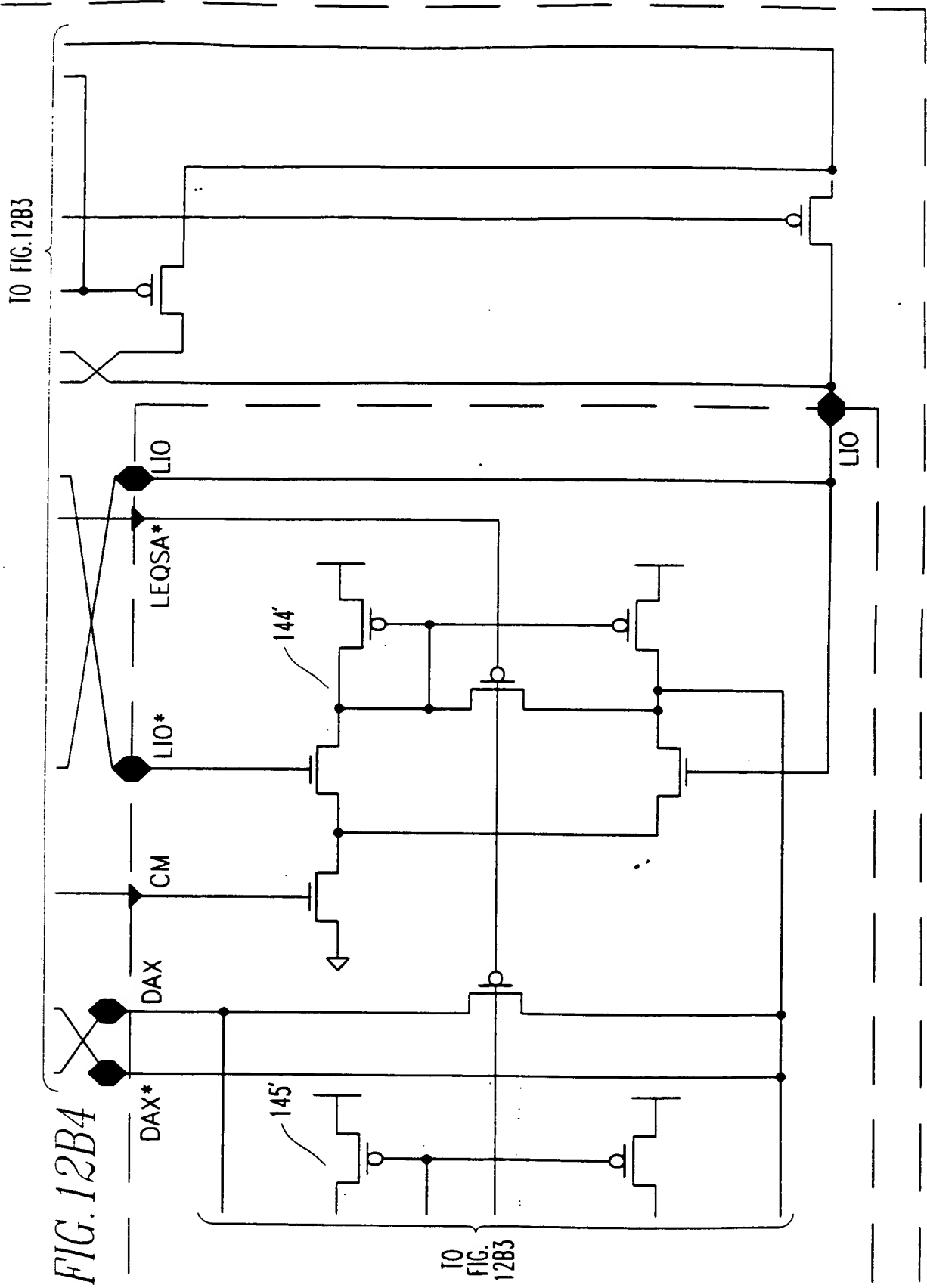
FIG. 12B2

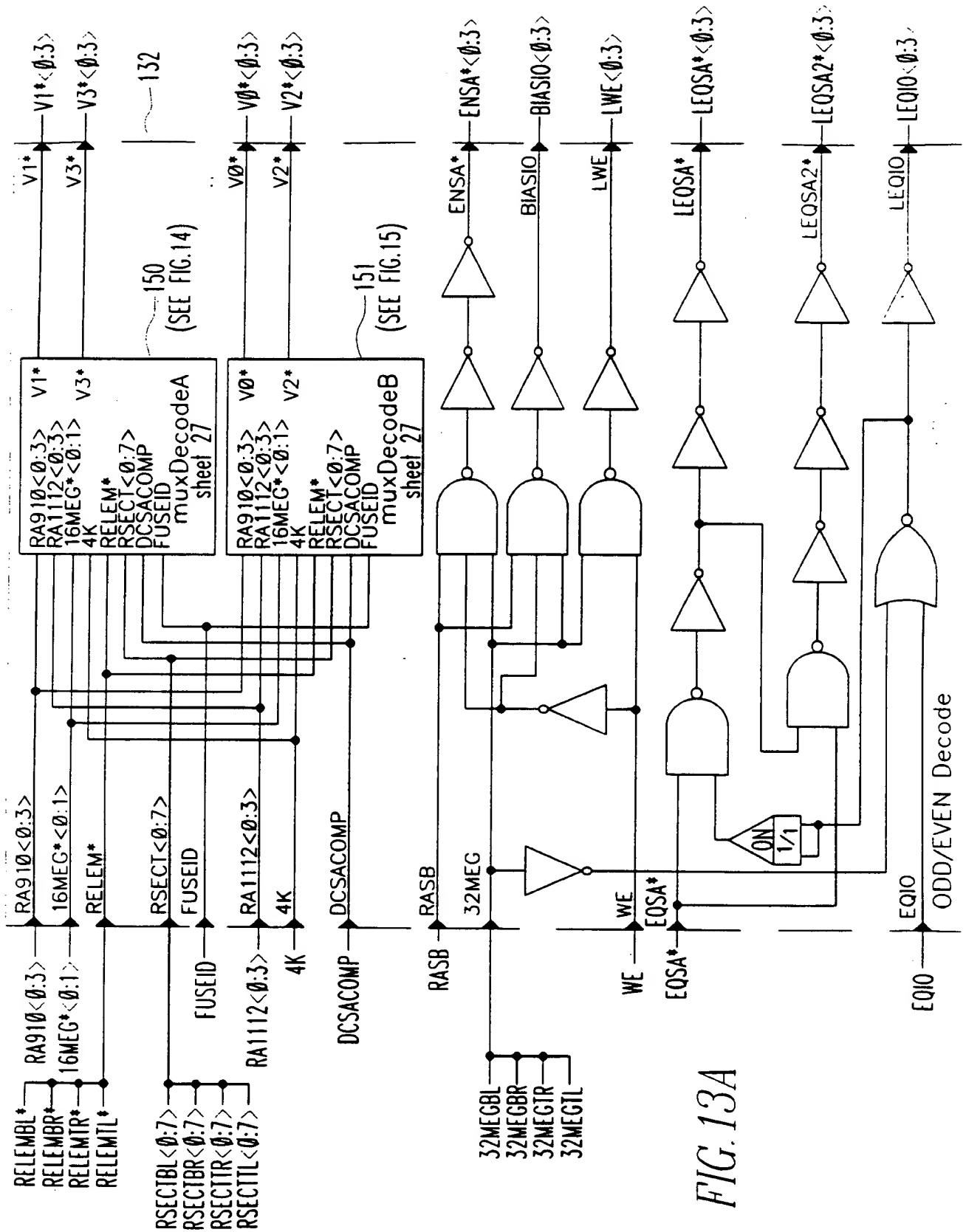
FIG. 12B1

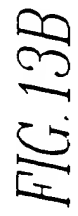
FIG. 12B3

TO FIG. 12B1









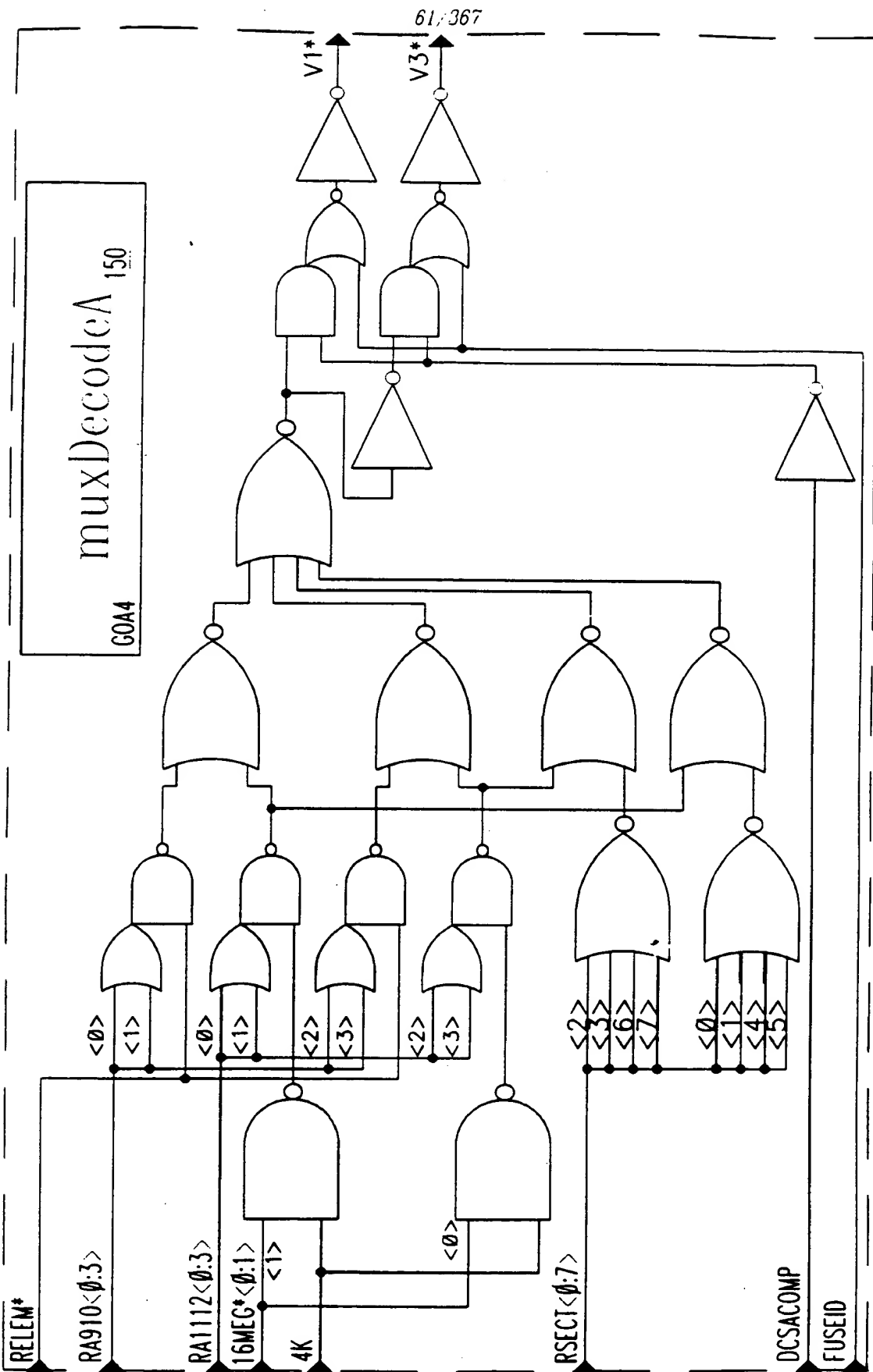
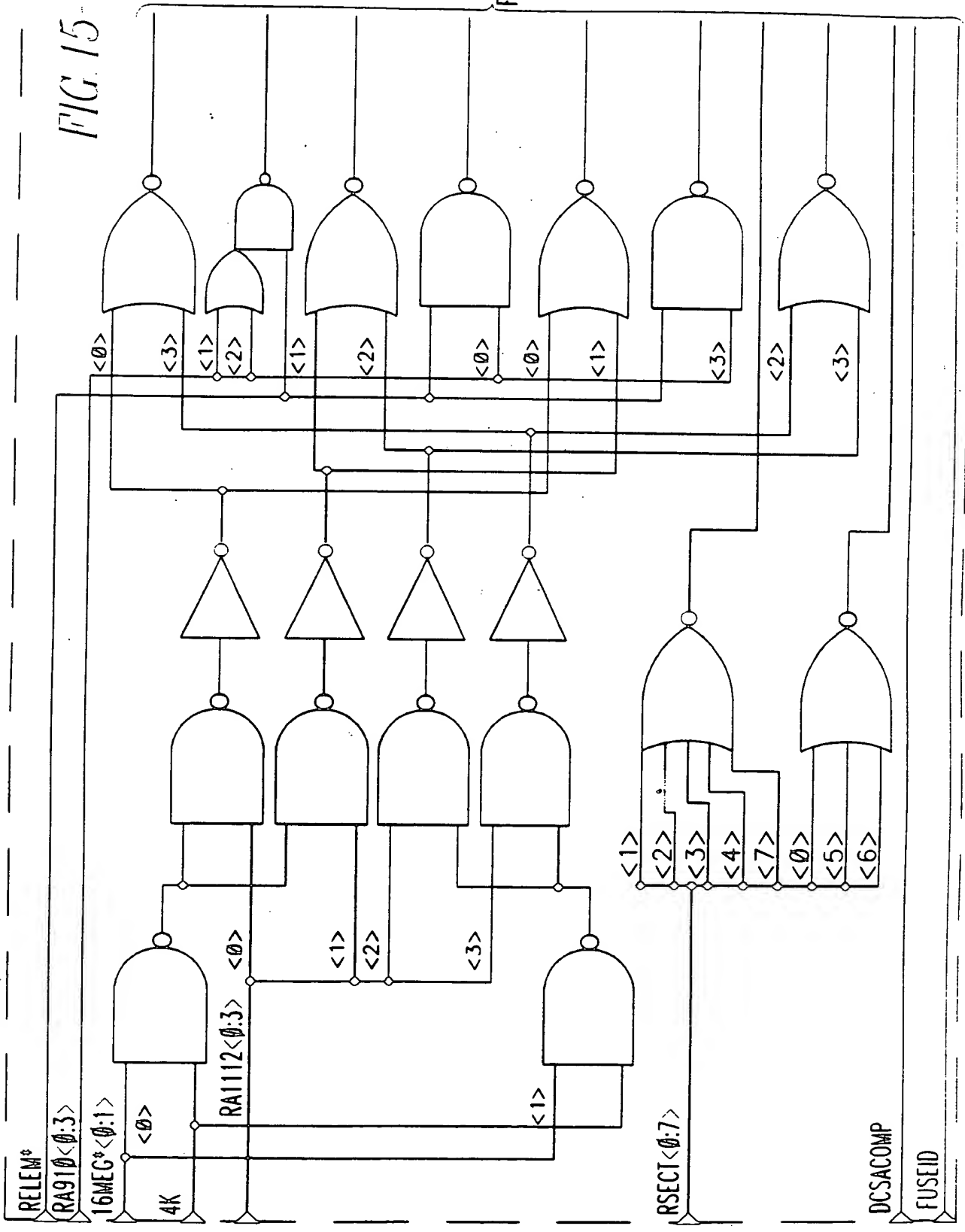


FIG. 15-1

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FIG. 15-2



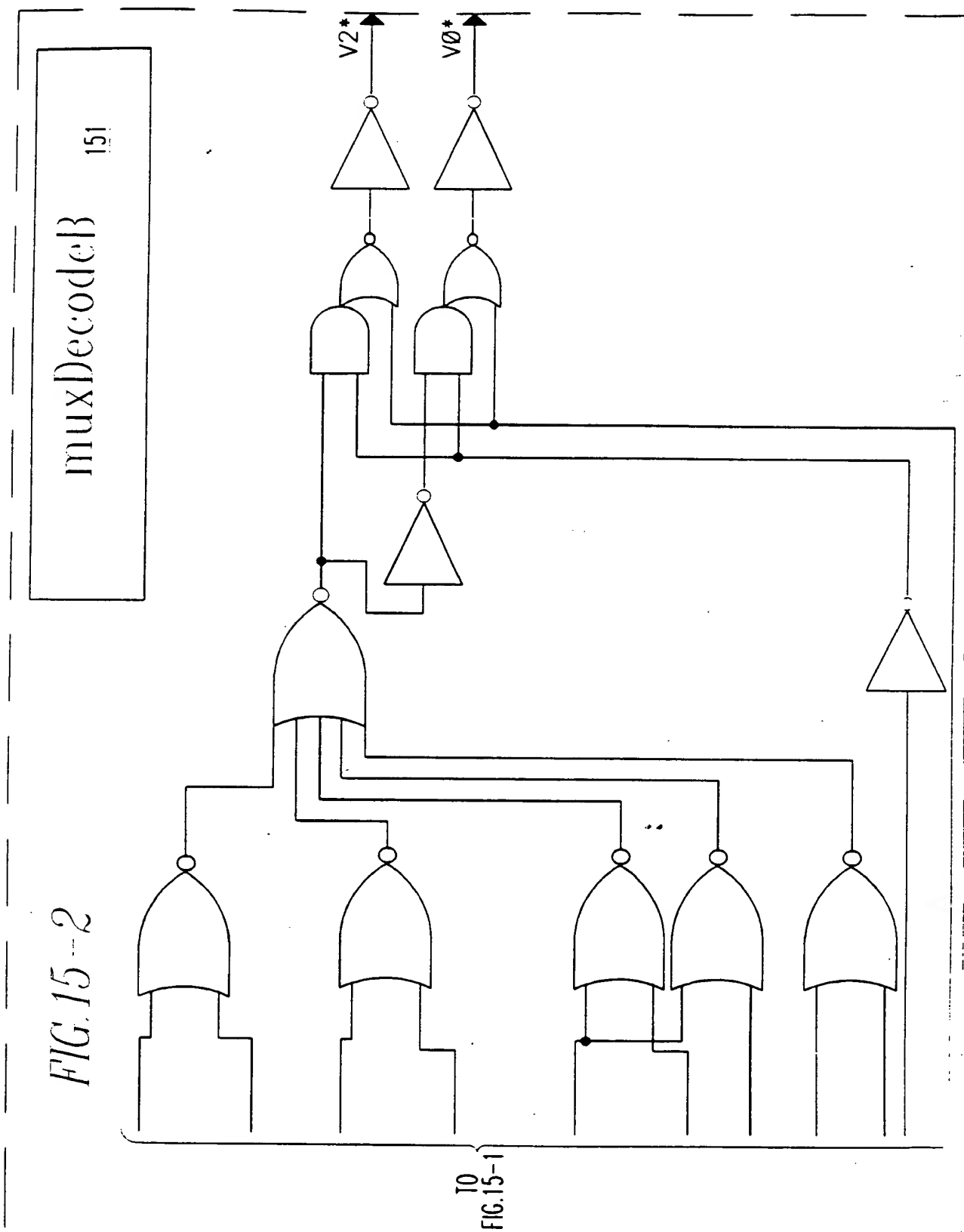
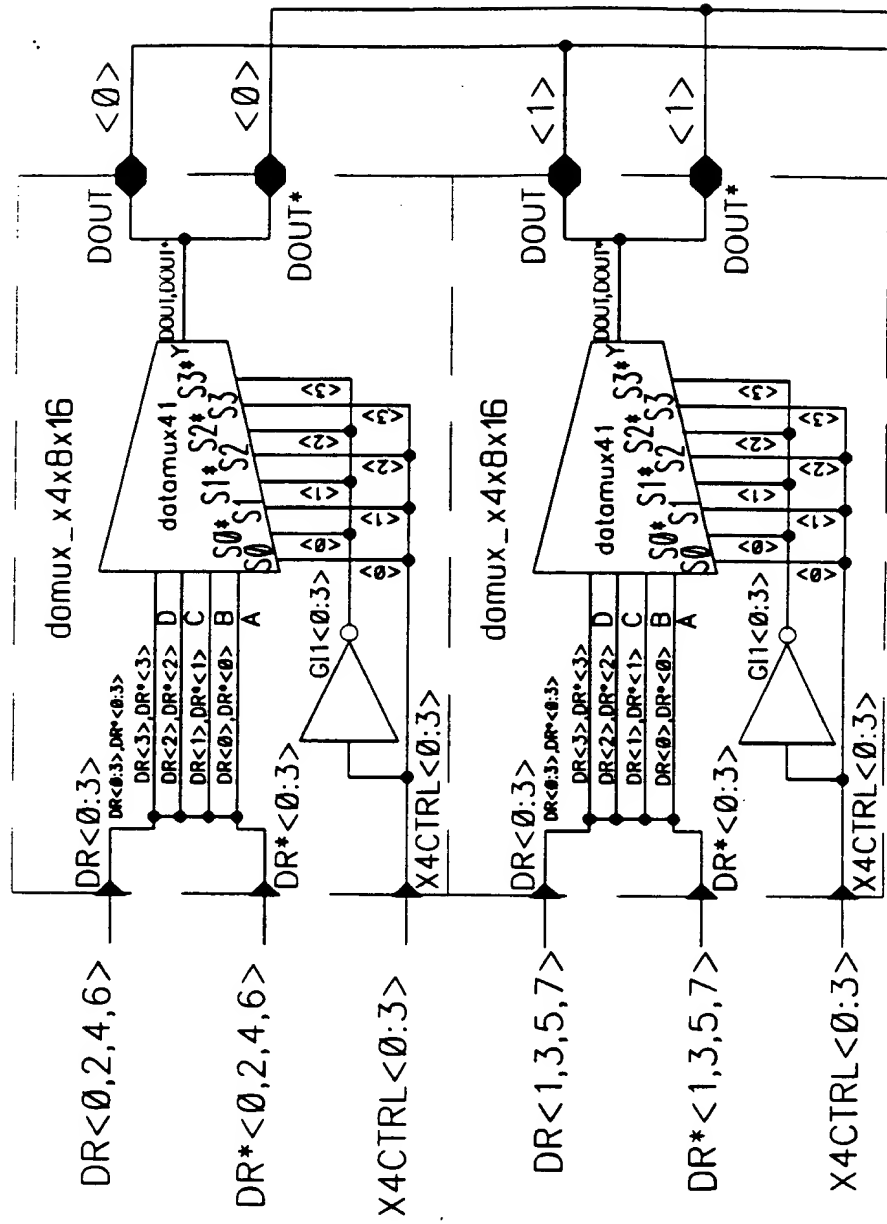
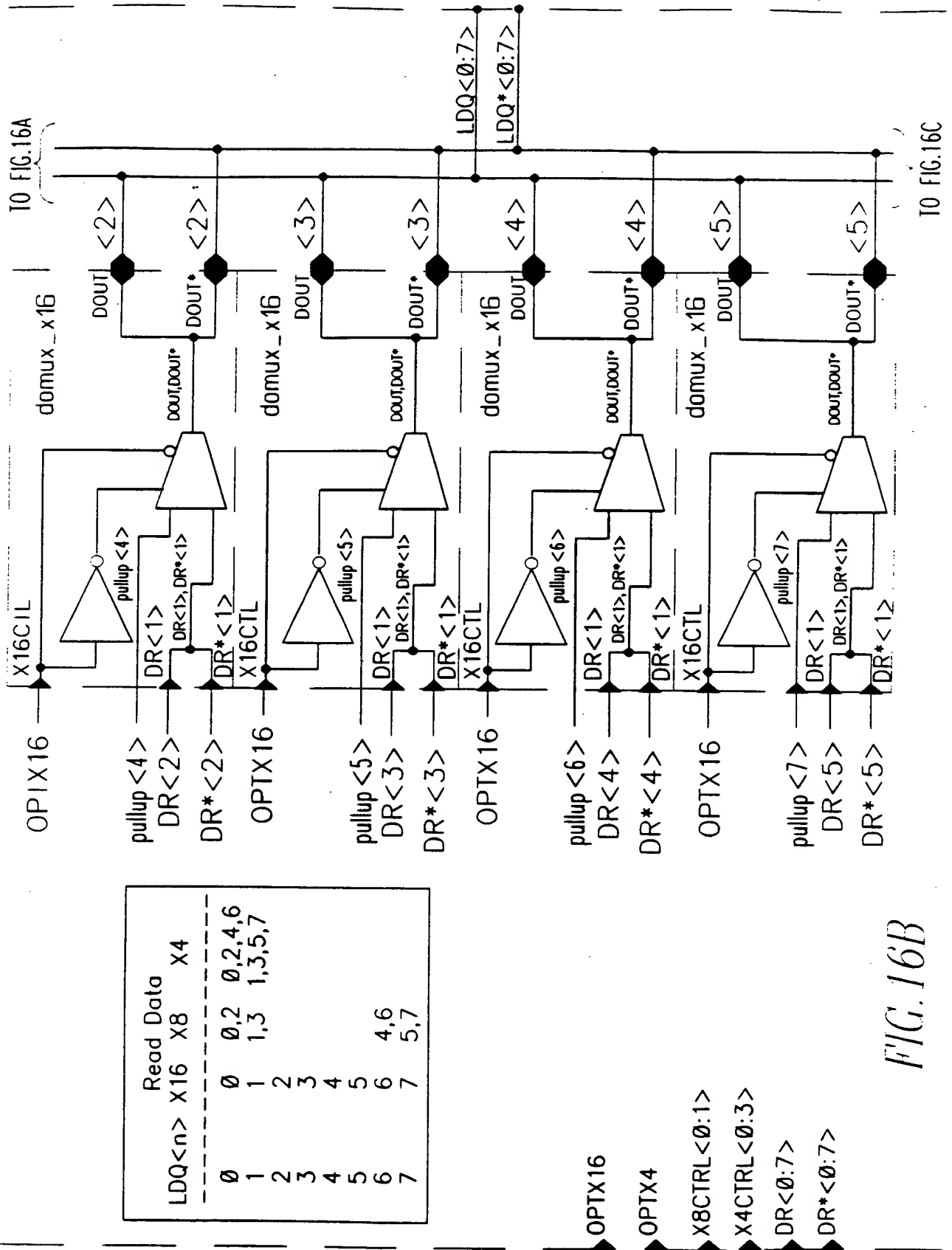
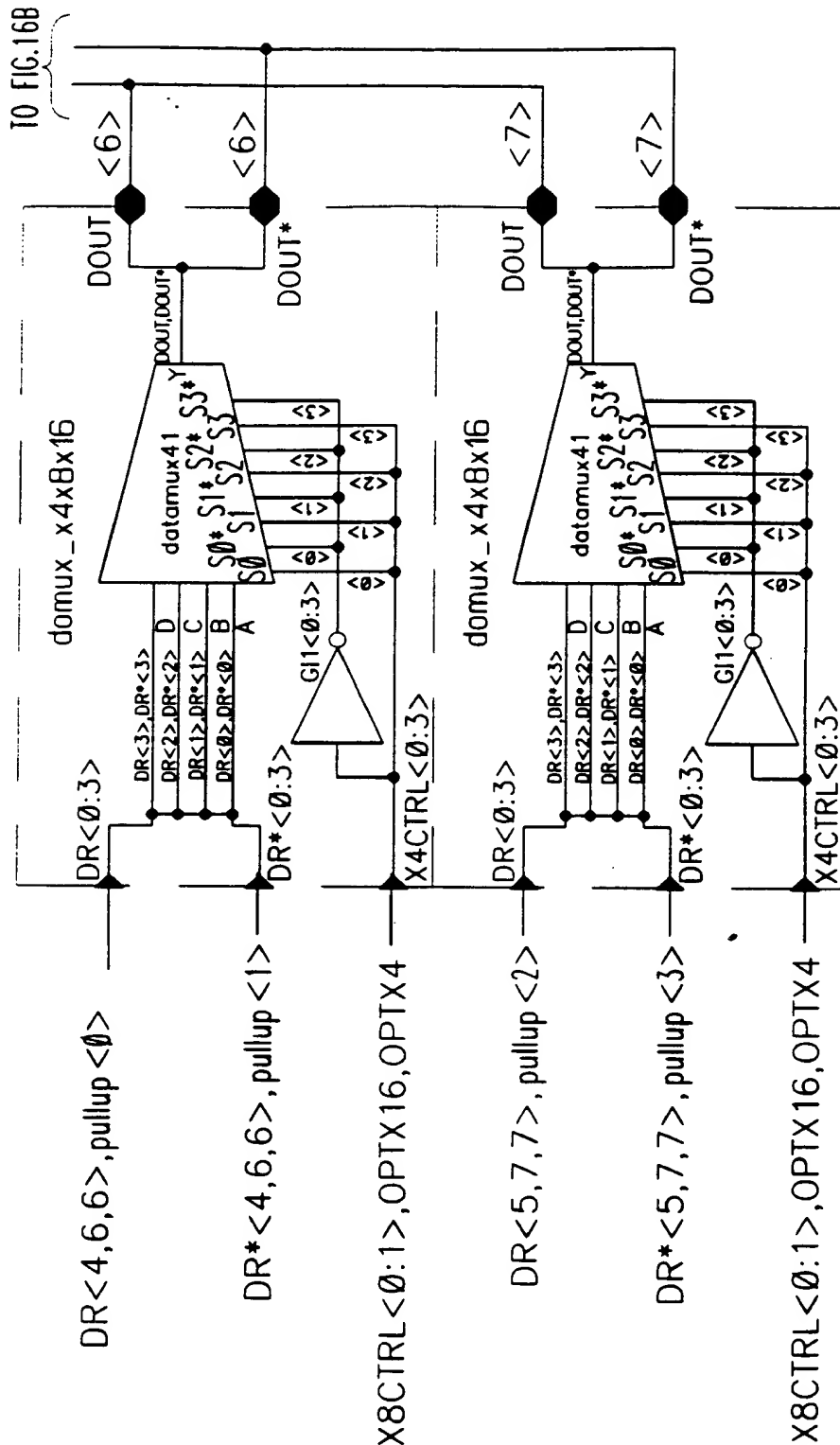


FIG. 16A

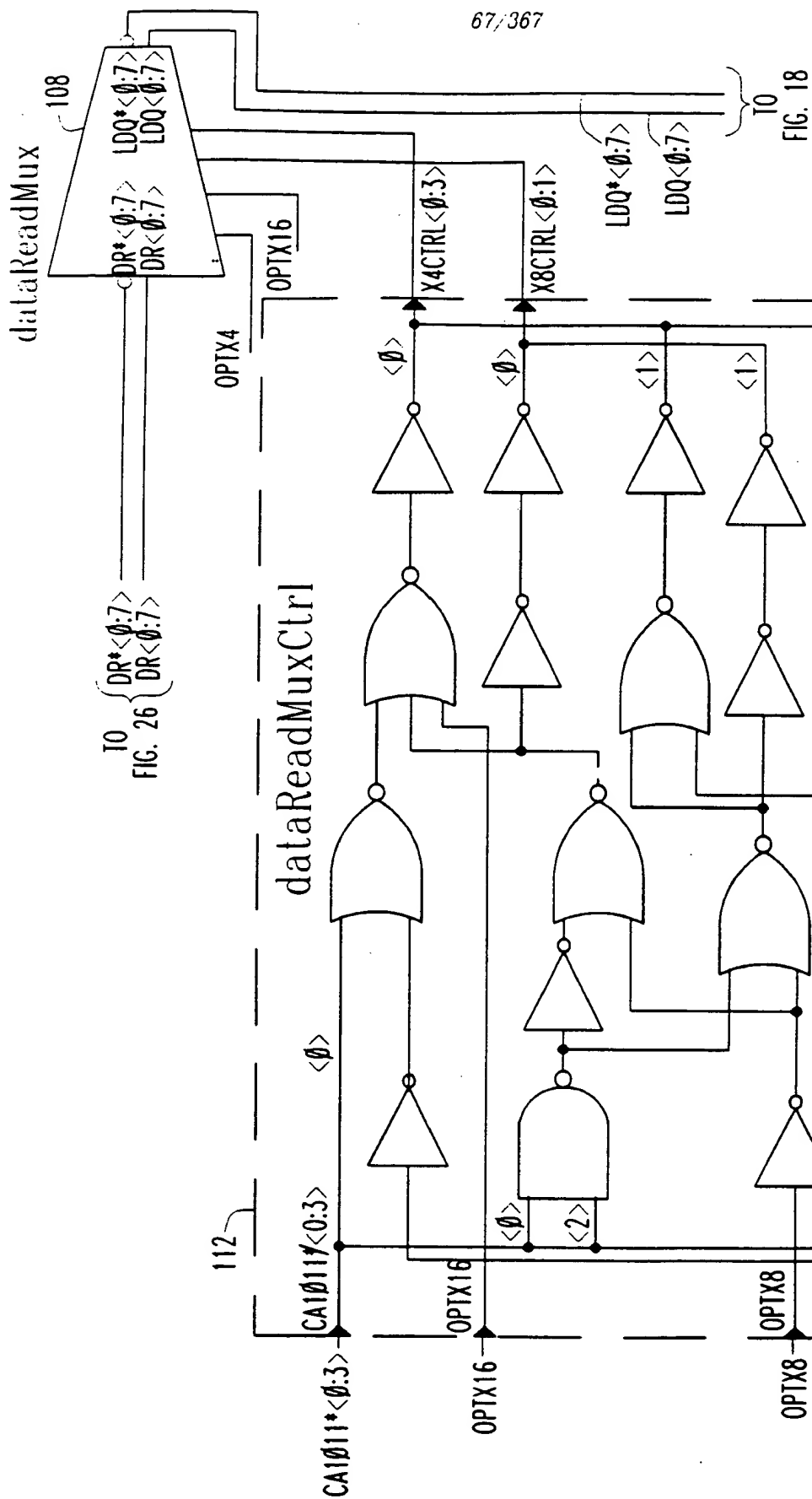






$T_{pullup} < 0:7 >$

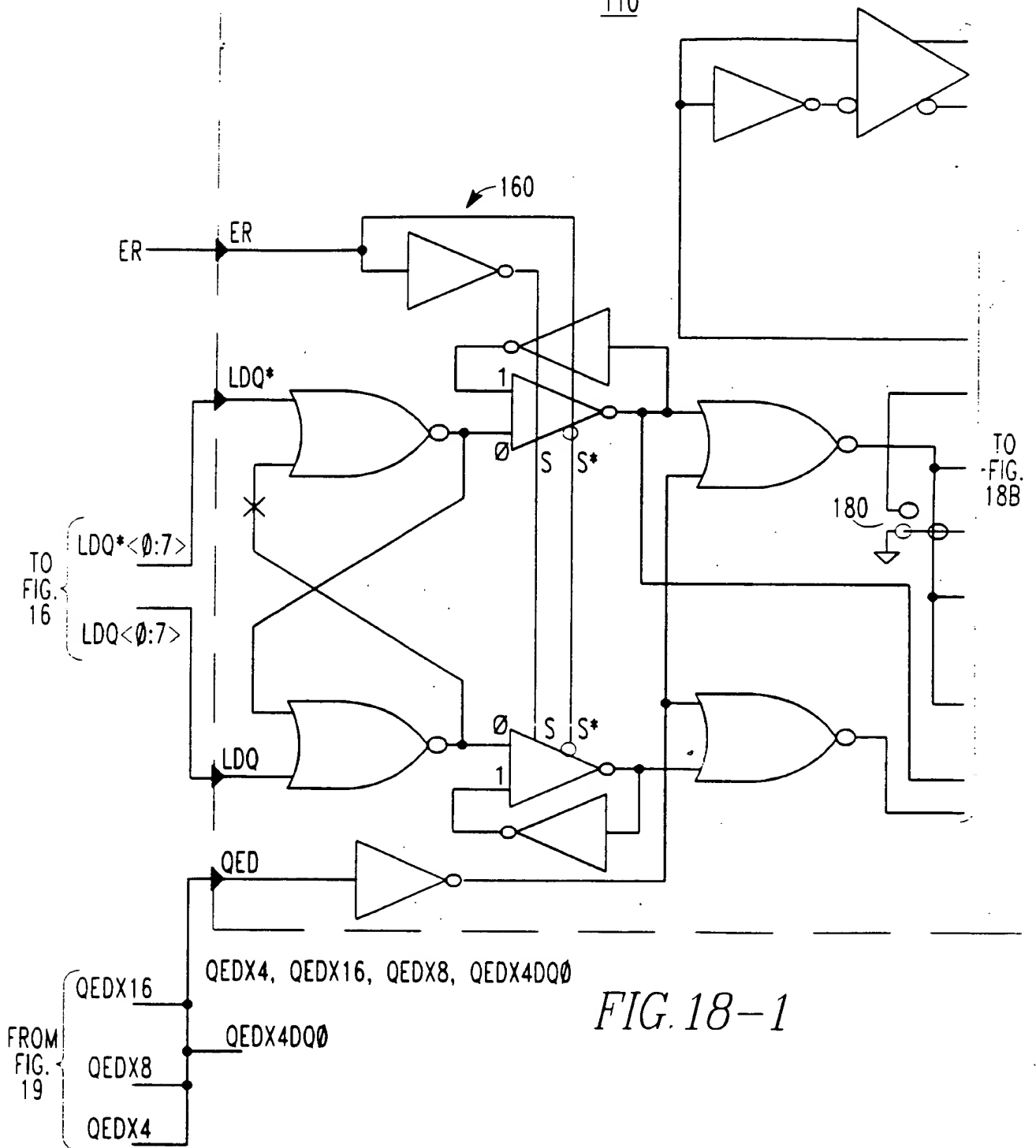
dataReadMux



2191

DATA OUTPUT BUFFER

110



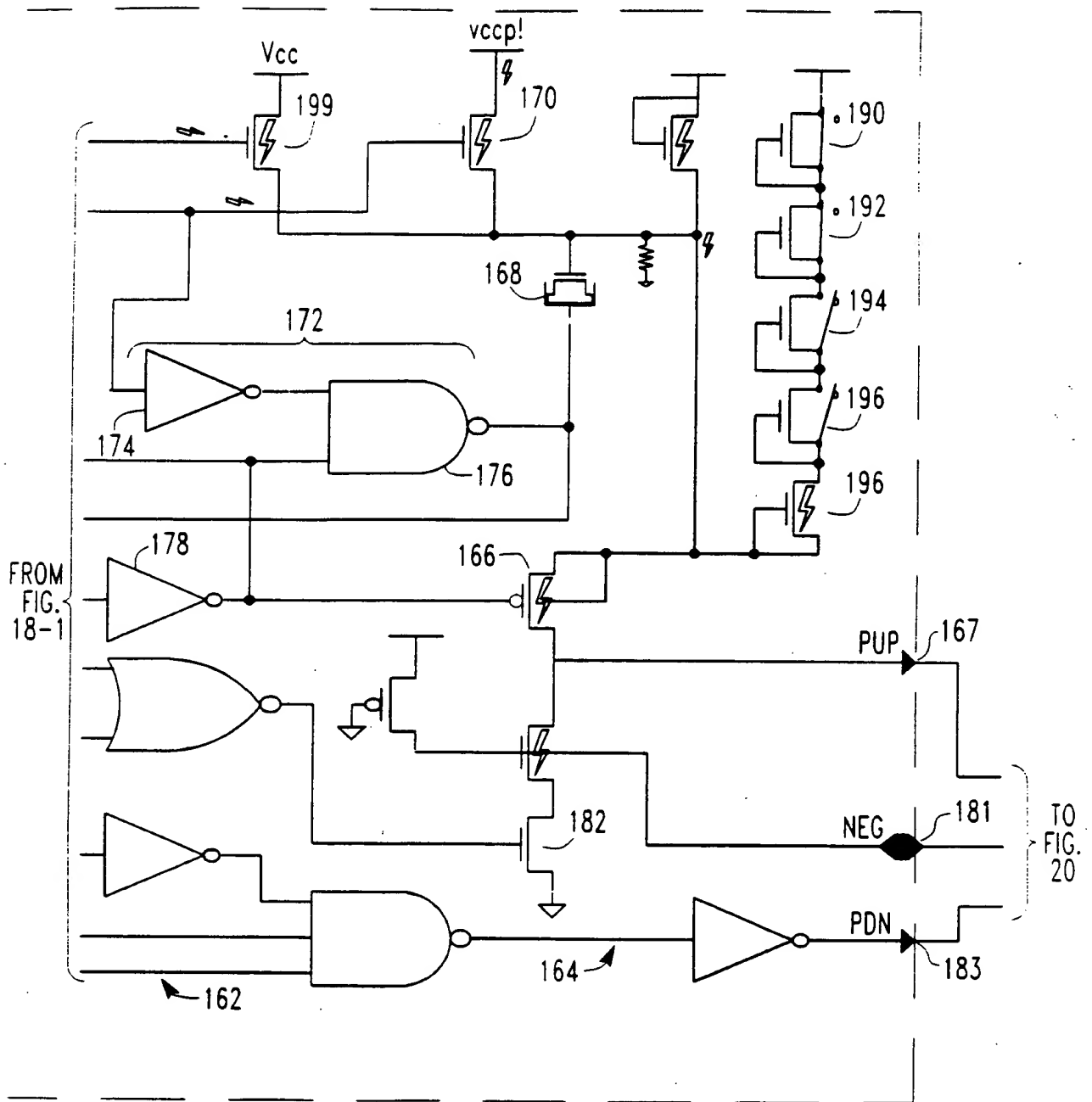
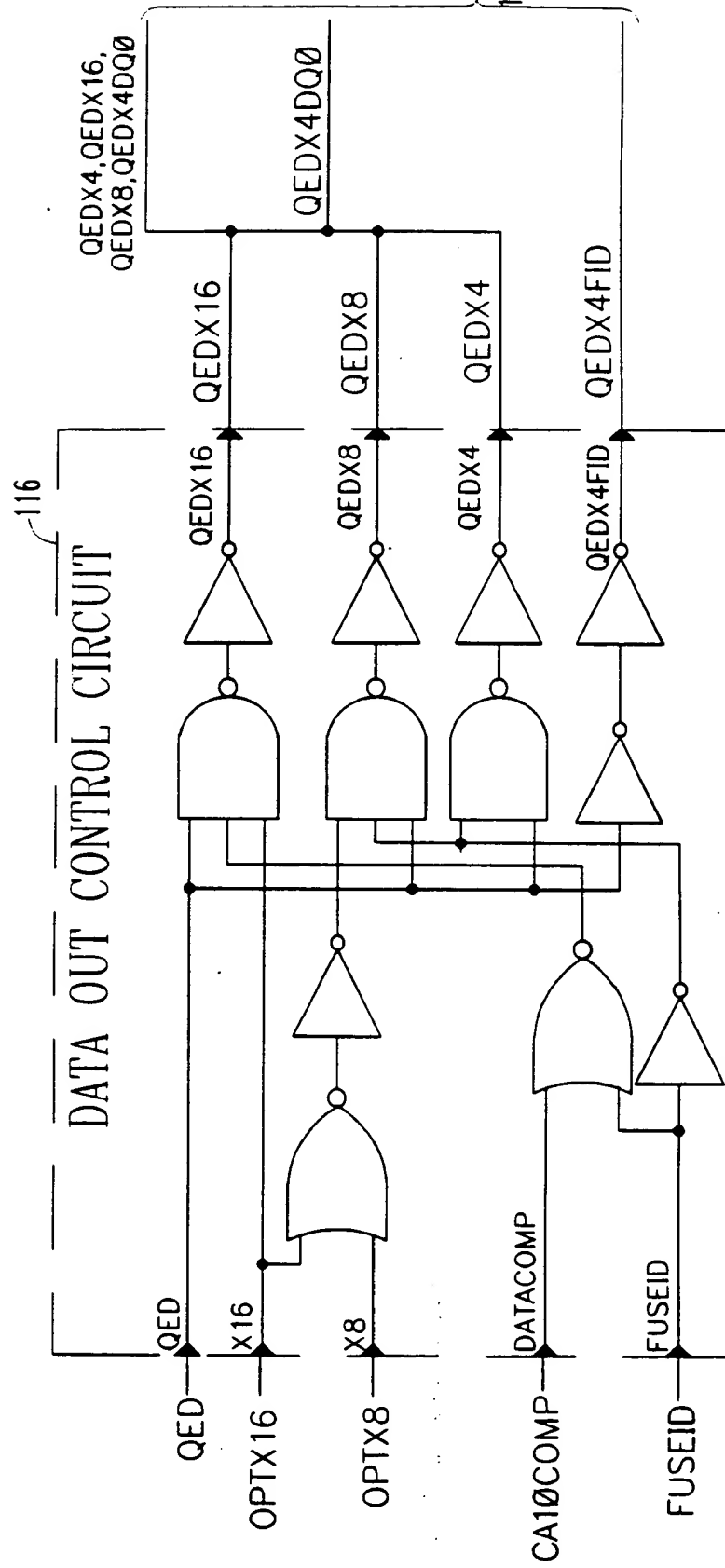


FIG. 18-2



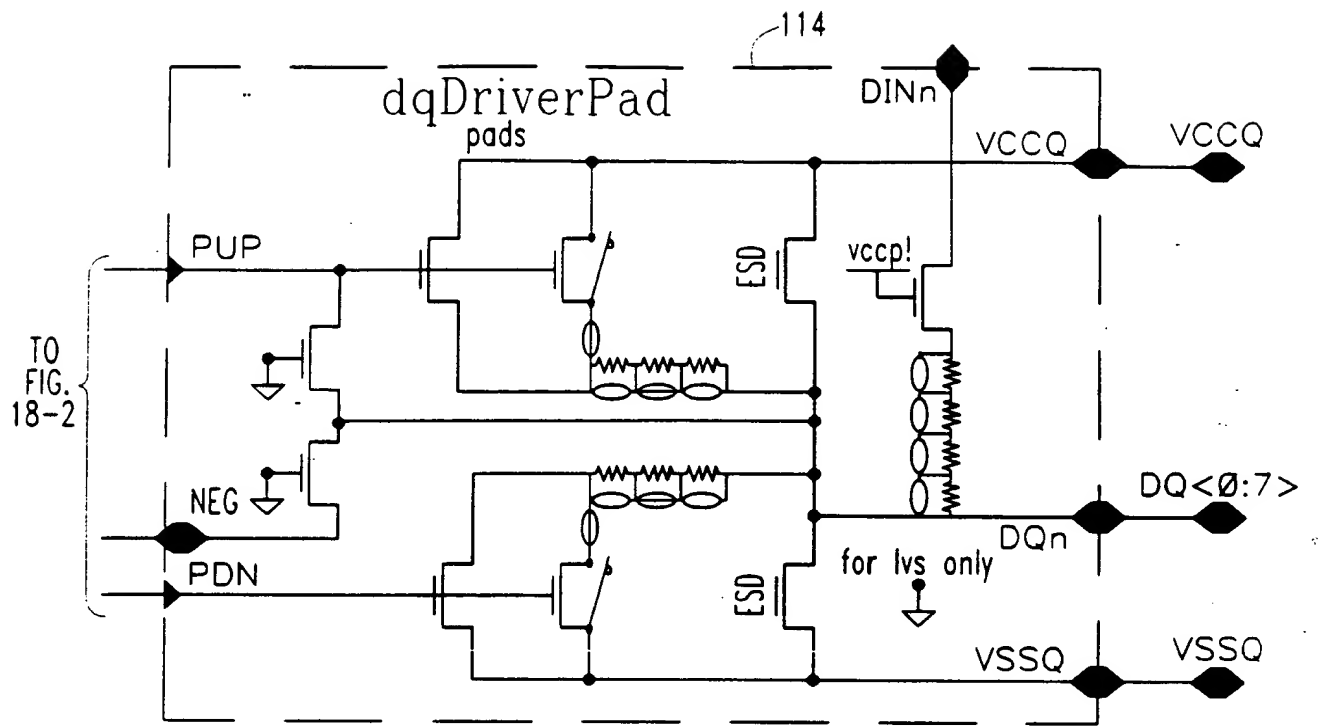


FIG. 20

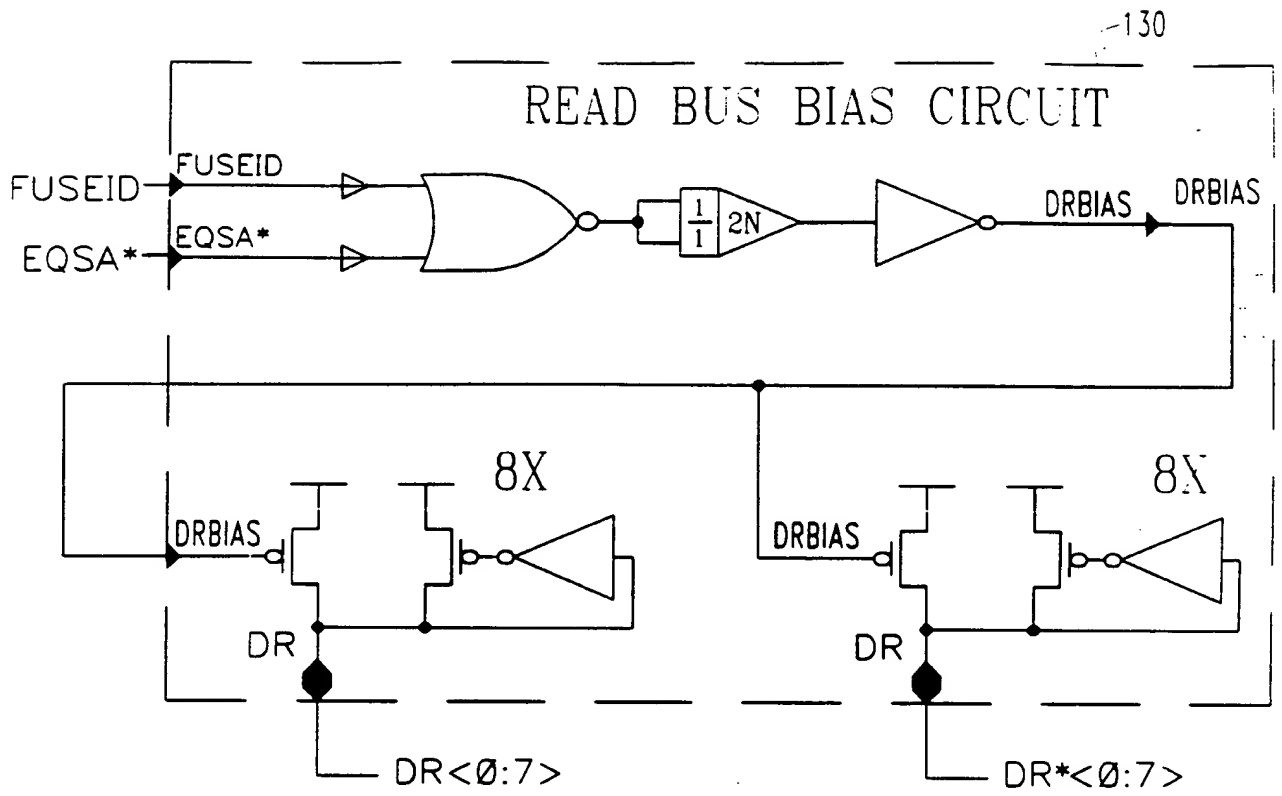


FIG. 21

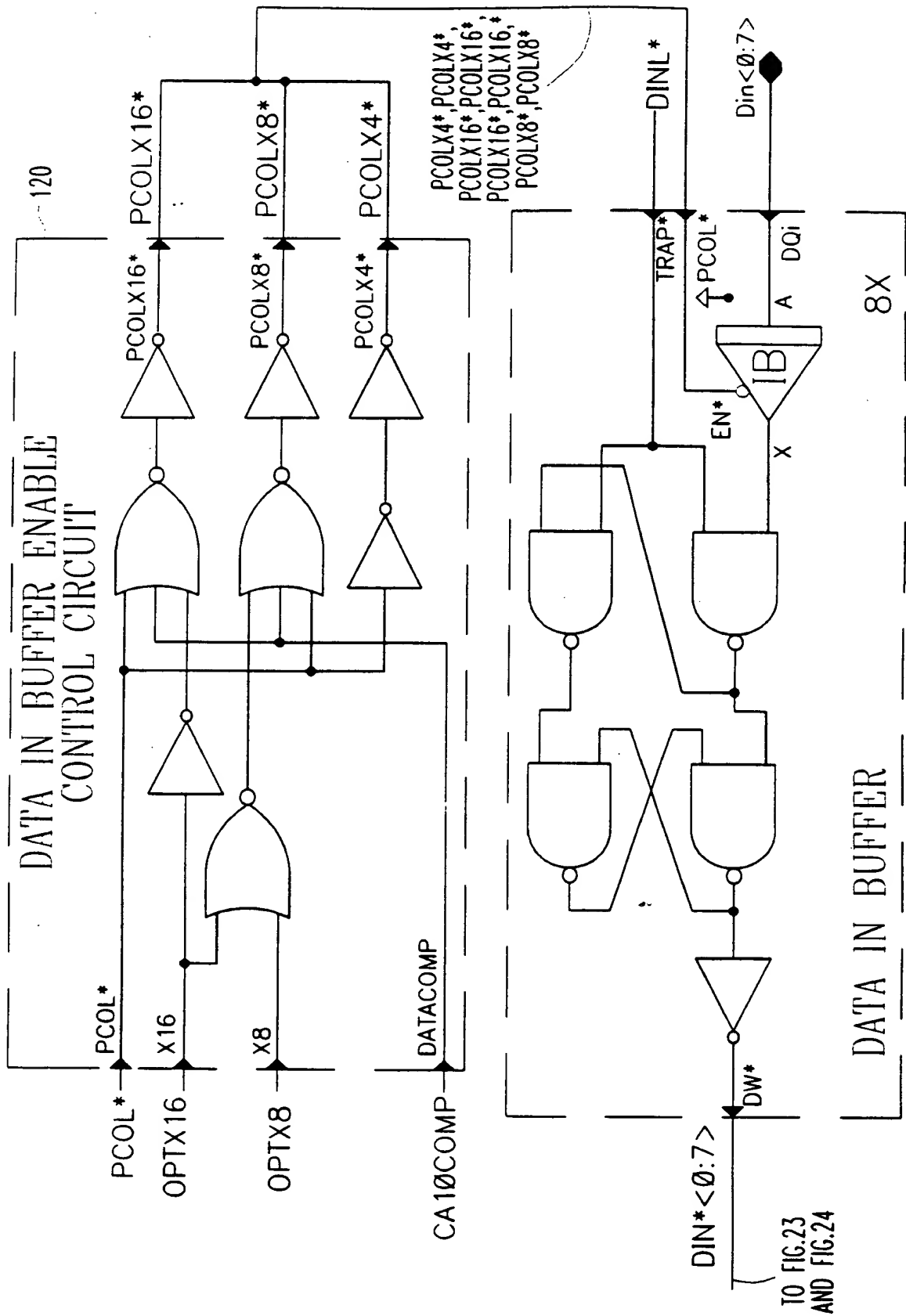
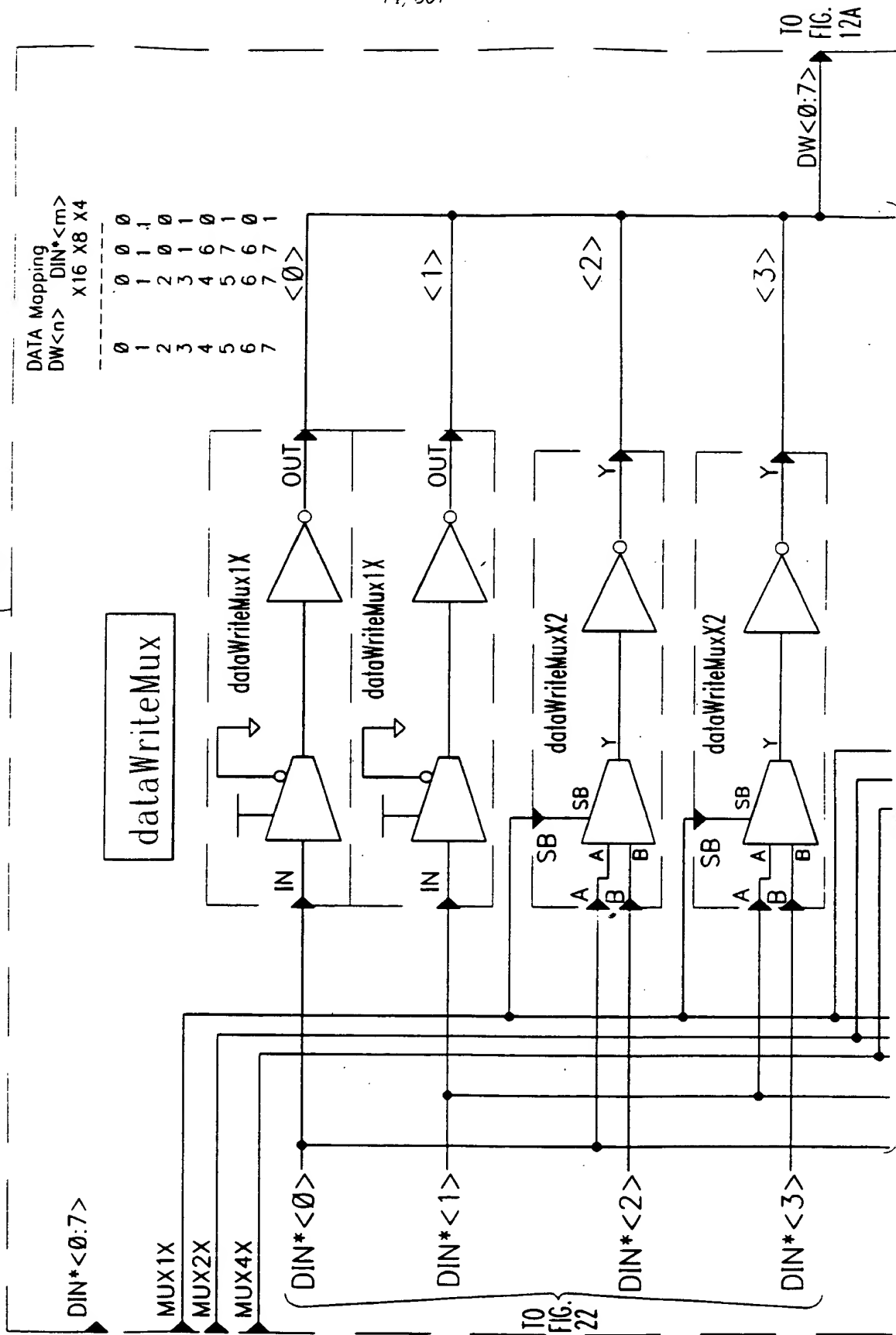


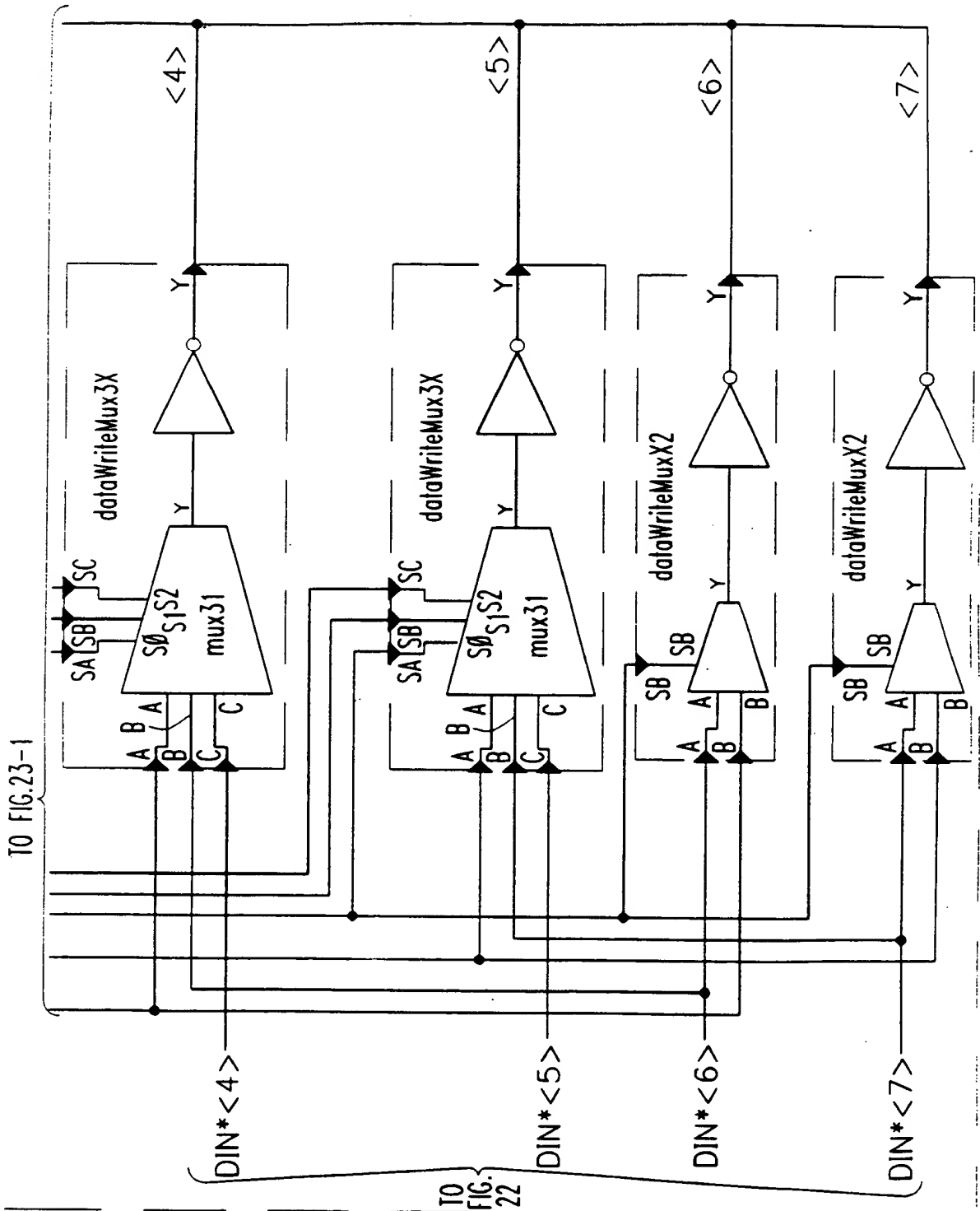
FIG. 22

FIG. 23-1

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TO FIG. 23-2



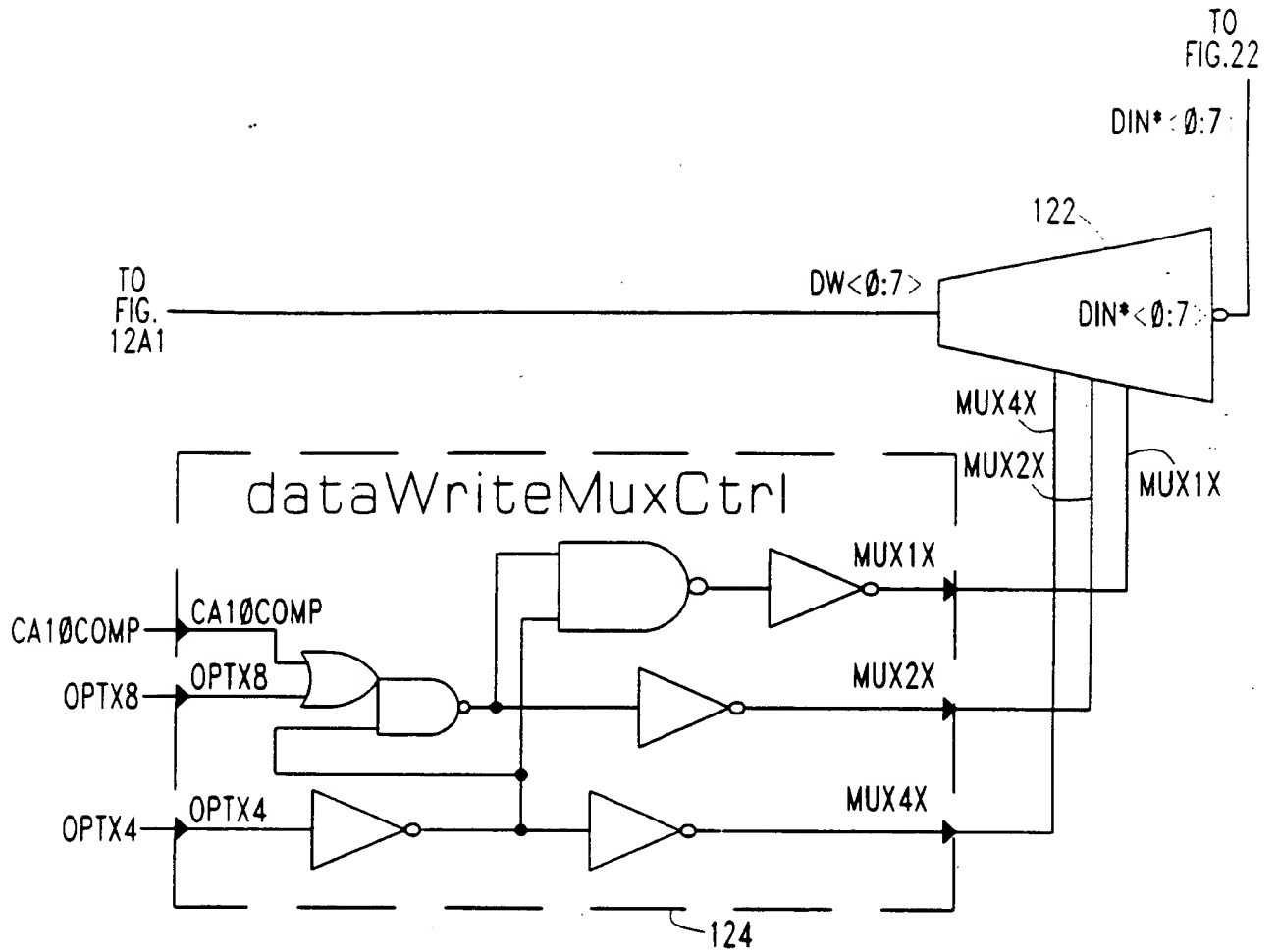


FIG. 24

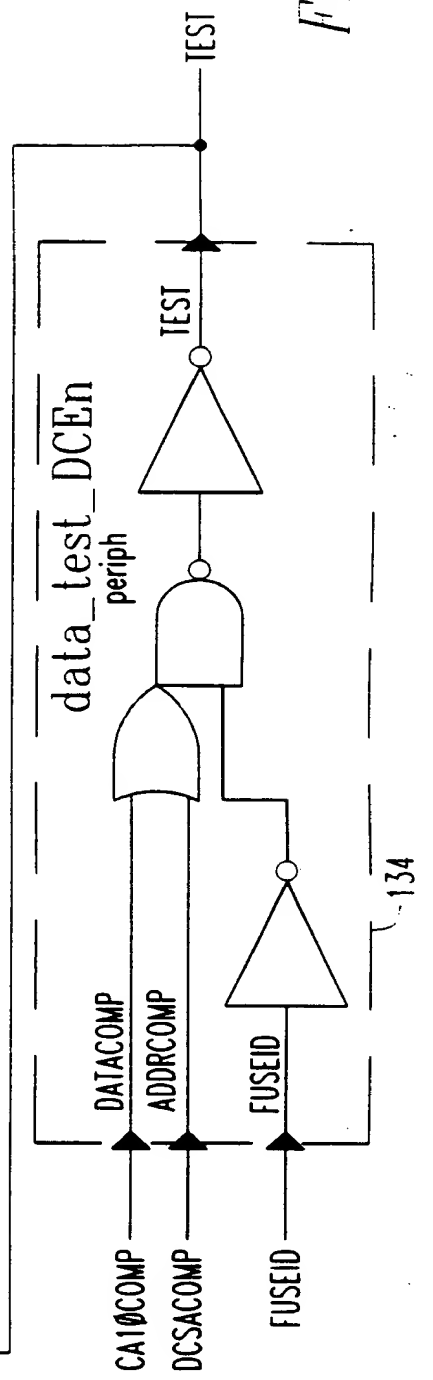
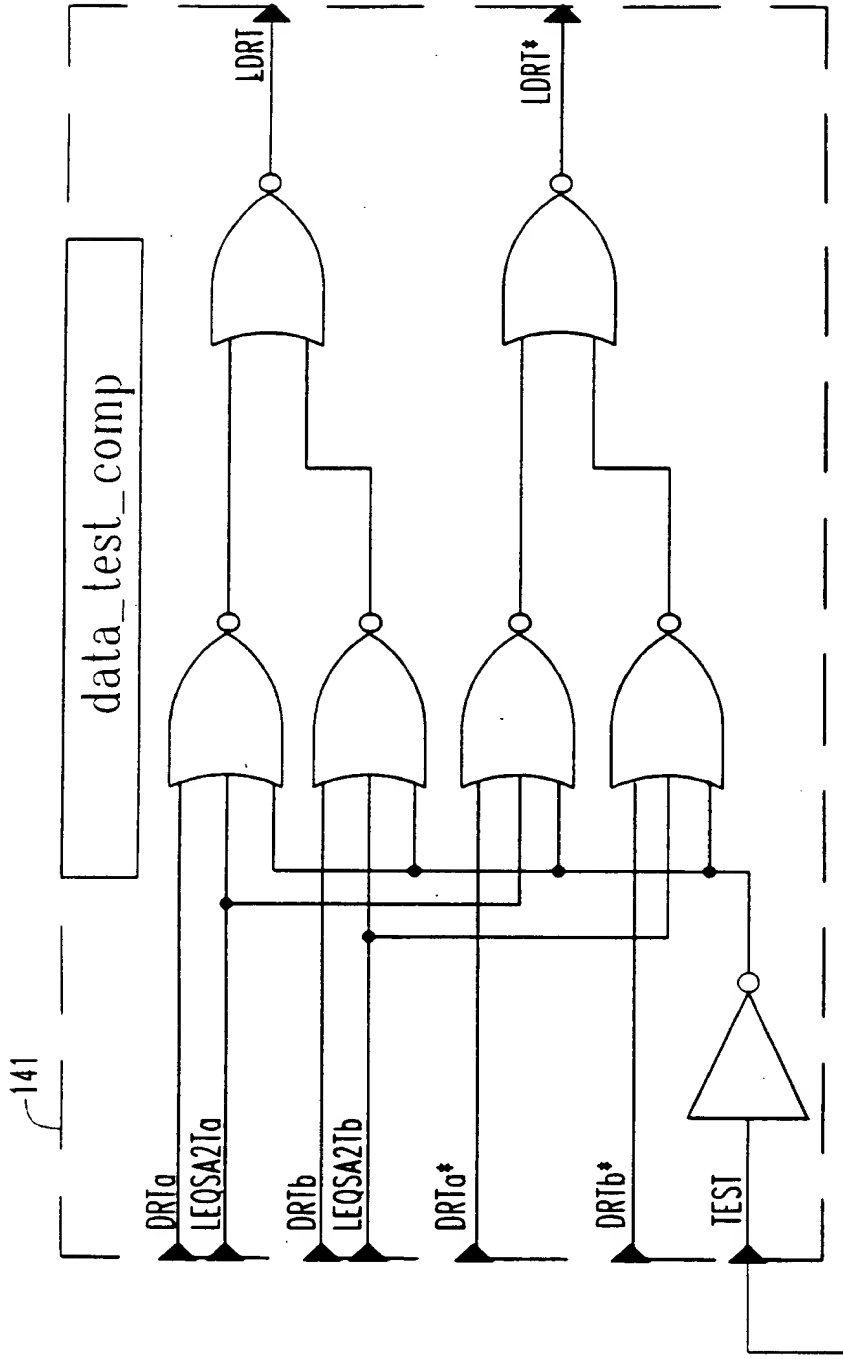


FIG. 25

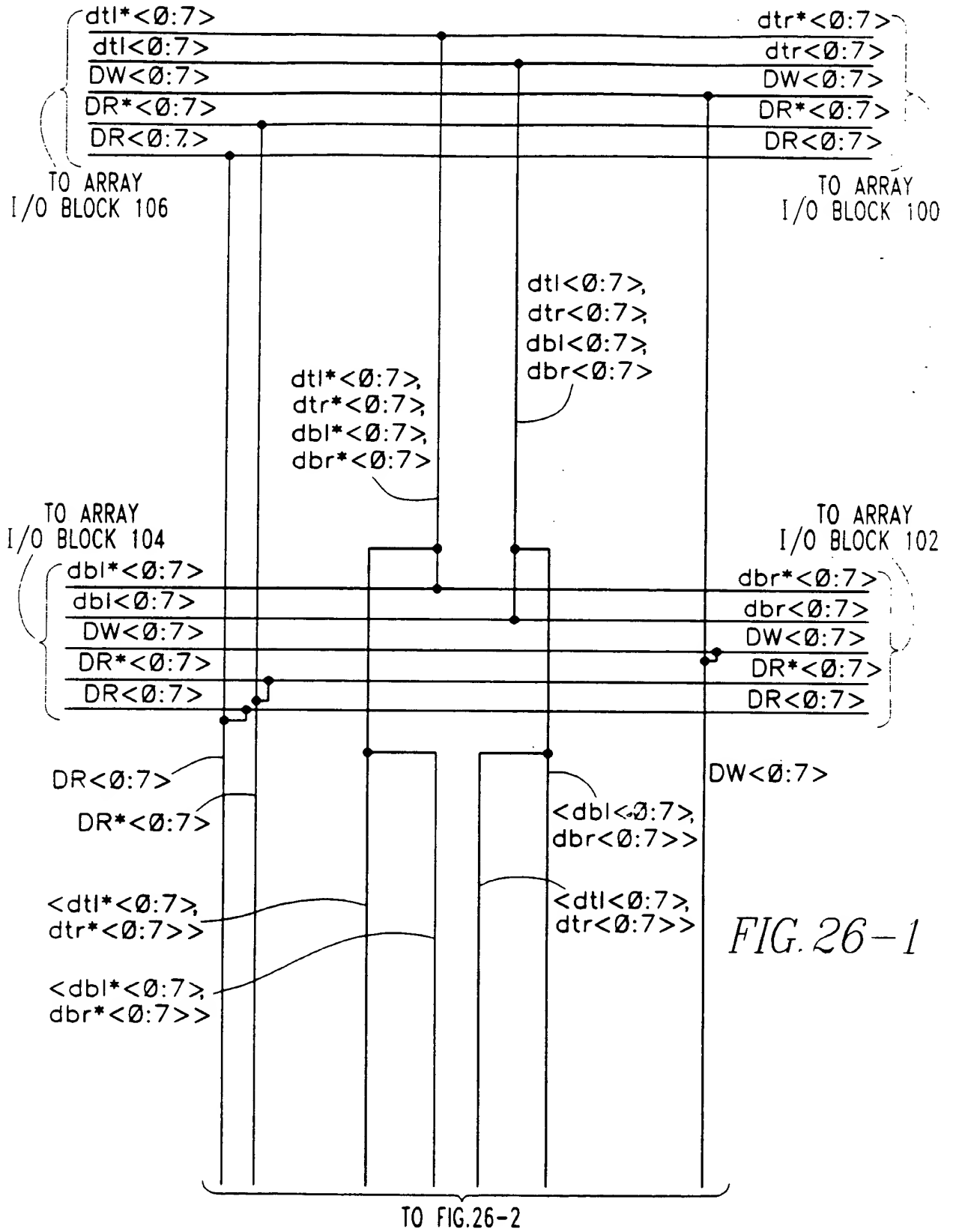


FIG. 26-1

FROM FIG.26-1

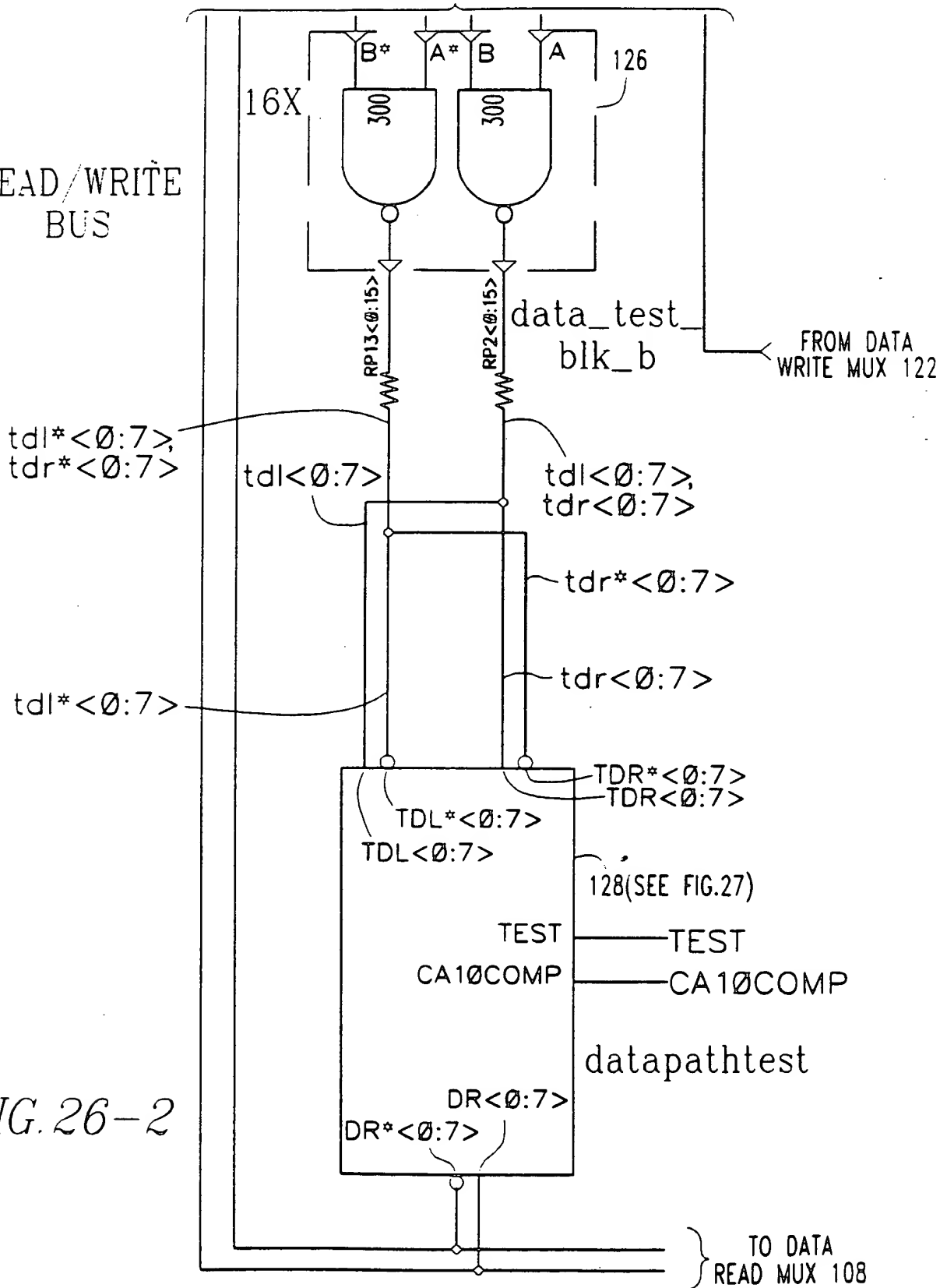
READ/WRITE
BUS

FIG. 26-2

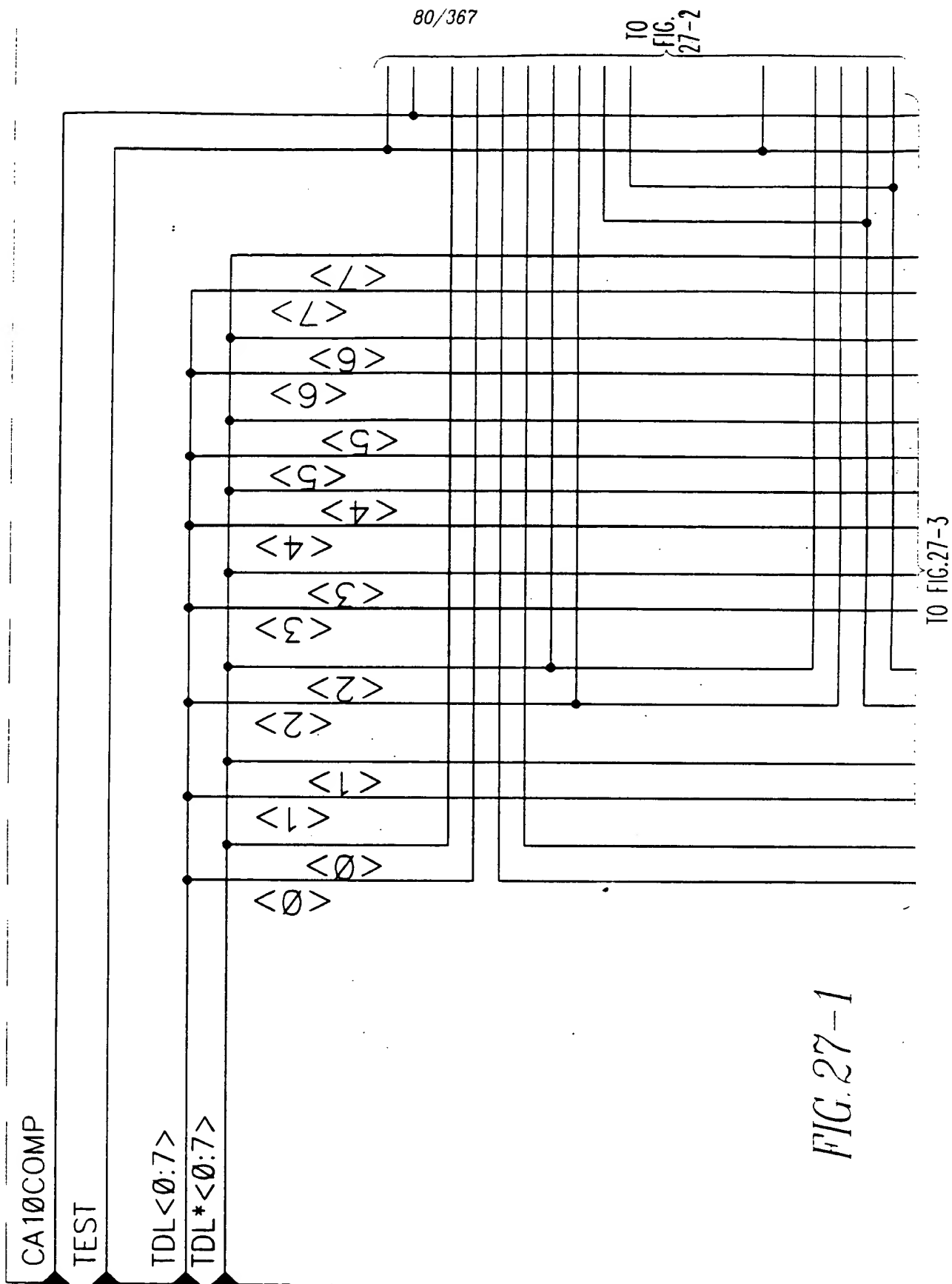
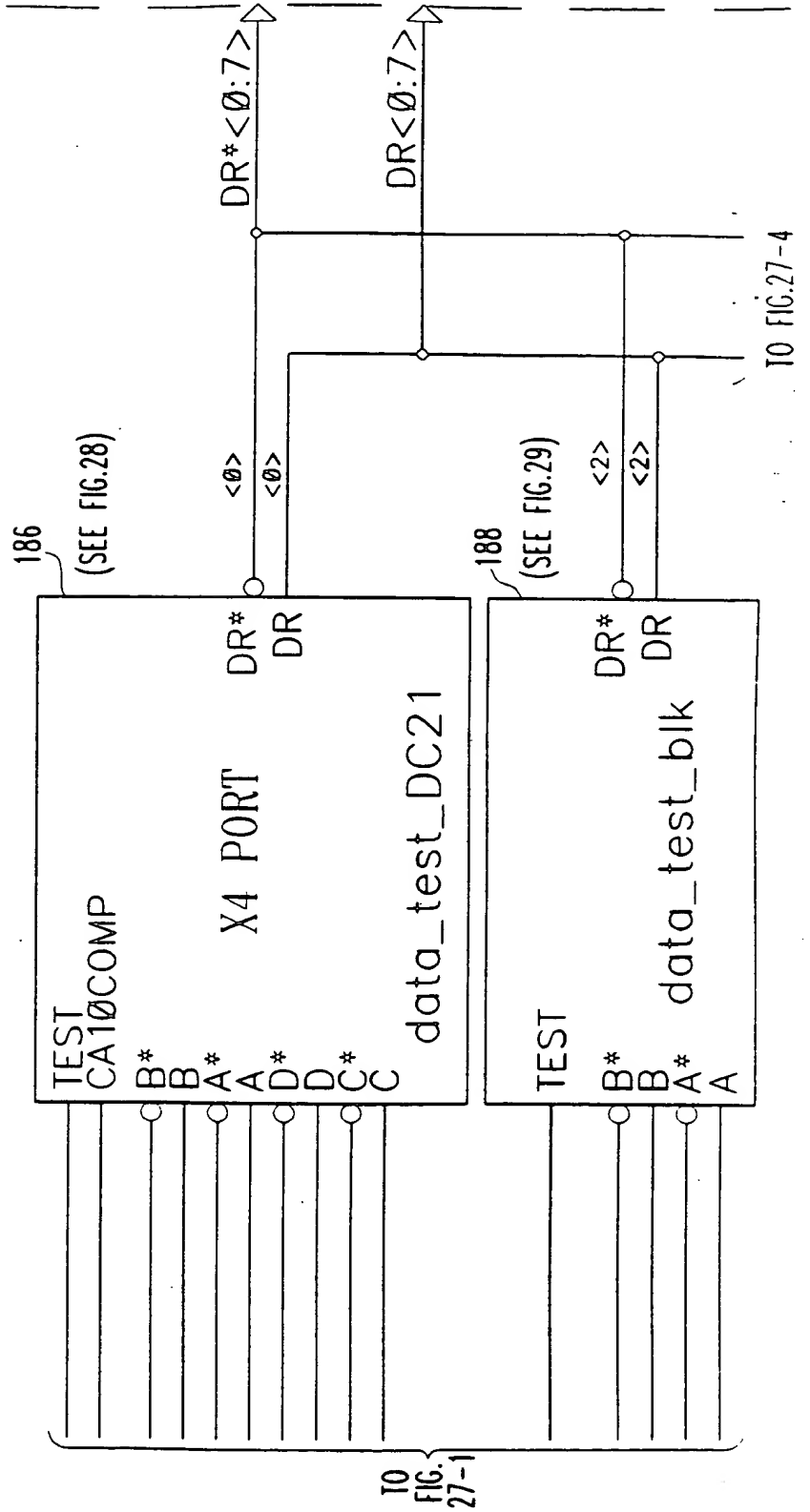


FIG. 27-1

dataPathTest
Block

128



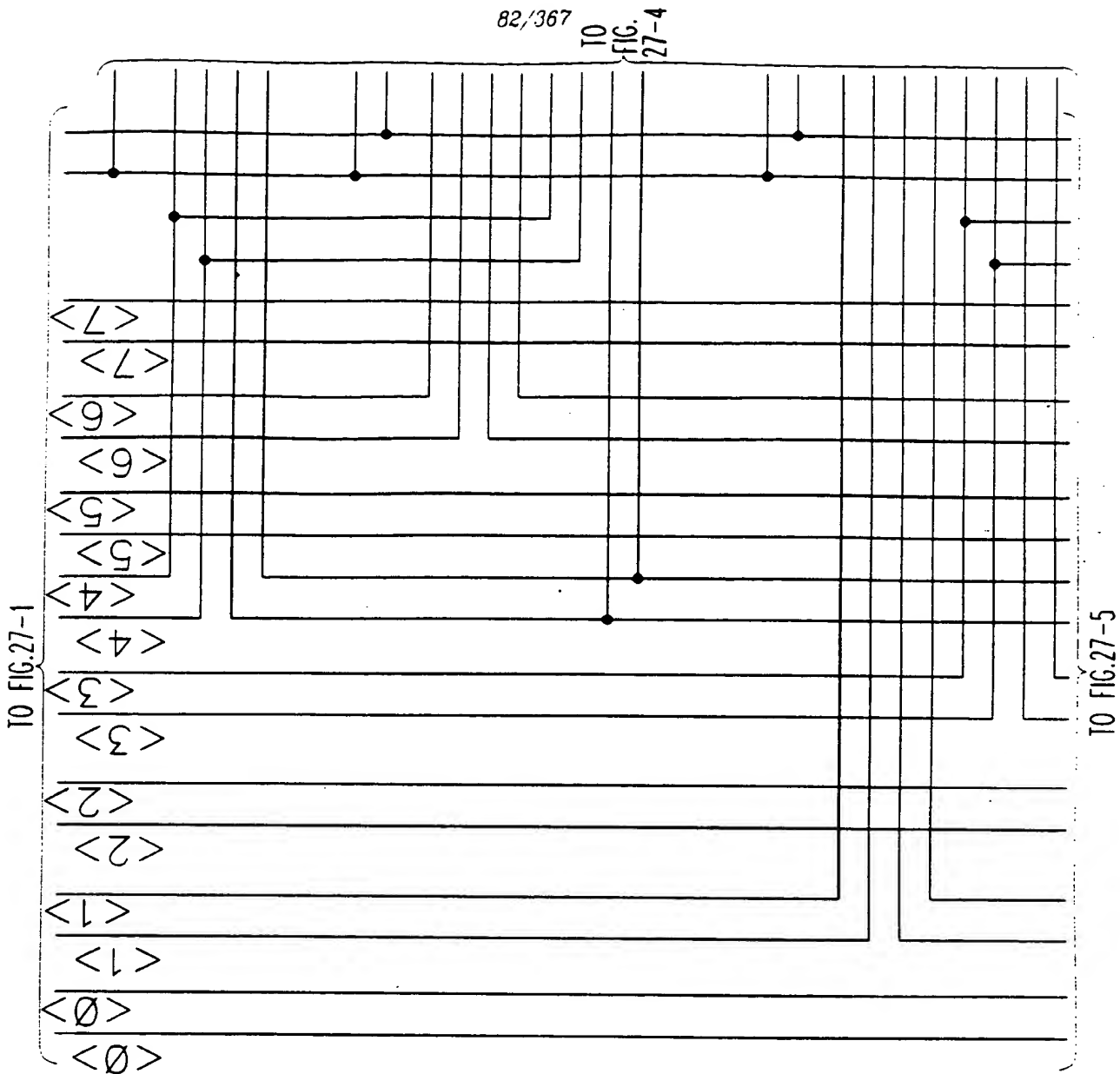


FIG. 27-3

FIG. 27-3

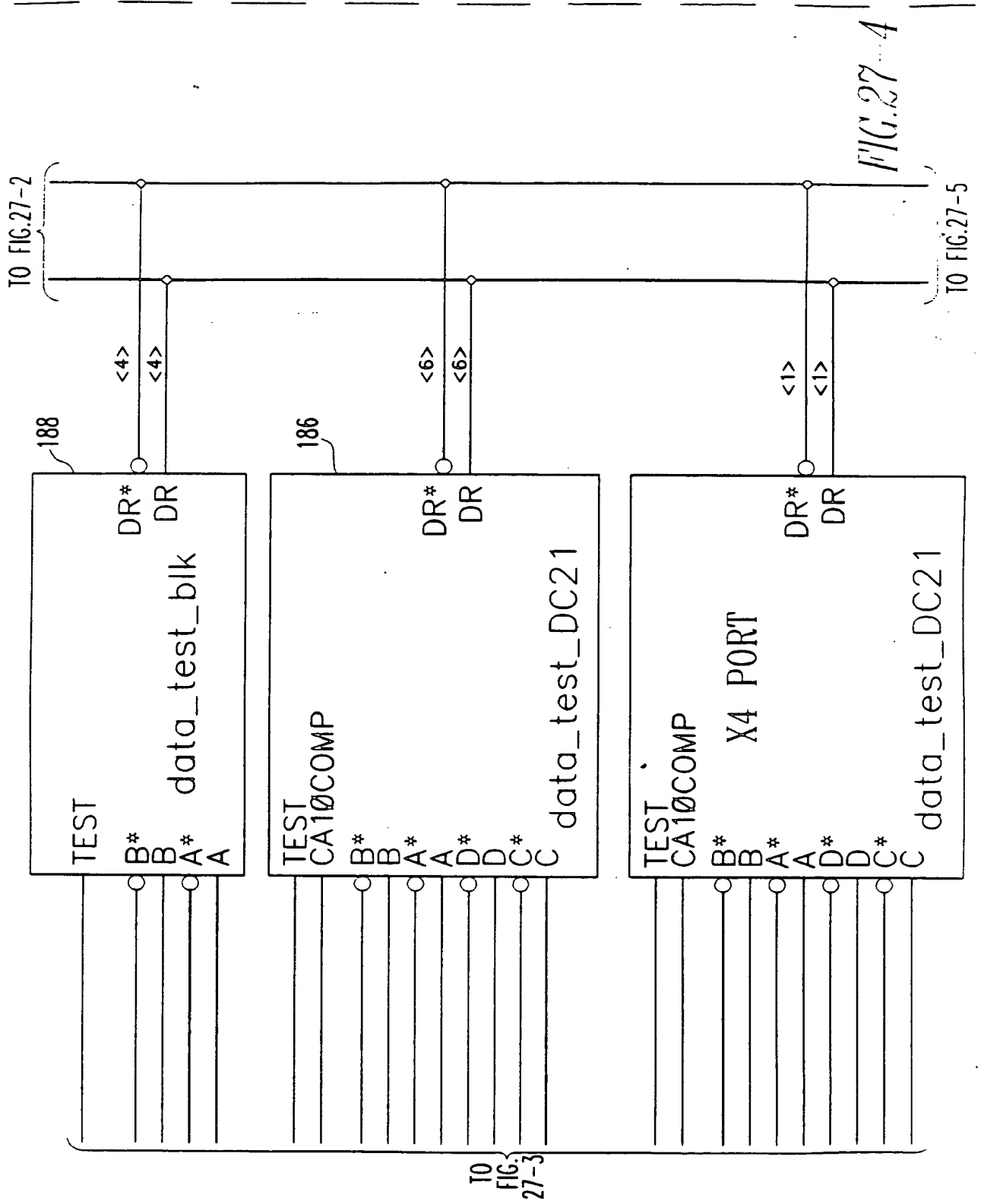
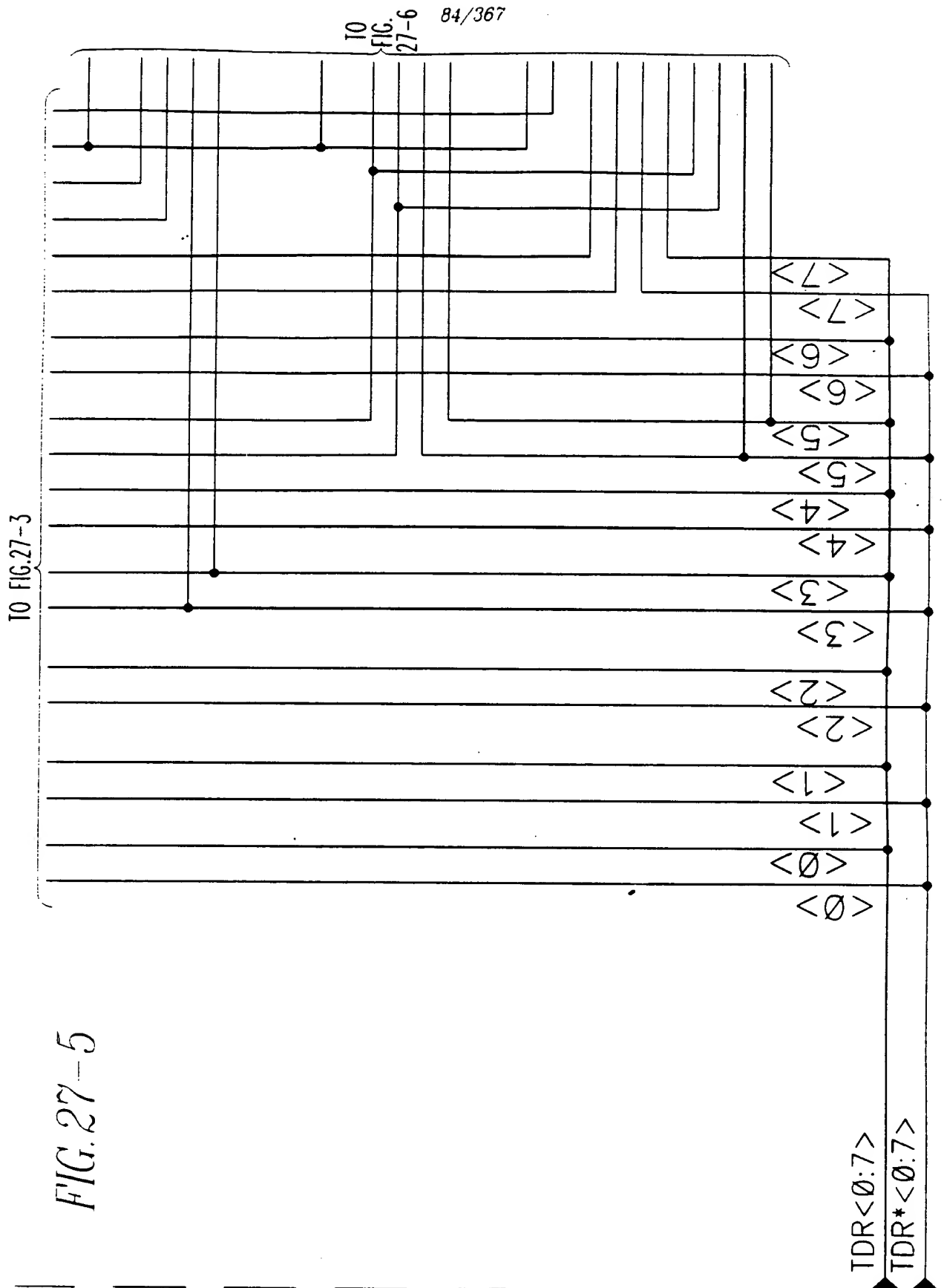


FIG. 27-5



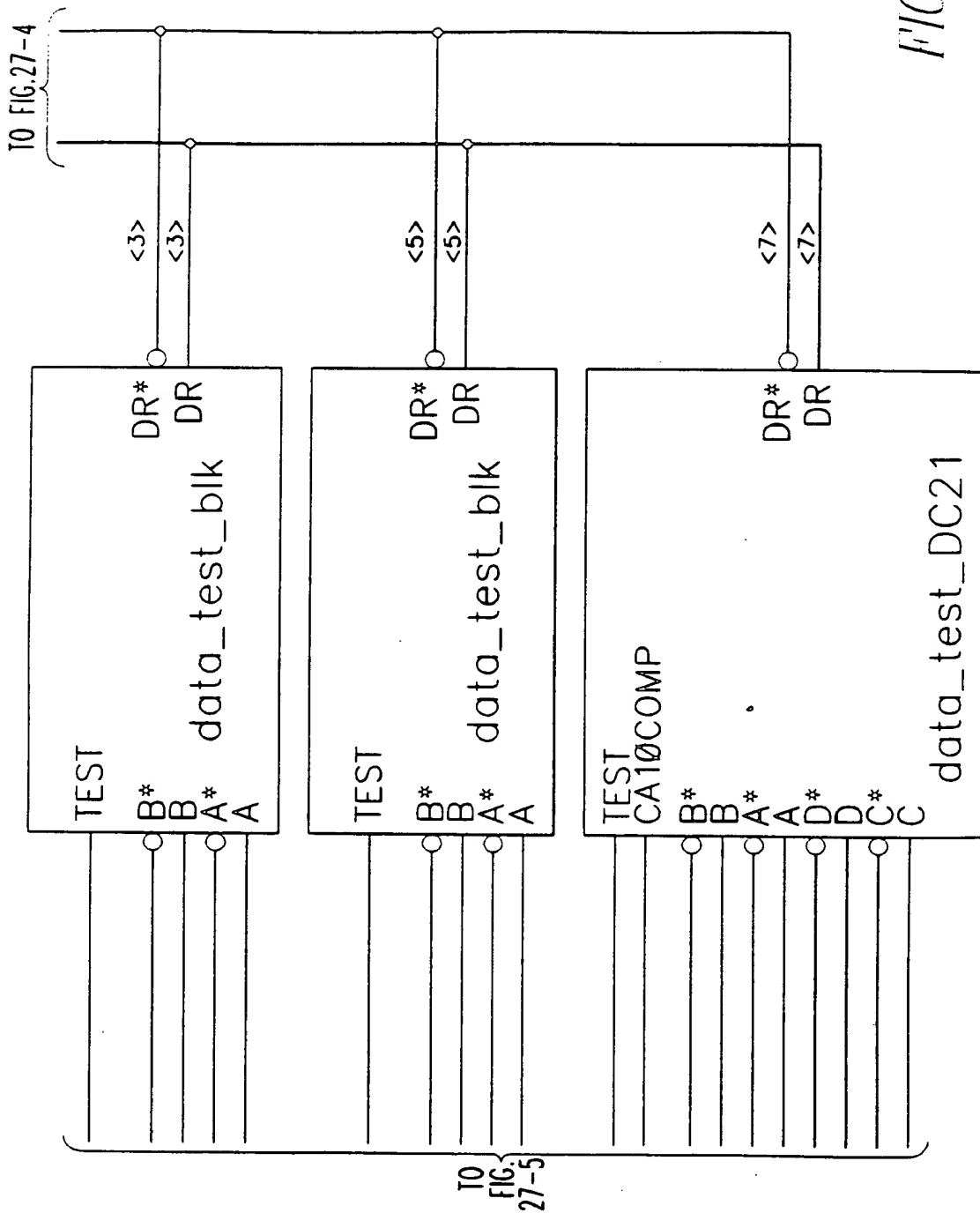


FIG. 27-6

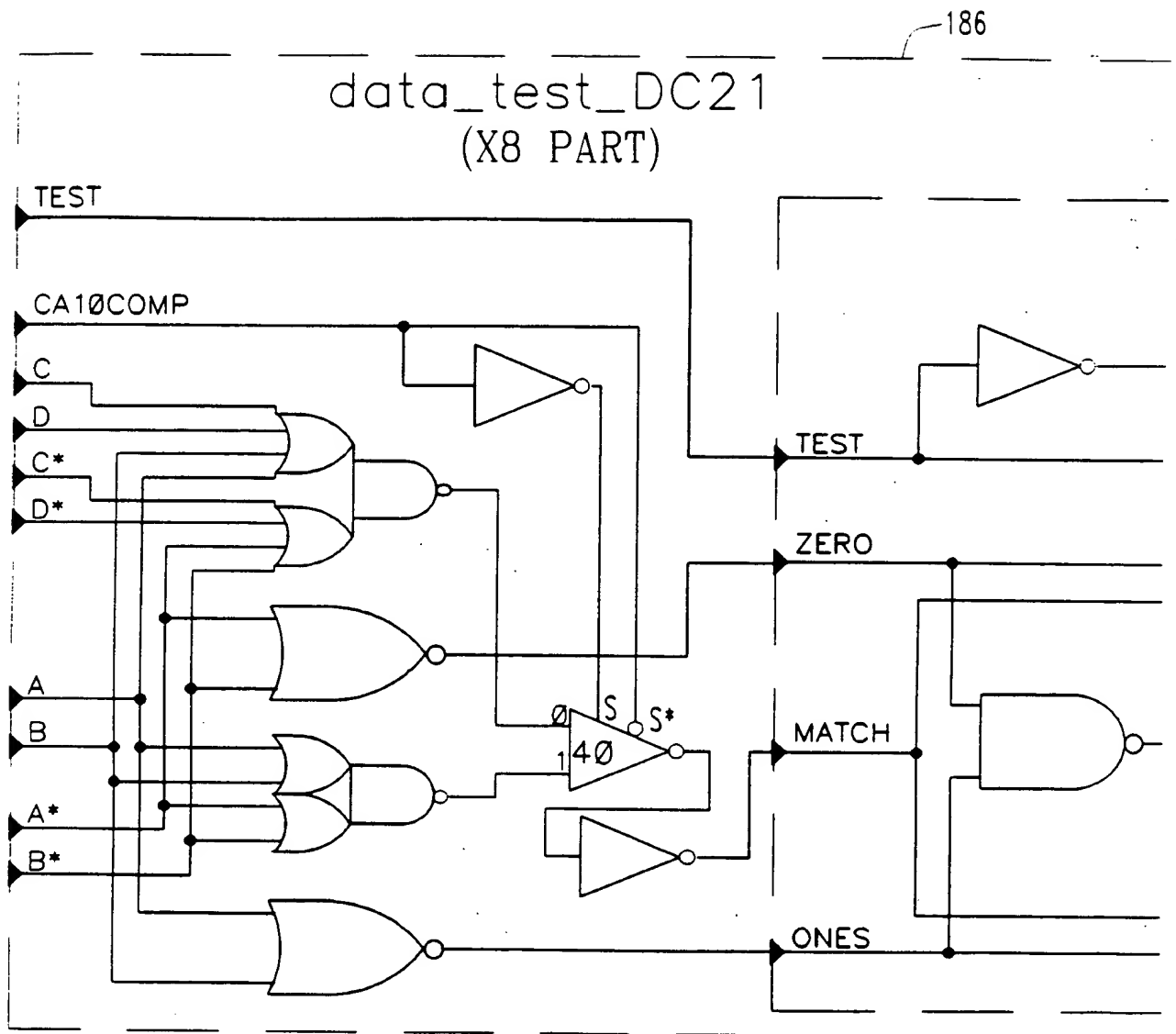


FIG. 28-1

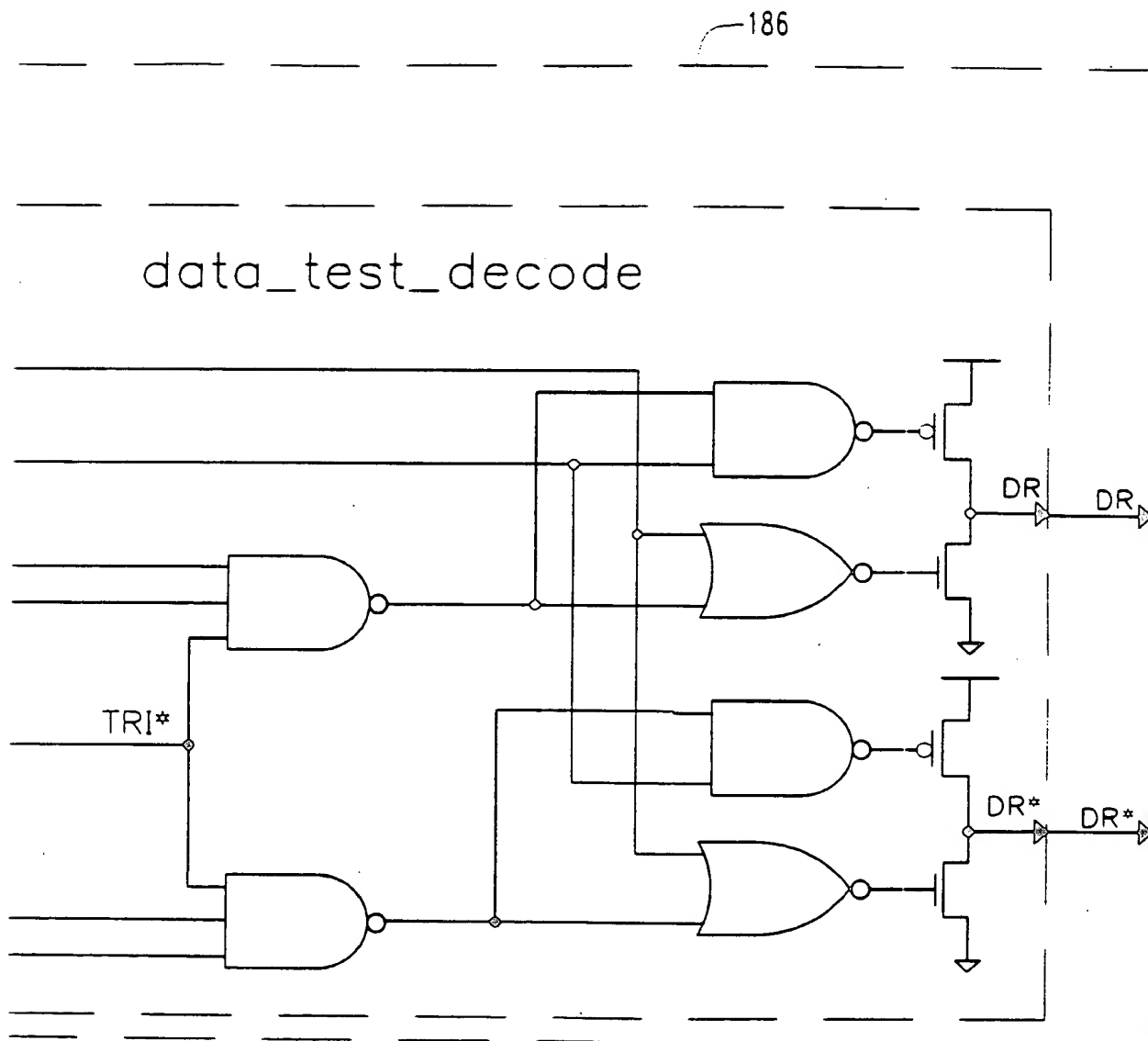


FIG. 28-2

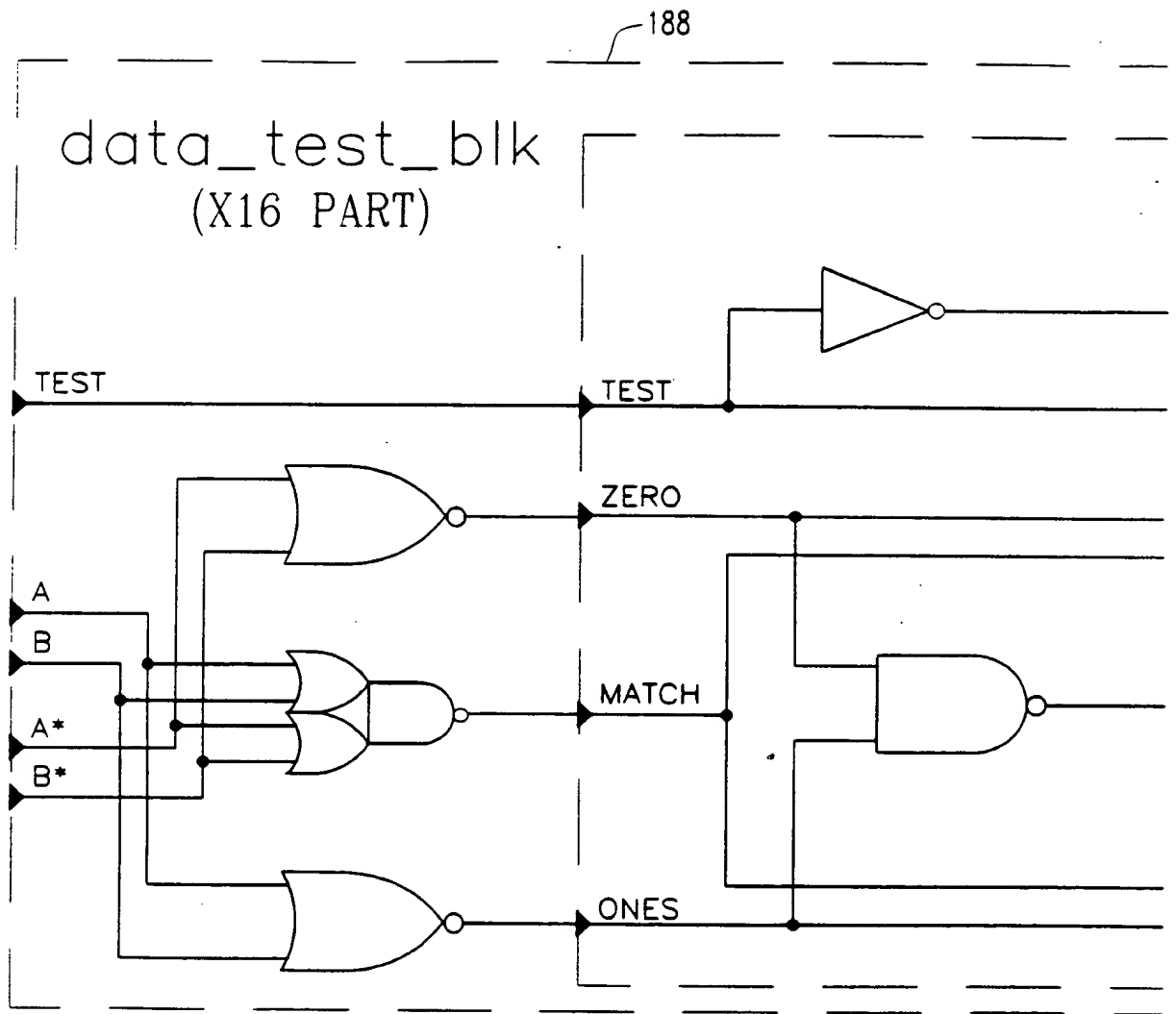


FIG. 29-1

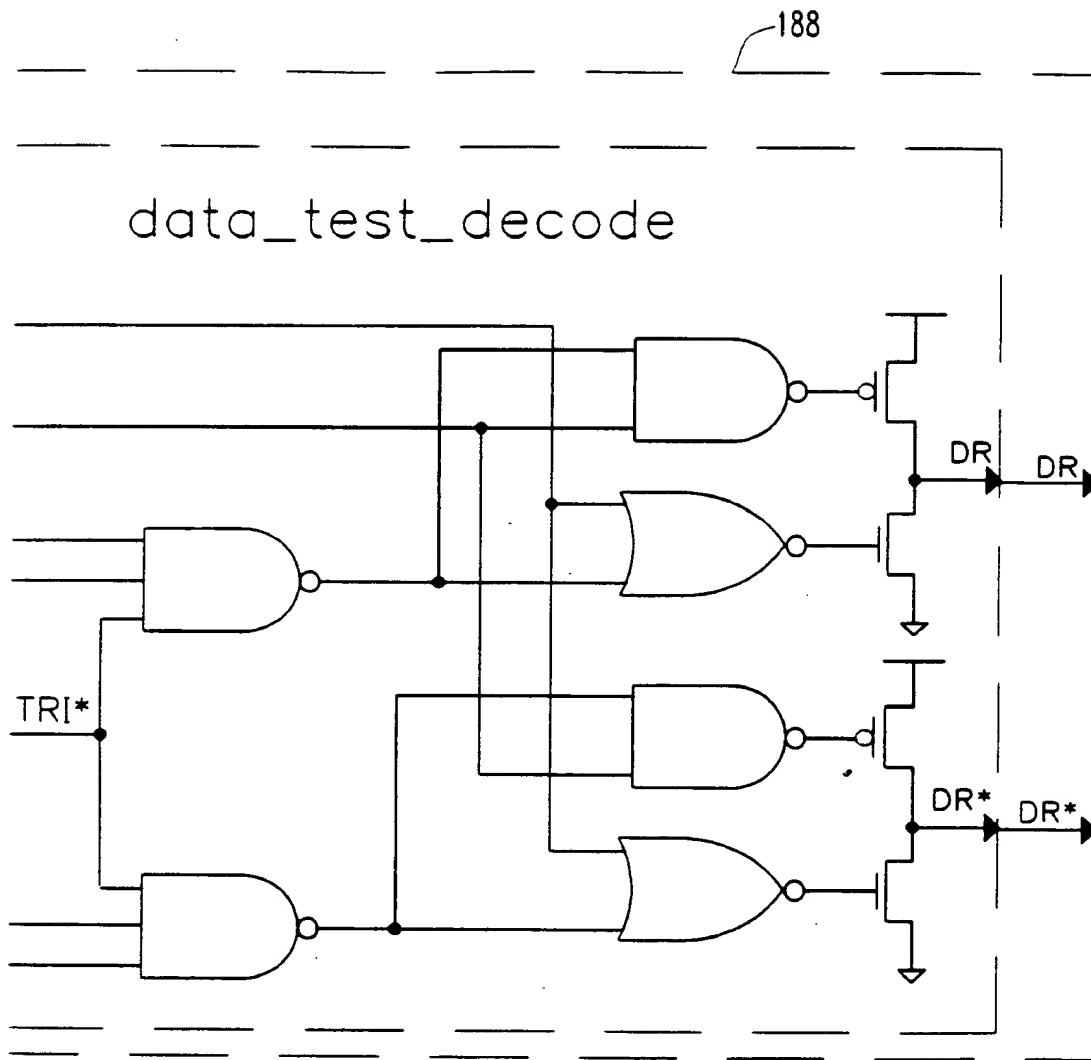


FIG. 29-2

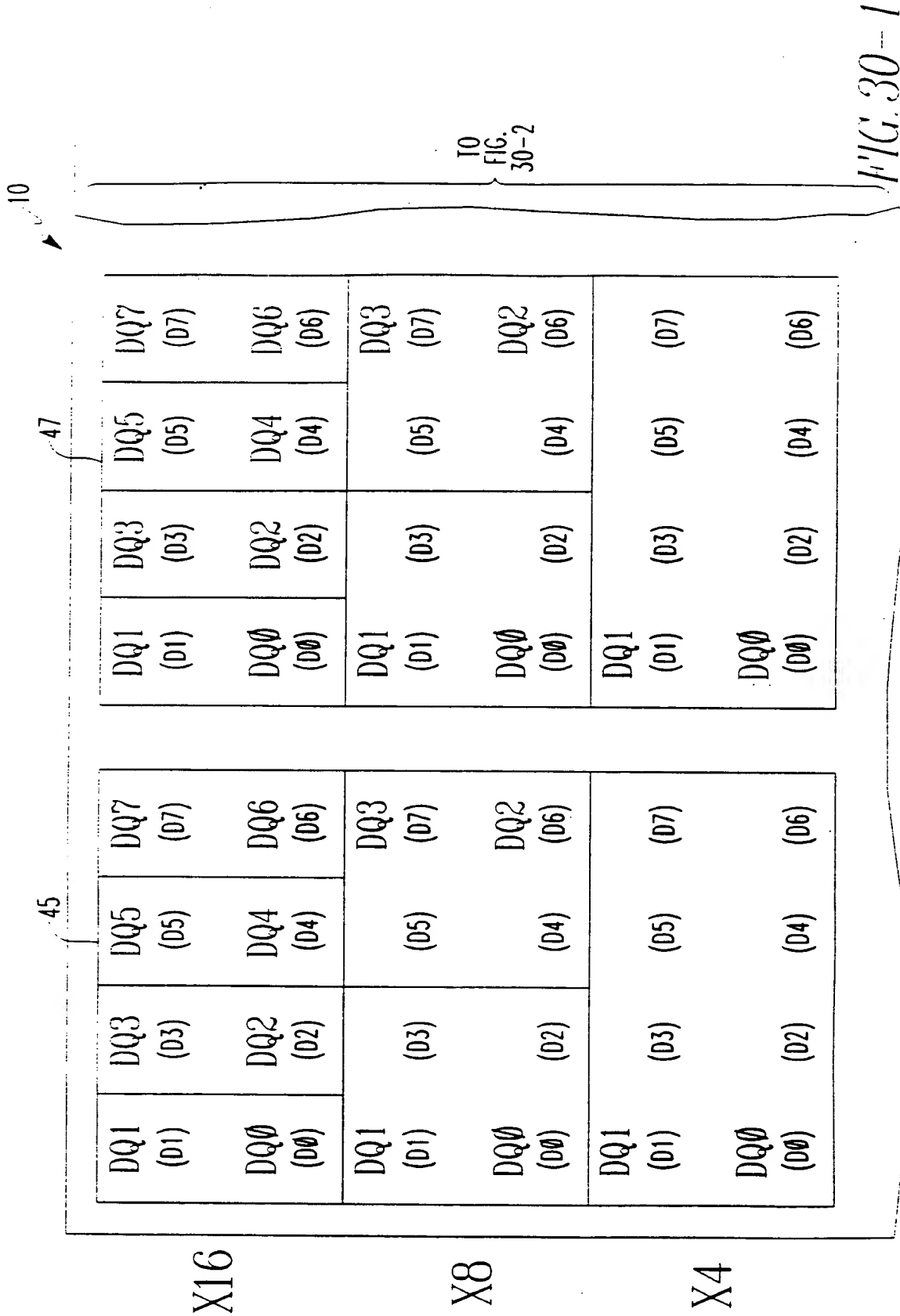


FIG. 30-3

TO
FIG.
30-1

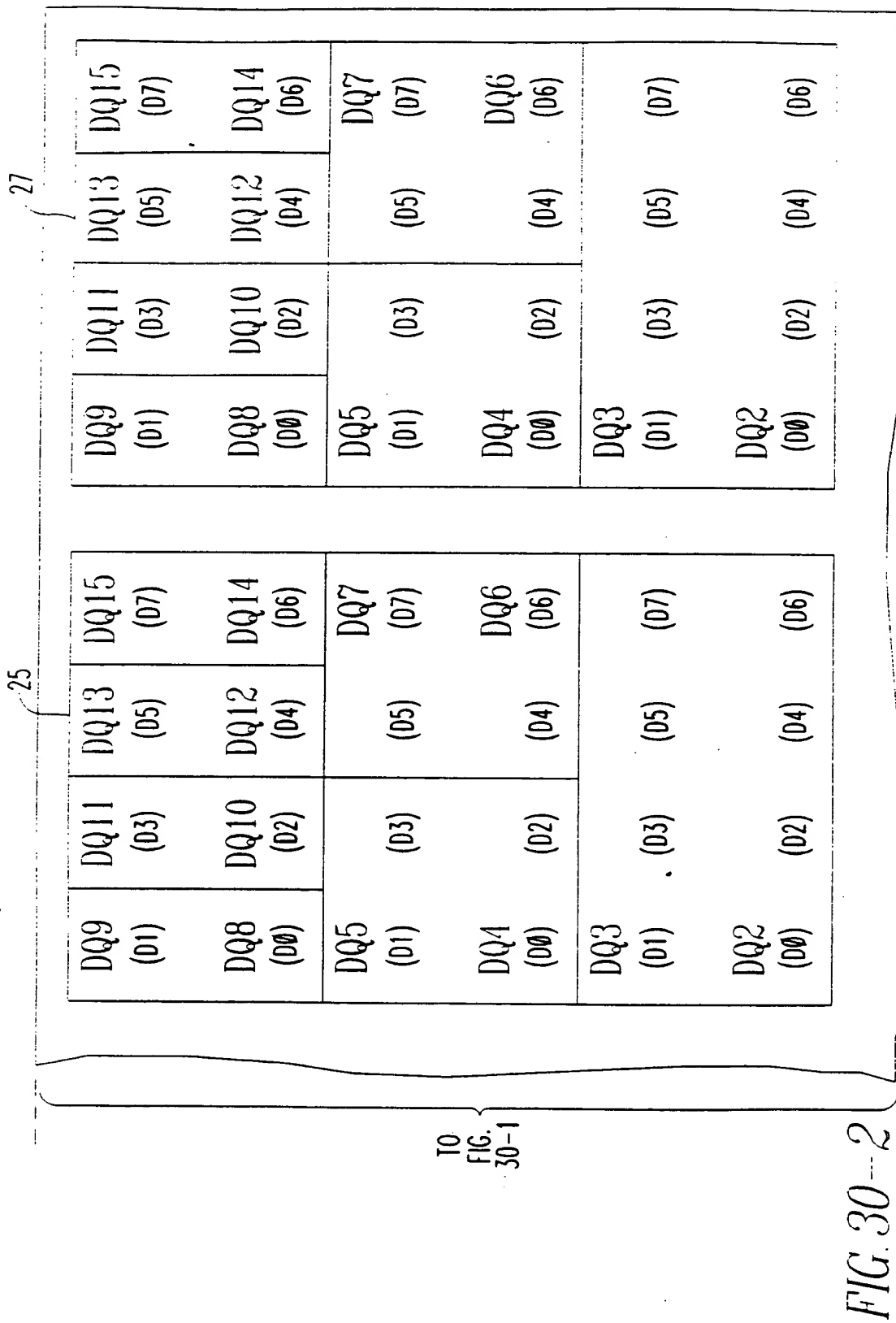


FIG. 30-4

TO FIG. 30-1

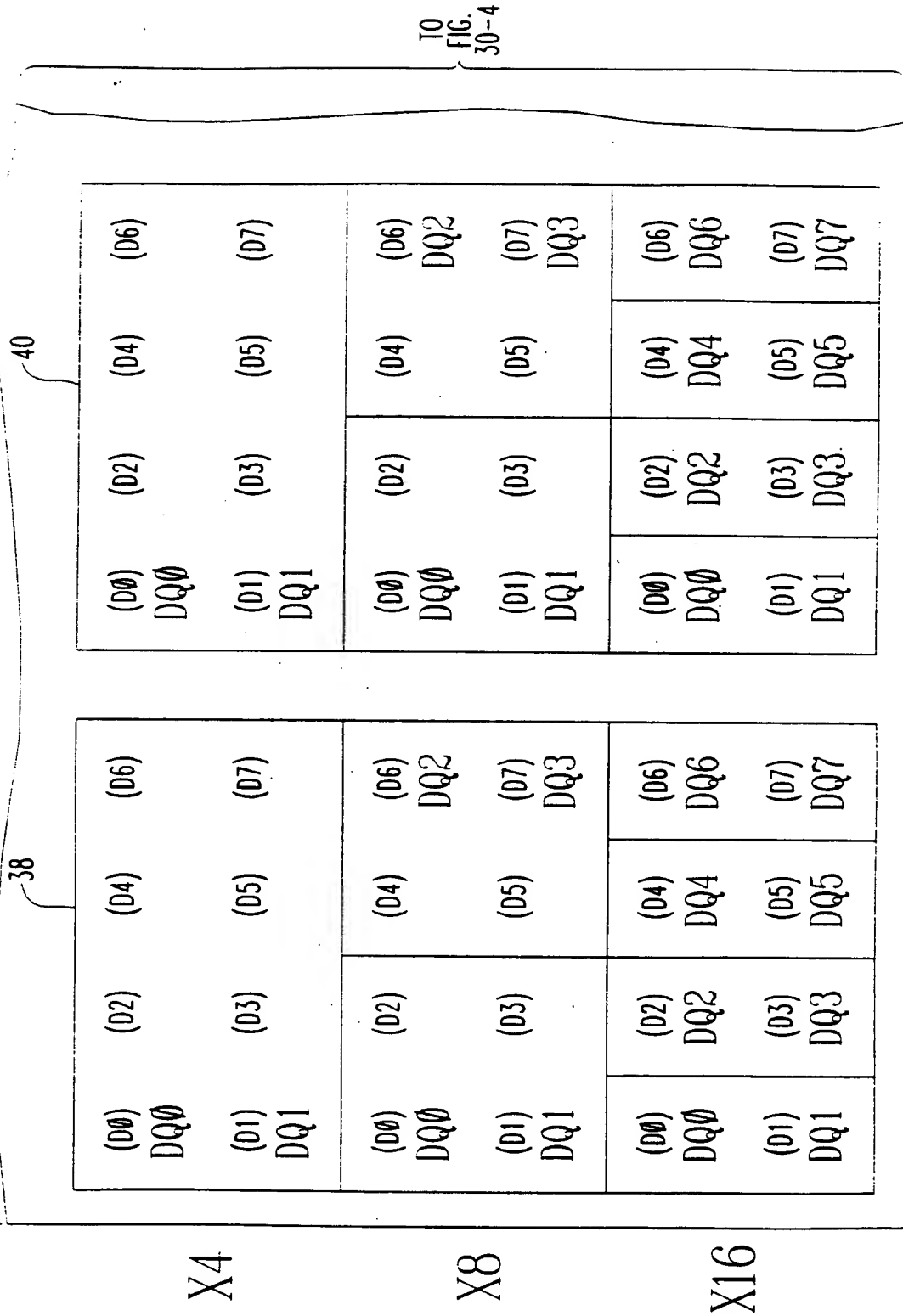


FIG. 30-2

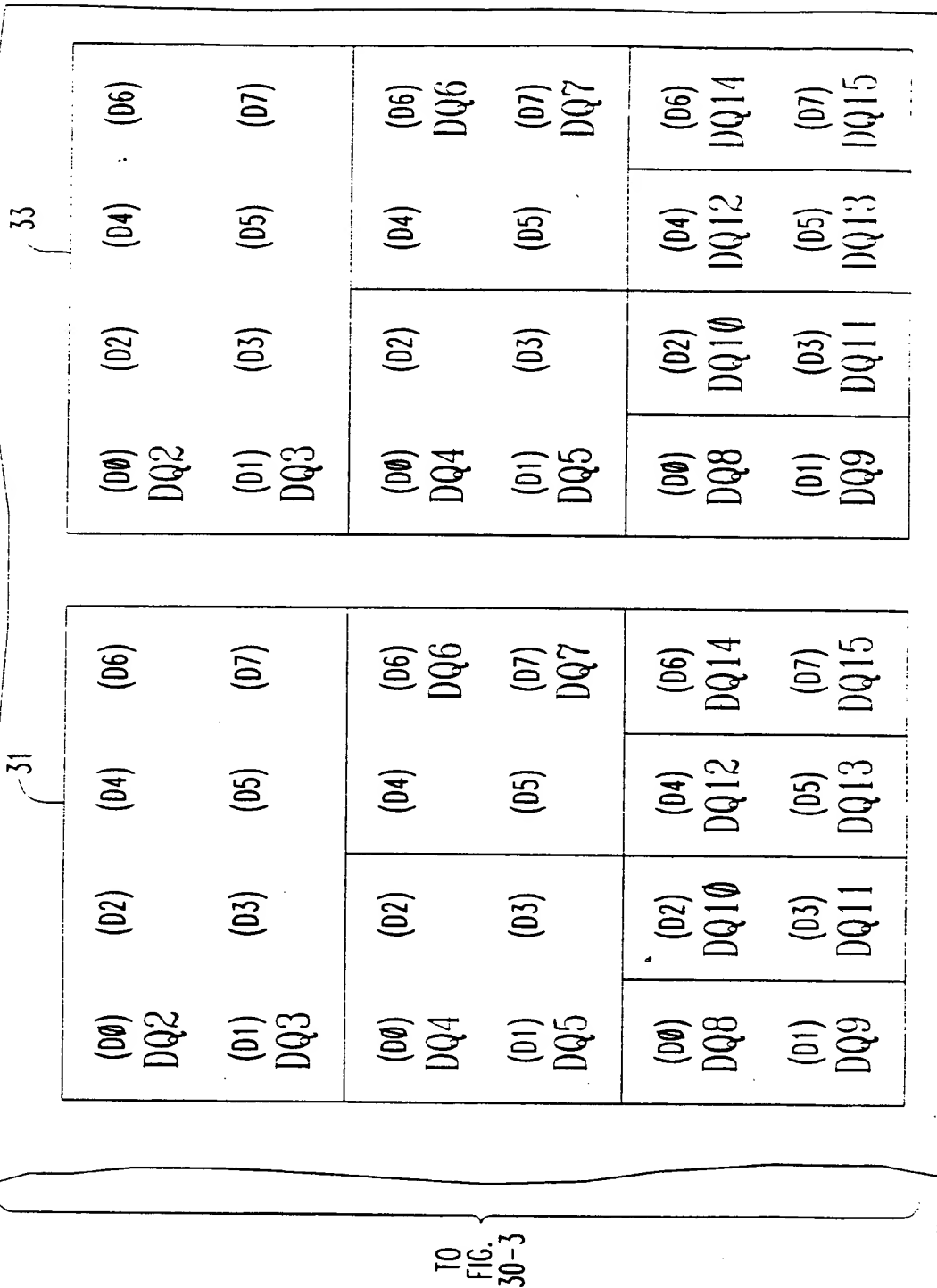


FIG. 30-4

X4	VSSQ	N/C	VSSQ	N/C	N/C						
X8	VSSQ	DQ3	VSSQ	DQ2	N/C						
X16	VSSQ	DQ7	VSSQ	DQ6	DQ5						
	1	2	3	4	5	6	7	8	9	10	11
X16	VCCQ	DQ0	VCCQ	DQ1	DQ2	VCCQ					
X8	VCCQ	DQ0	VCCQ	DQ1	N/C	N/C					
X4	VCCQ	DQ0	VCCQ	DQ1	N/C	N/C					

TO FIG. 31A2

FIG. 31A1

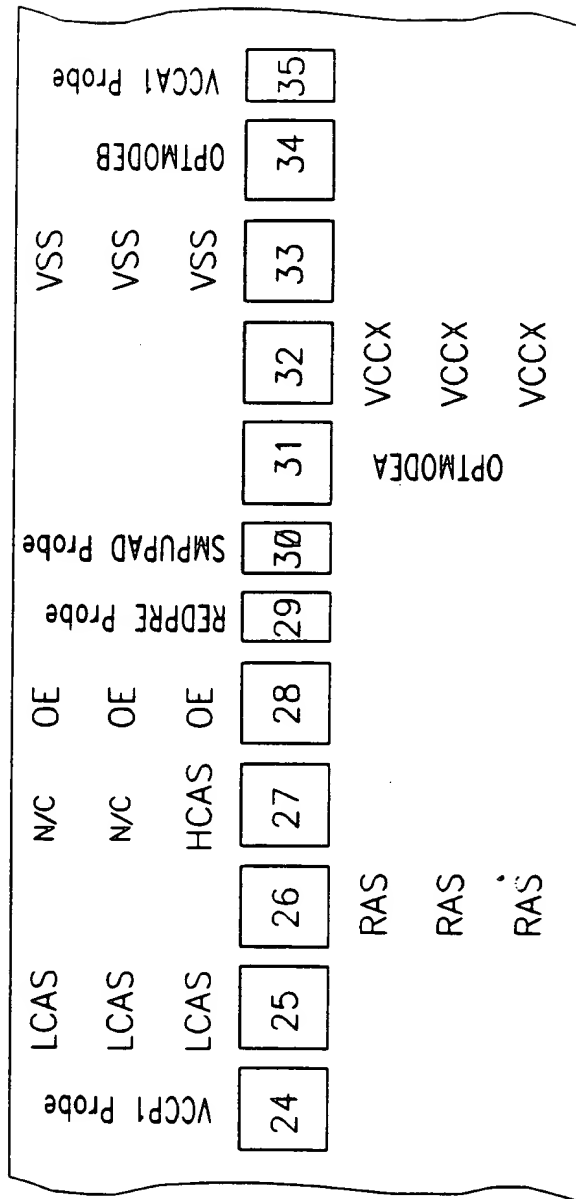


FIG. 31B1

FIG. 31B2

TO FIG. 31B1

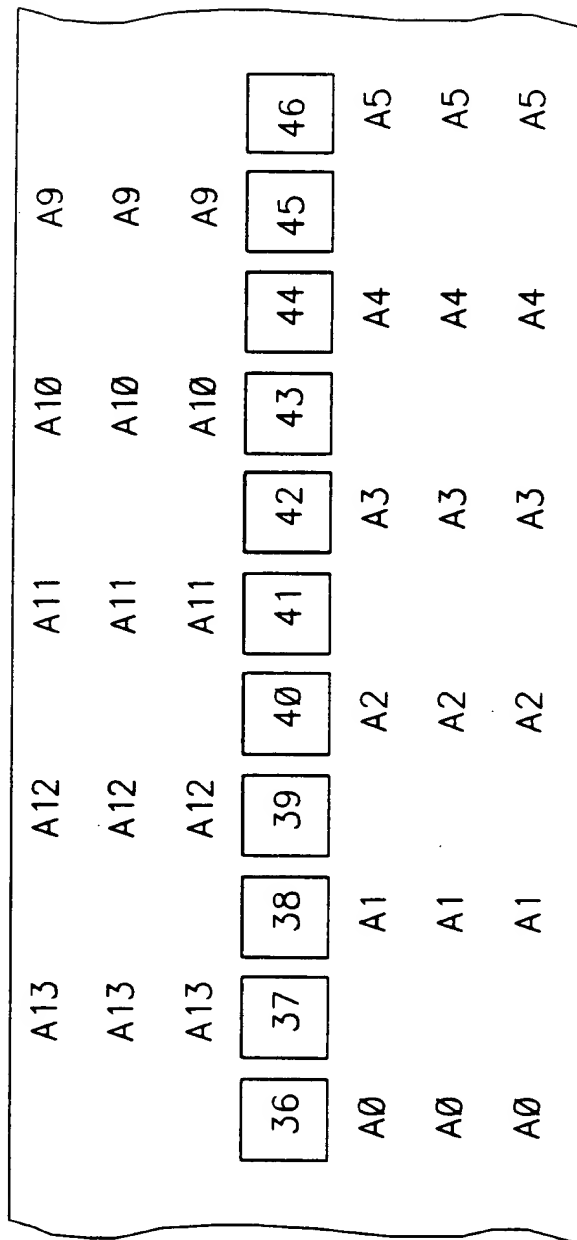


FIG. 31B2

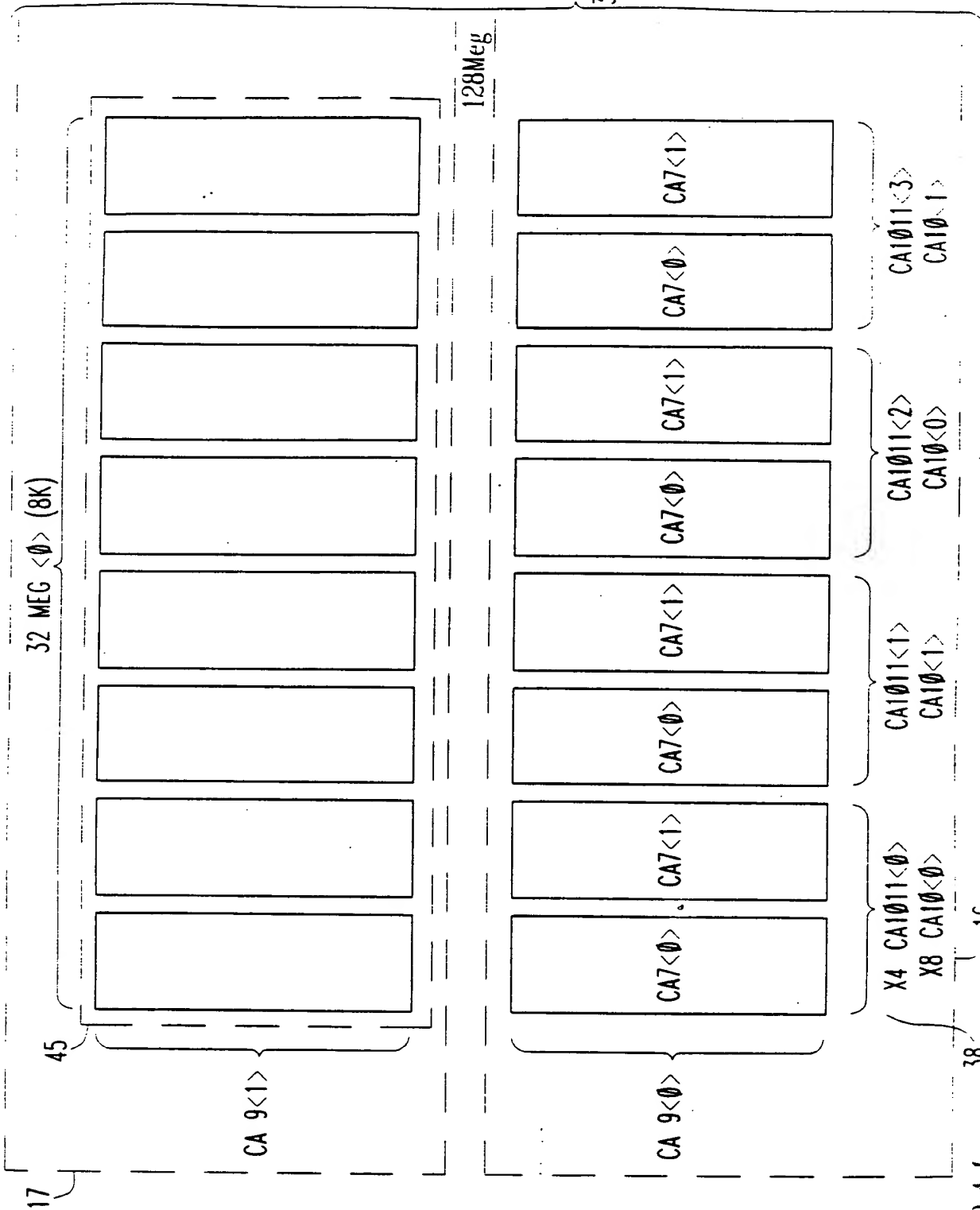
TO FIG. 31C2

FIG. 31C1

TO FIG. 31C1

N/C	N/C	N/C	VSSQ	N/C	VSSQ	VBB
N/C	DQ6	N/C	VSSQ	N/C	VSSQ	VBB
VSSQ	DQ14	DQ13	VSSQ	DQ12	VSSQ	VBB
60	61	62	63	64	65	66
DQ9	DQ10	VCCQ	DQ11	VCCQ	VBB	
DQ5	N/C	VCCQ	N/C	VCCQ	VBB	
DQ3	N/C	VCCQ	N/C	VCCQ	VBB	

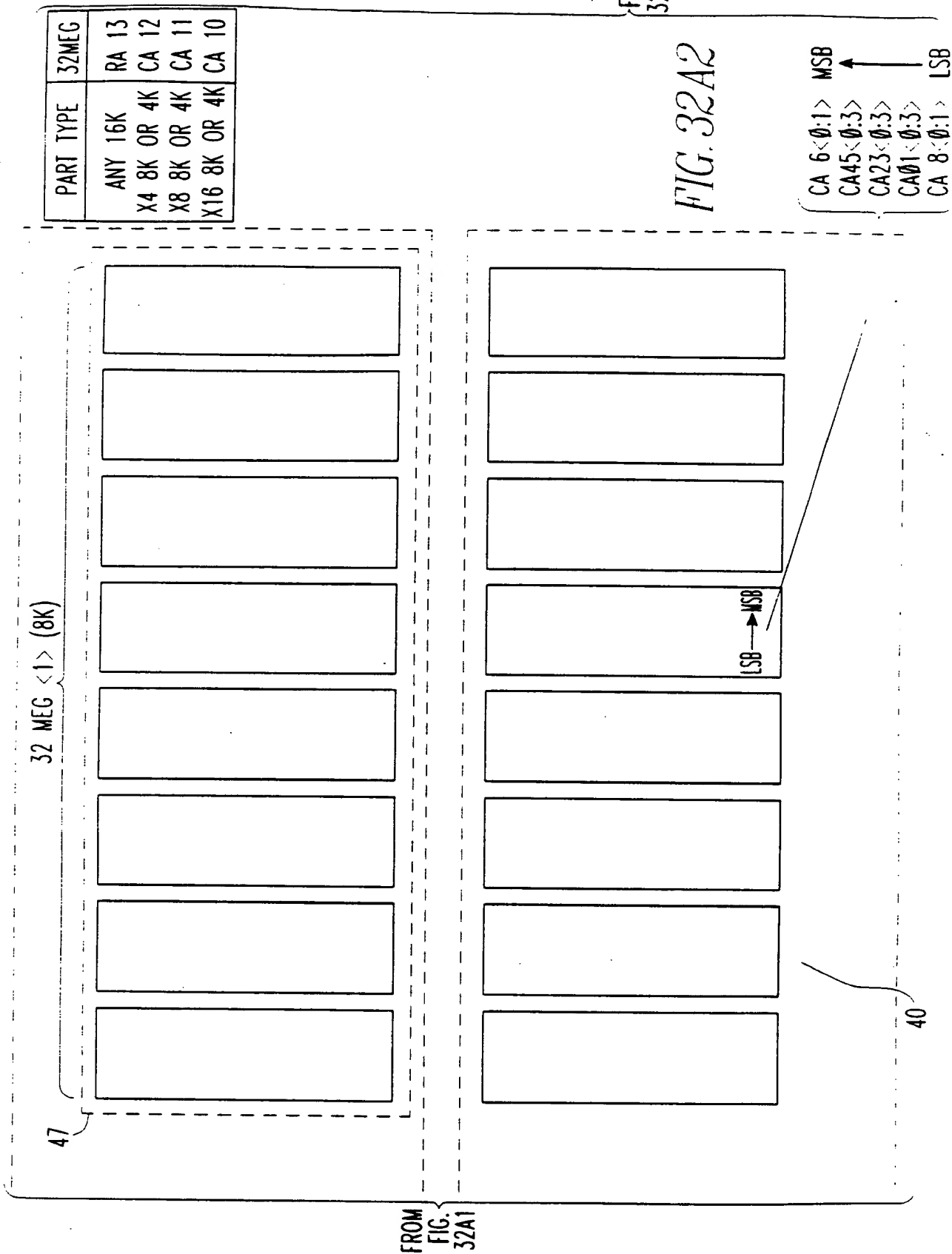
FIG. 31C2



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TO
FIG.
32A2

FIG. 32A1



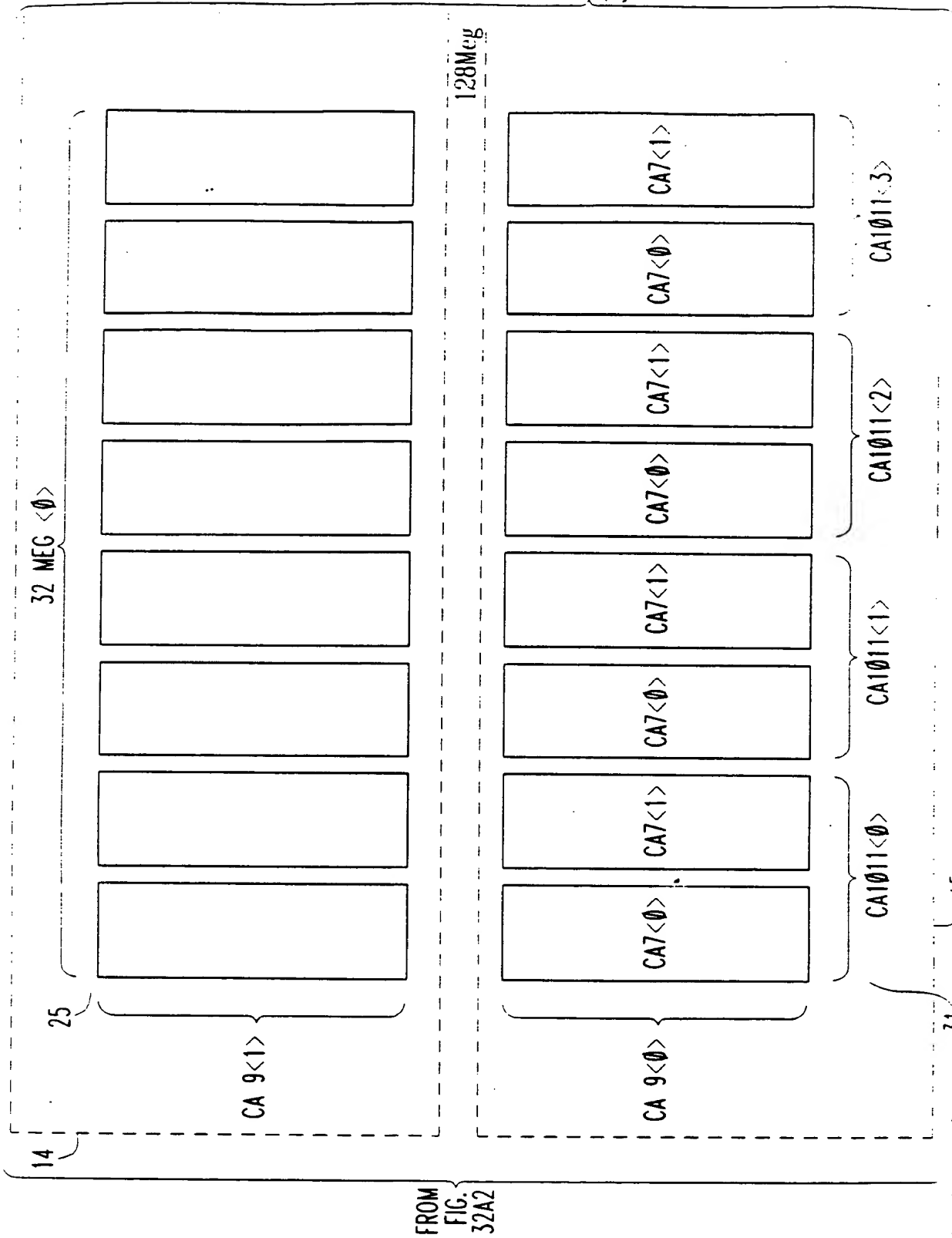
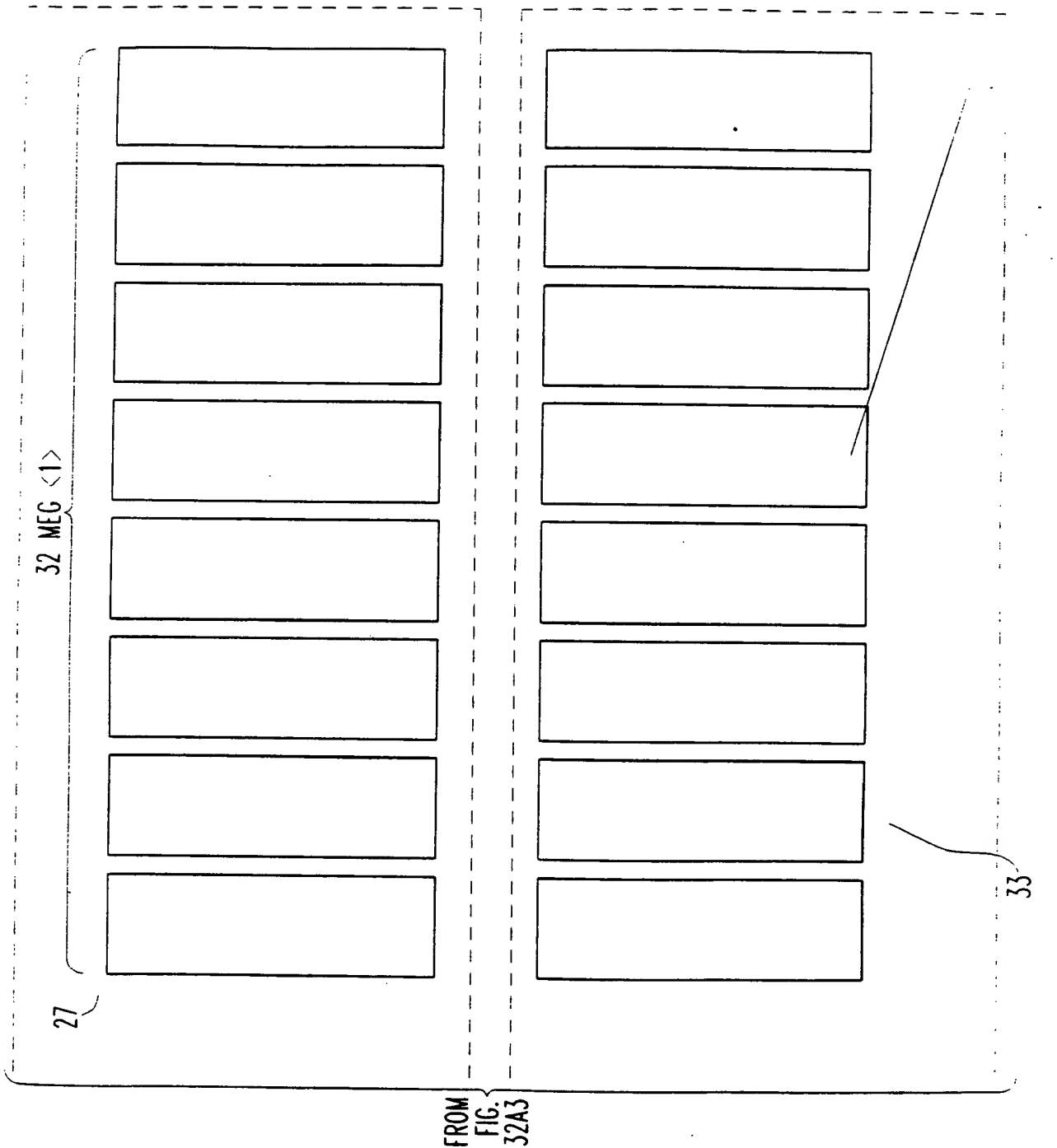


FIG. 32A3



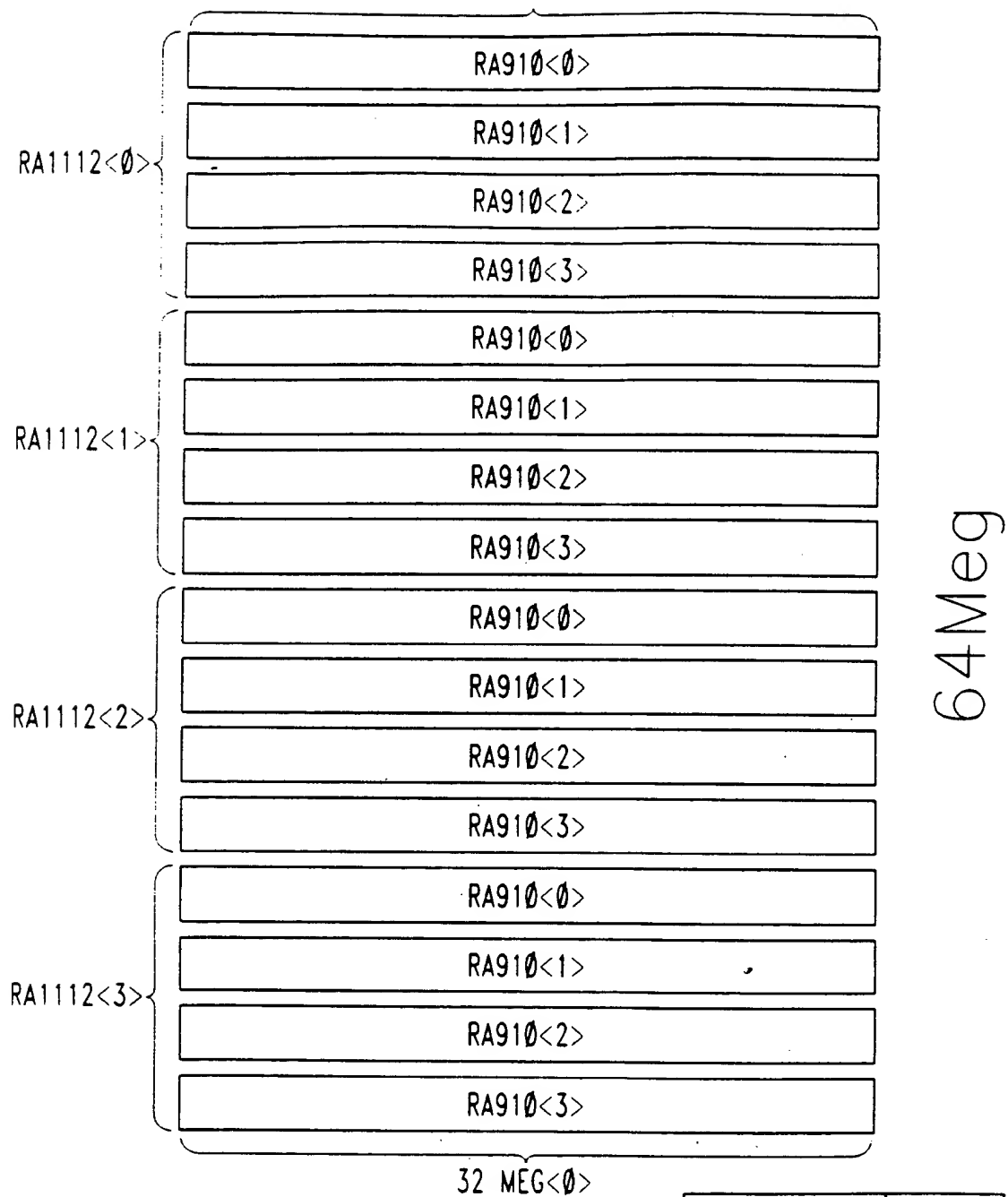


FIG. 32B1

PART TYPE	32MEG
ANY 16K	RA_13
X4 8K OR 4K	CA_12
X8 8K OR 4K	CA_11
X16 8K OR 4K	CA_10

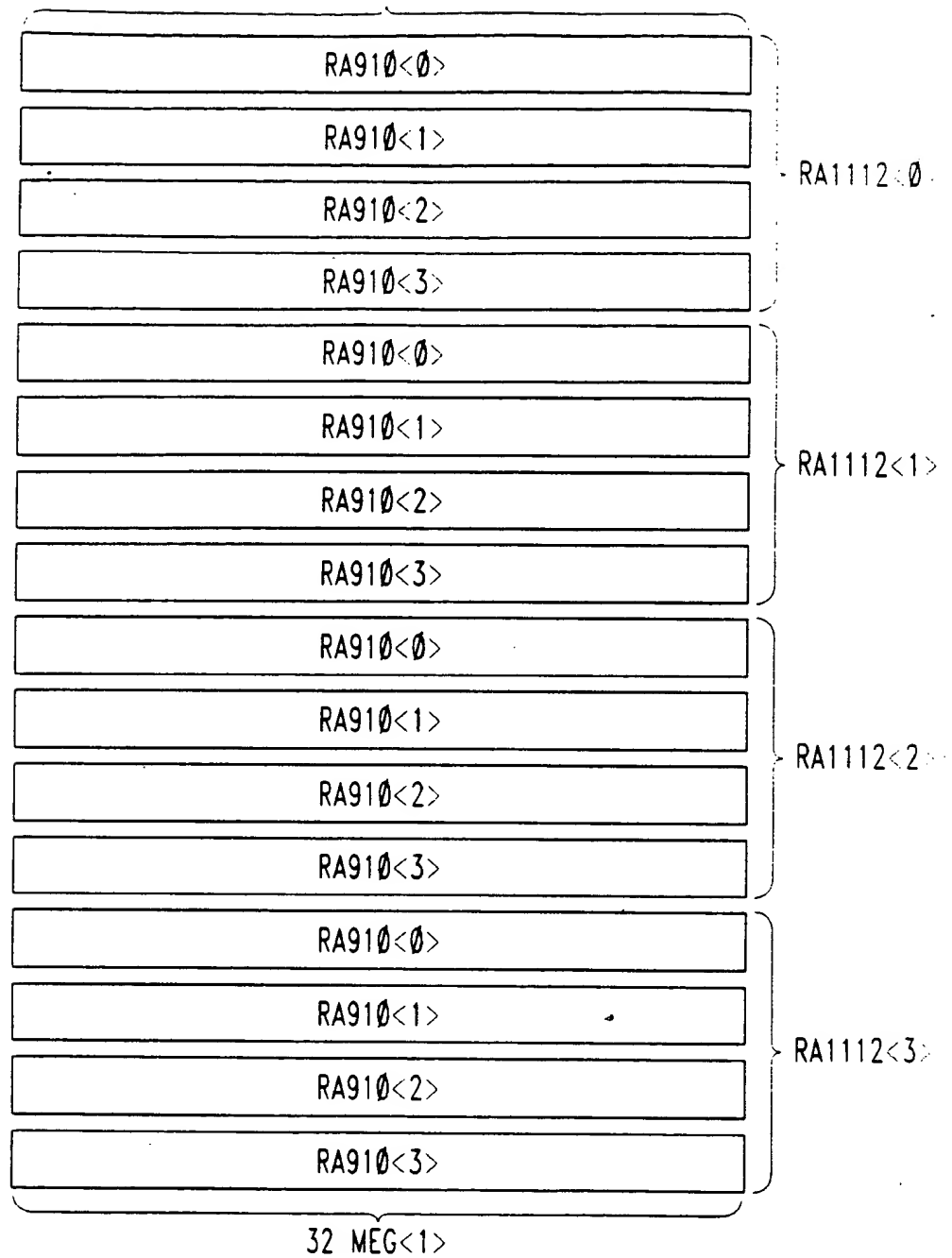


FIG. 32B2

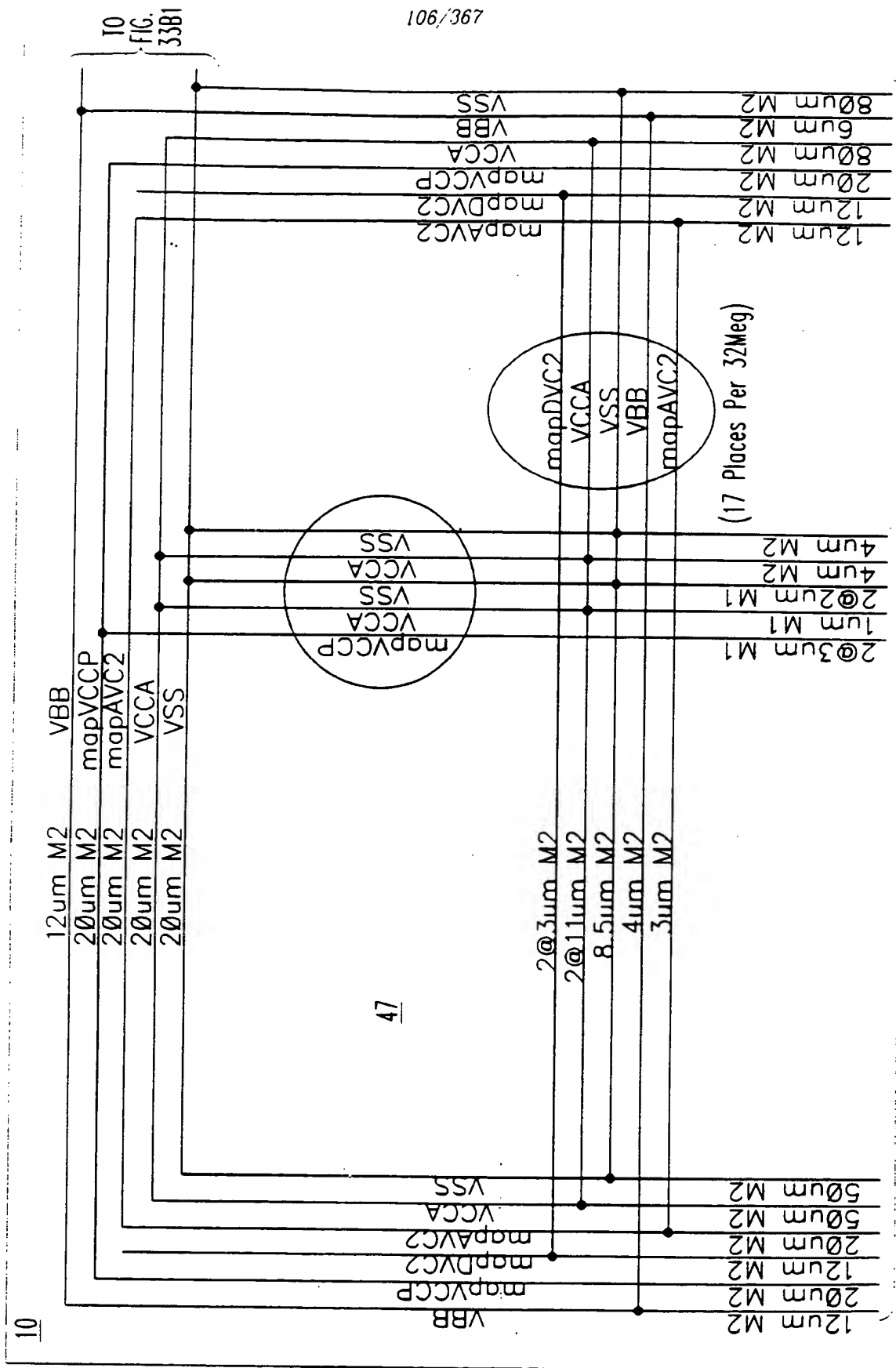
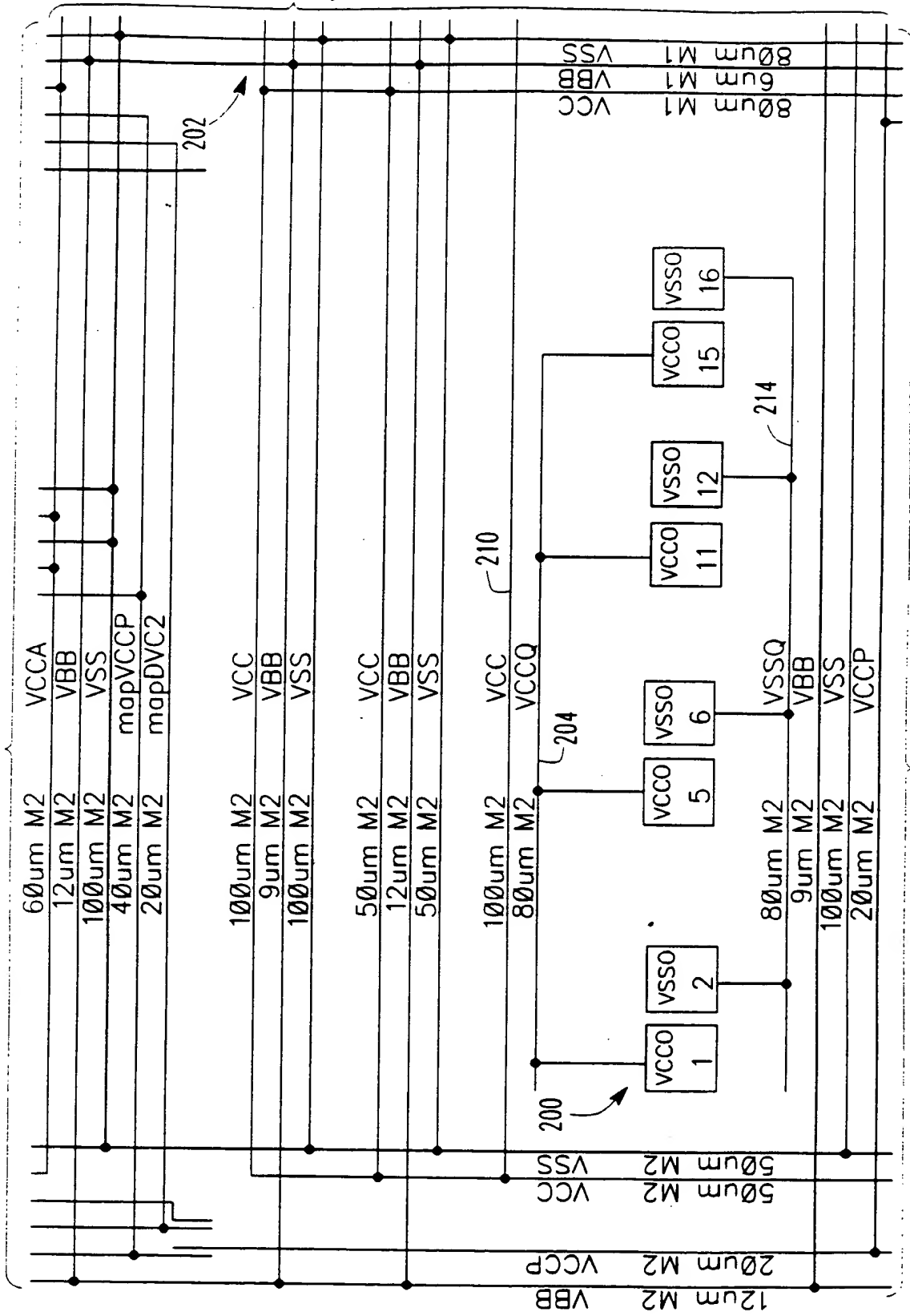


FIG. 33A2

FIG. 33A1

FROM FIG. 33A1

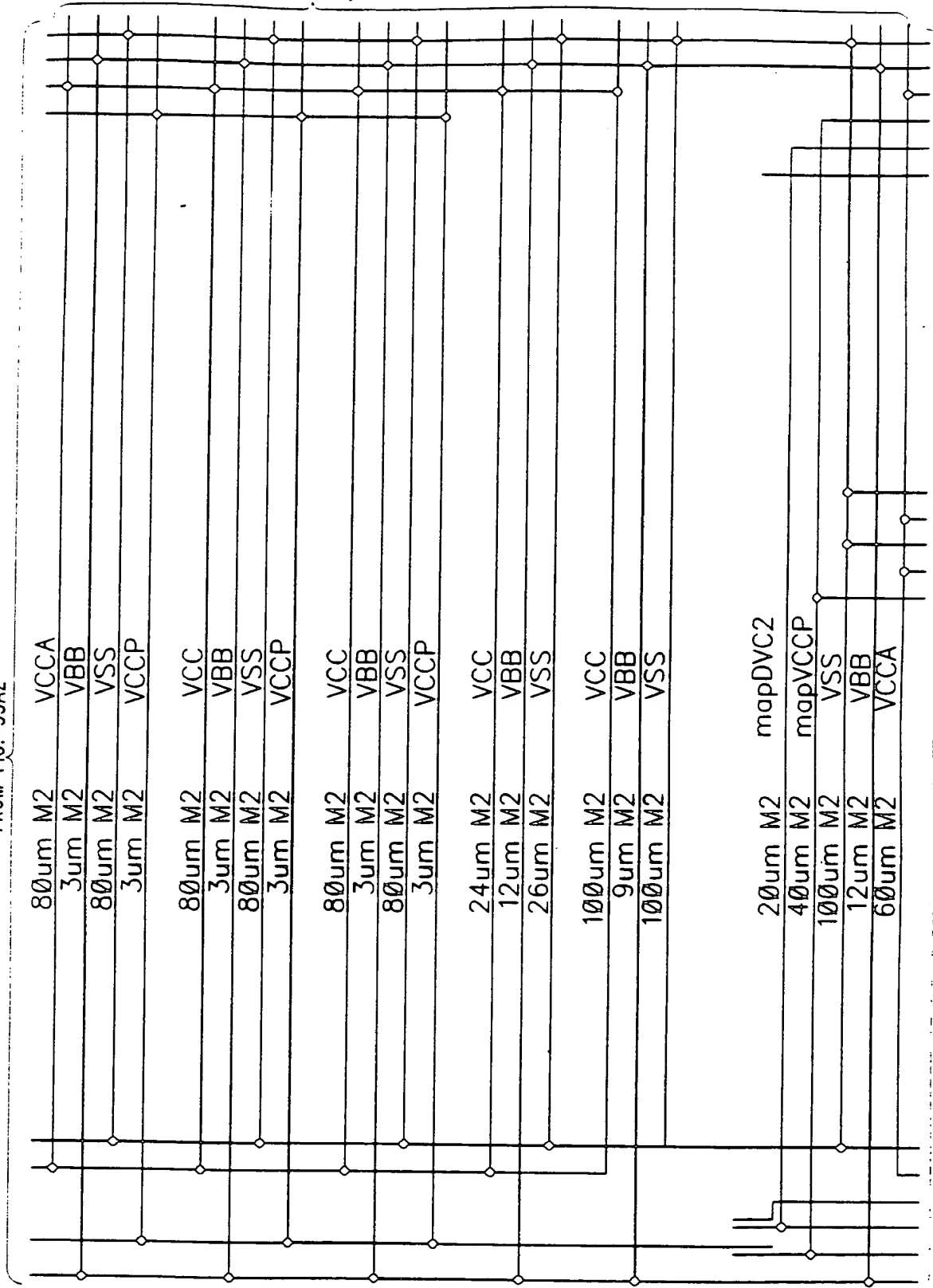


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FIG. 33B3

TO FIG. 33A3

FIG. 33A2

FROM FIG. 33A2



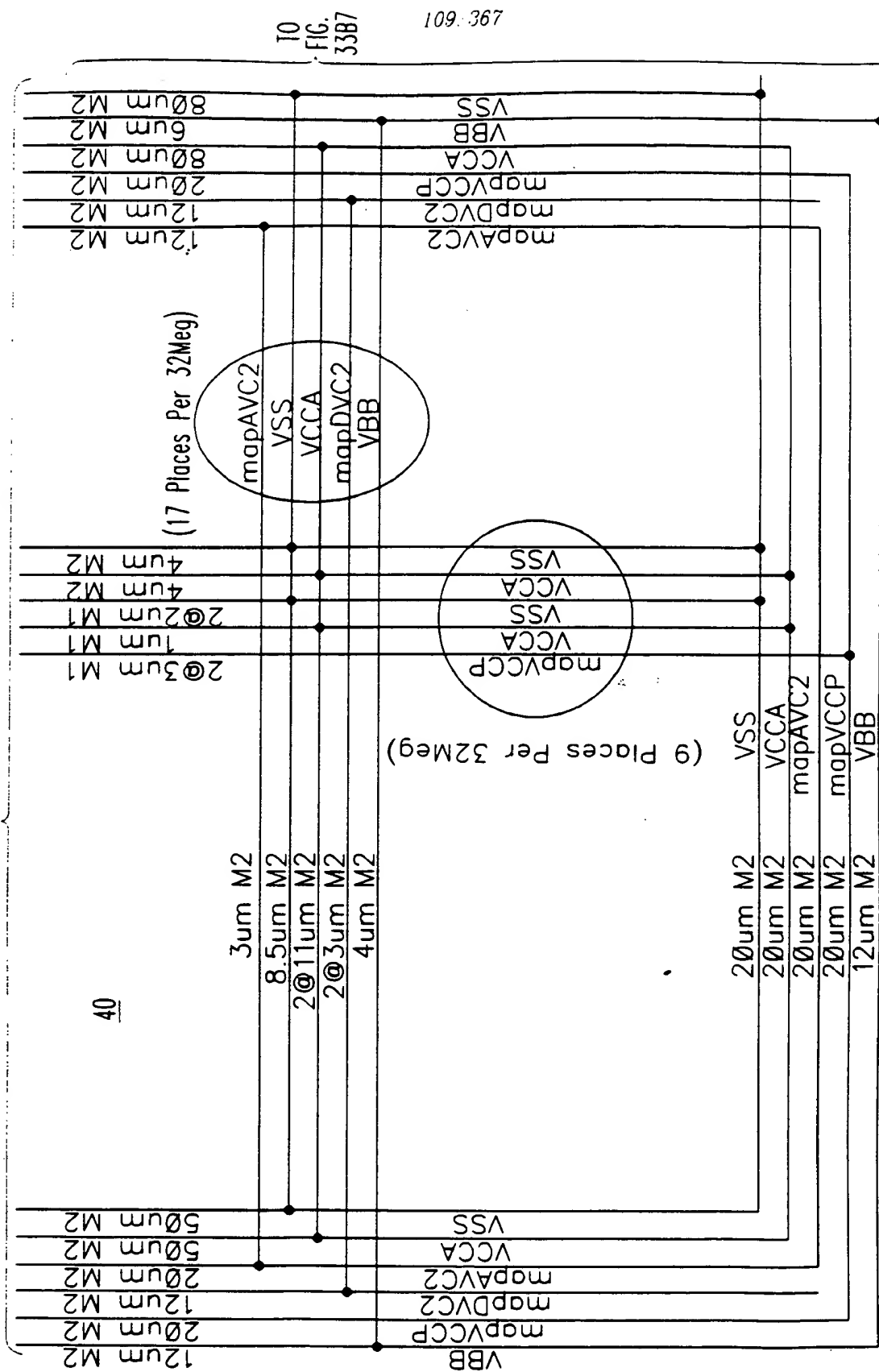
TO
FIG.
33B5

108, 367

TO FIG. 33A4

FIG. 33A3

FROM FIG. 33A3



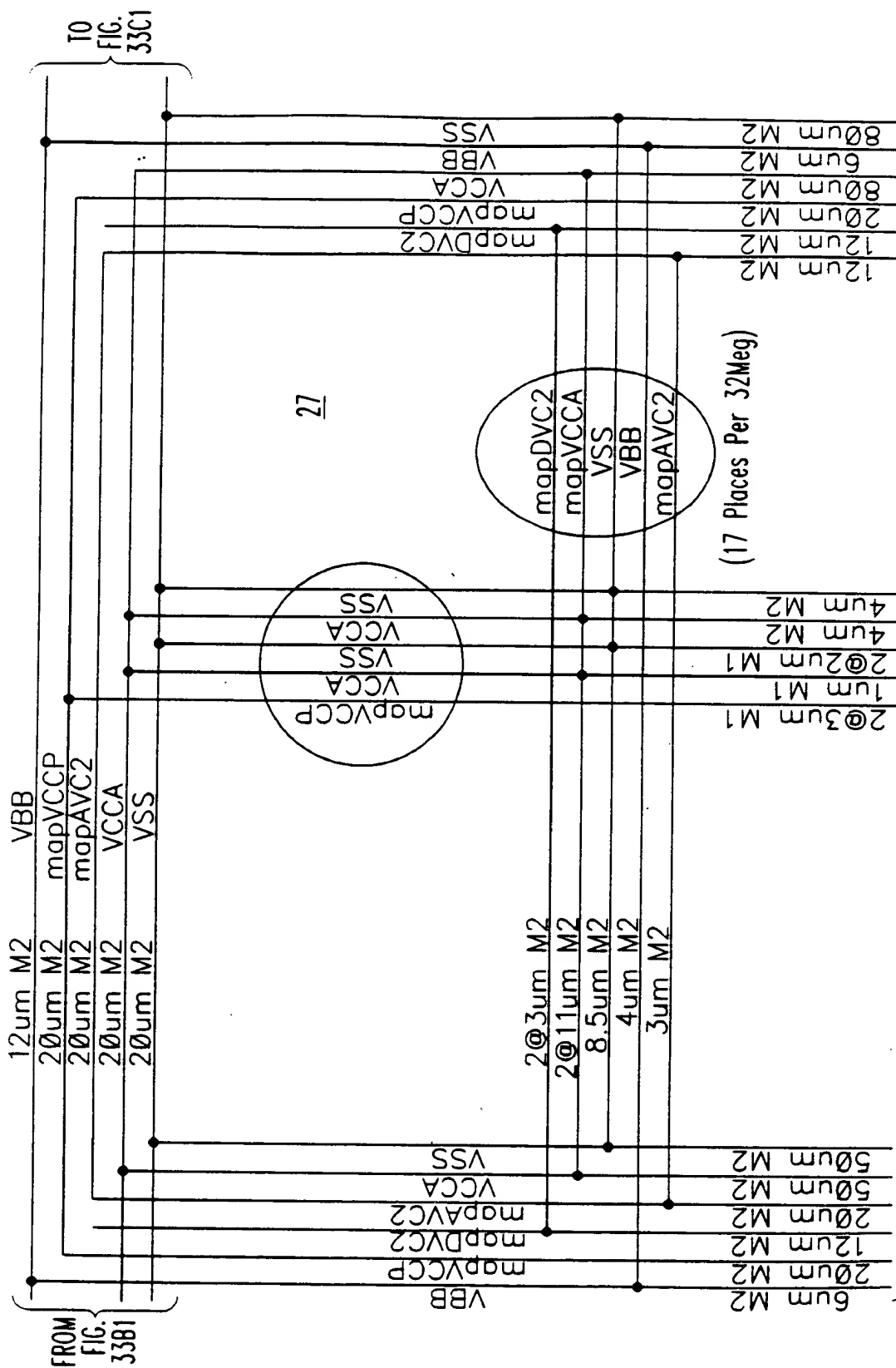
10
FIG.
33B7

109.367

11/13/74



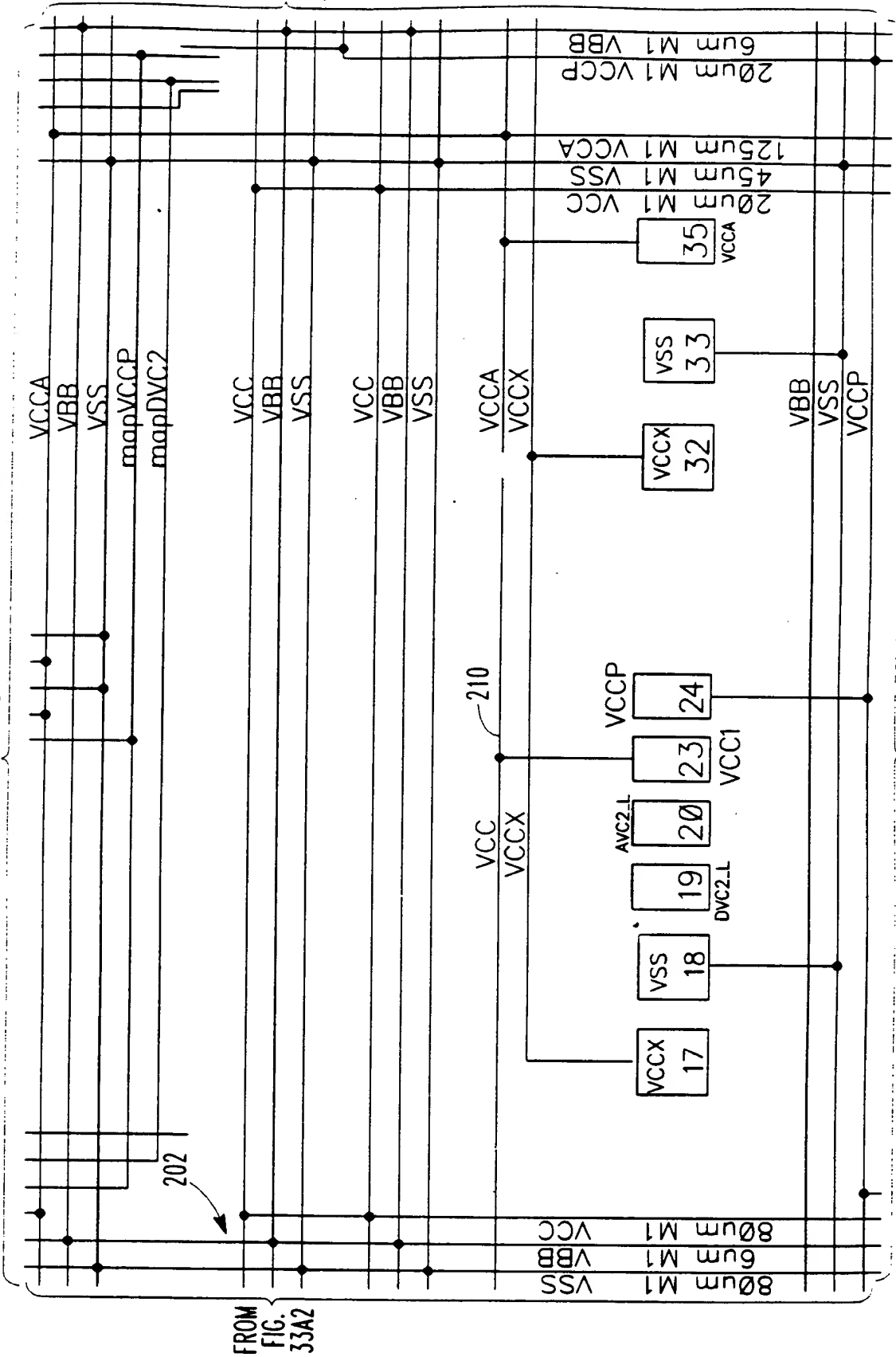
1763331



TO FIG. 3384

FIG. 33B2

FROM FIG. 33B1



FROM FIG. 33A2

TO FIG. 33B3

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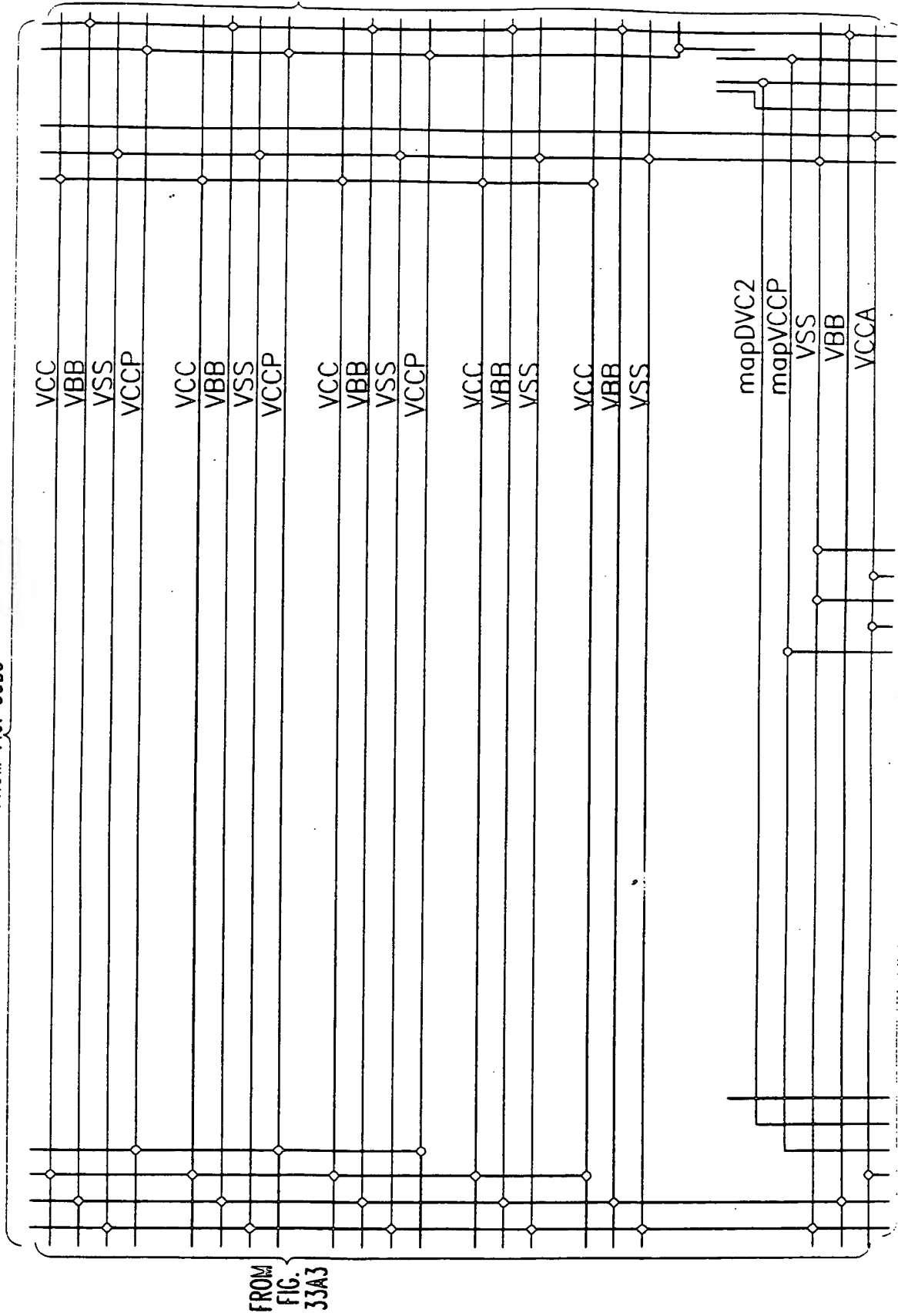
TO FIG. 33B3

FIG. 33B3



FIG. 33B1

FROM FIG. 33B3

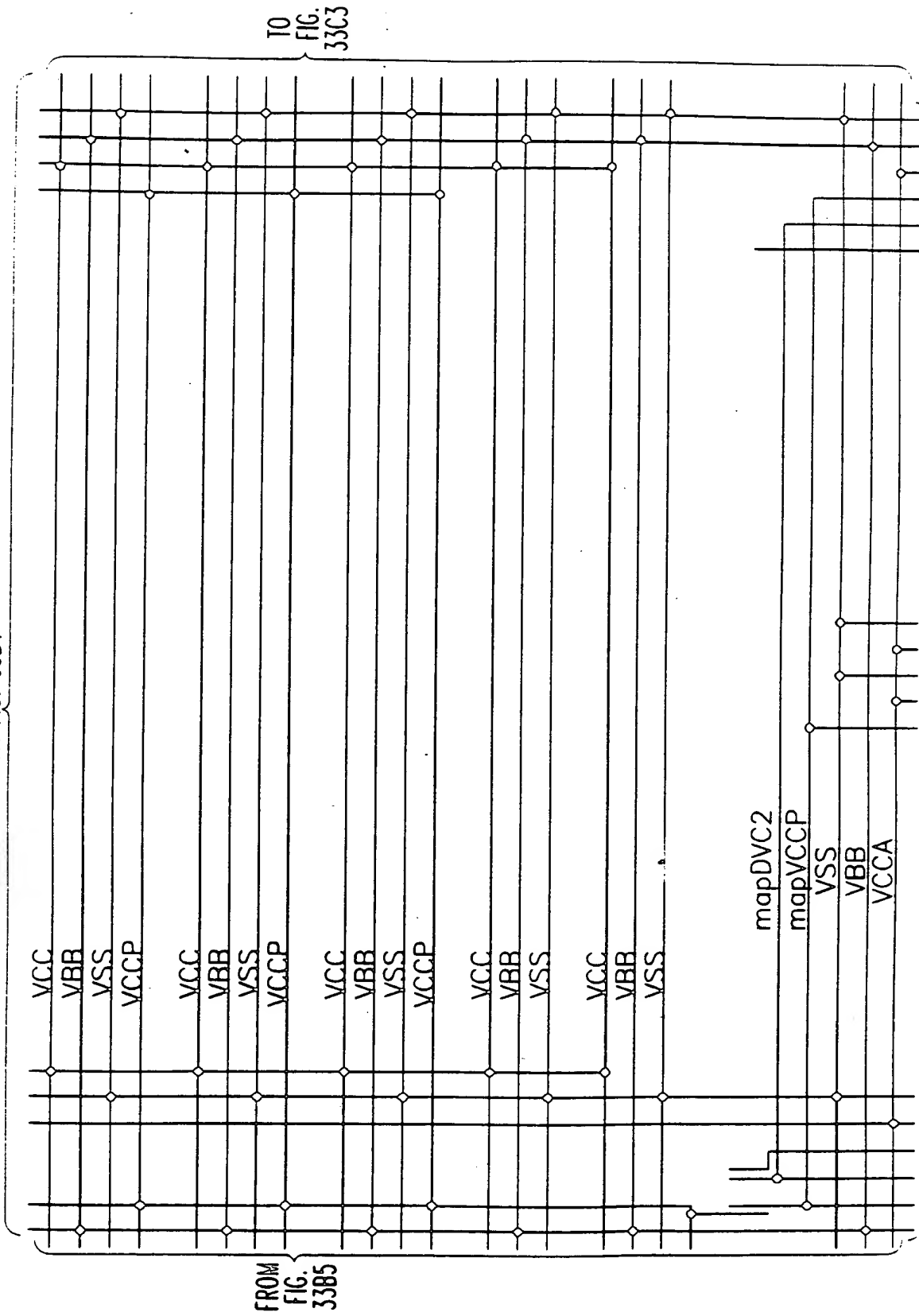


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TO FIG. 33B7

FIG. 33B5

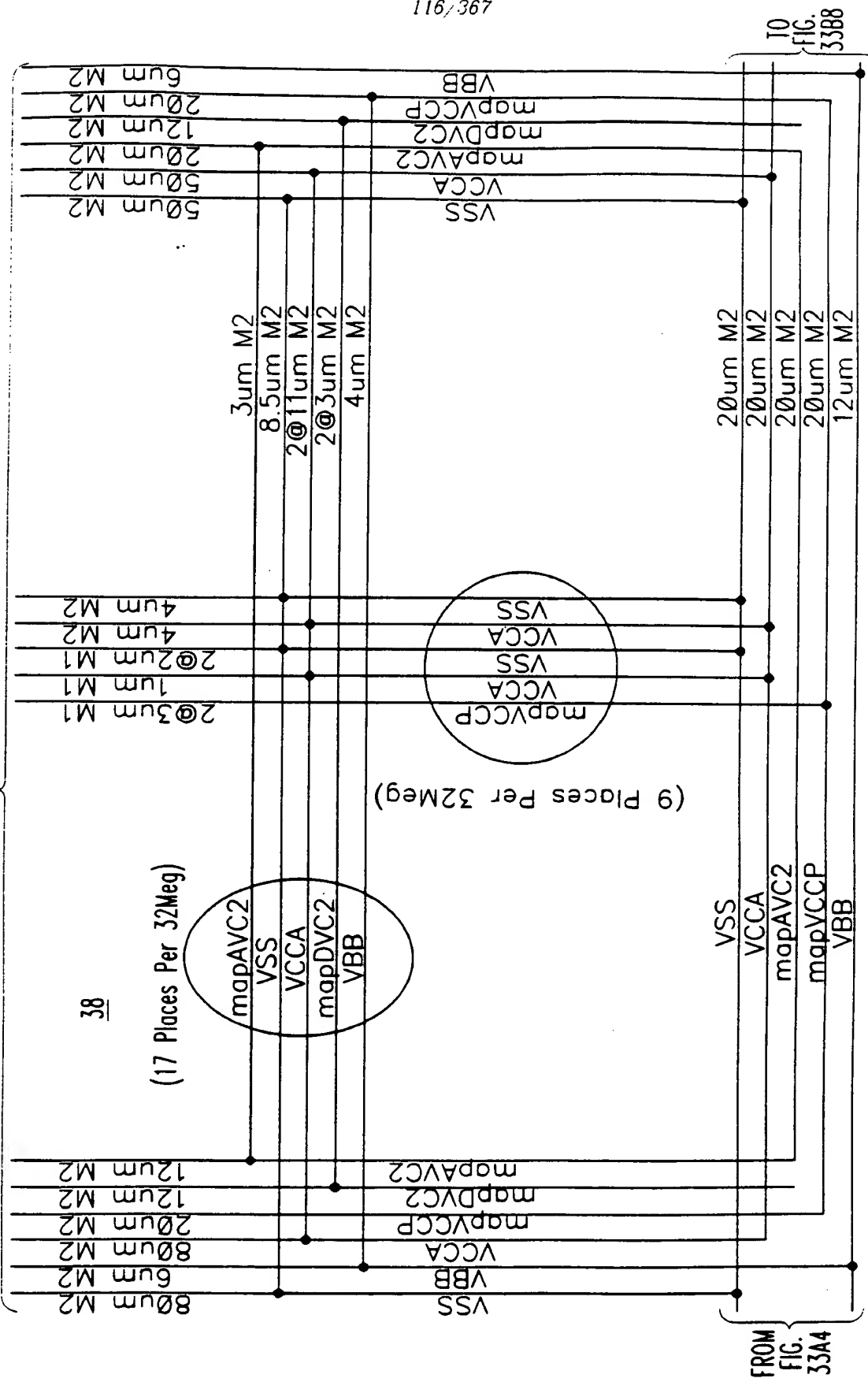
FROM FIG. 33B4



TO FIG. 33B6

FIG. 33B6

FROM FIG. 33B5



1163337

FROM FIG. 33B6

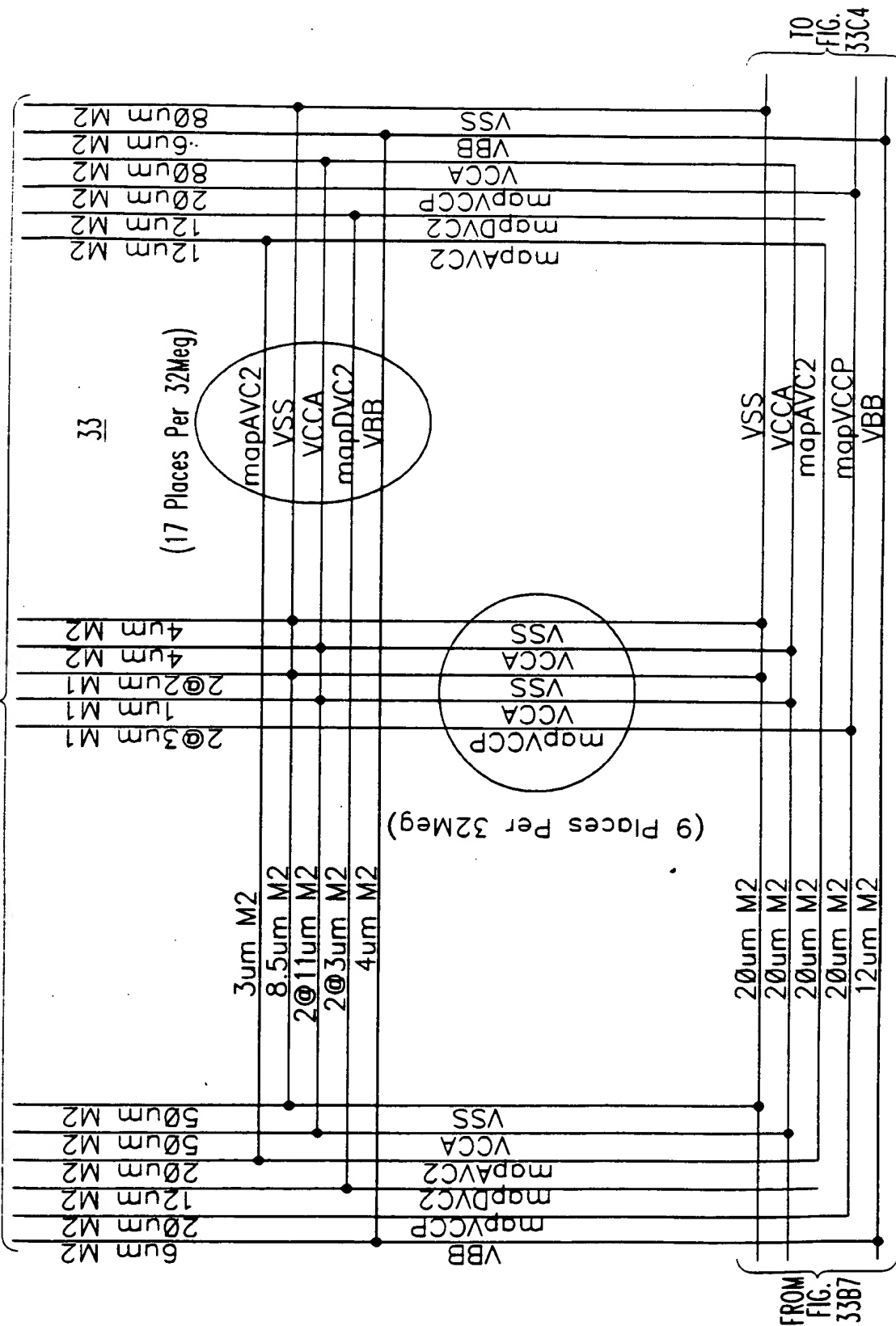
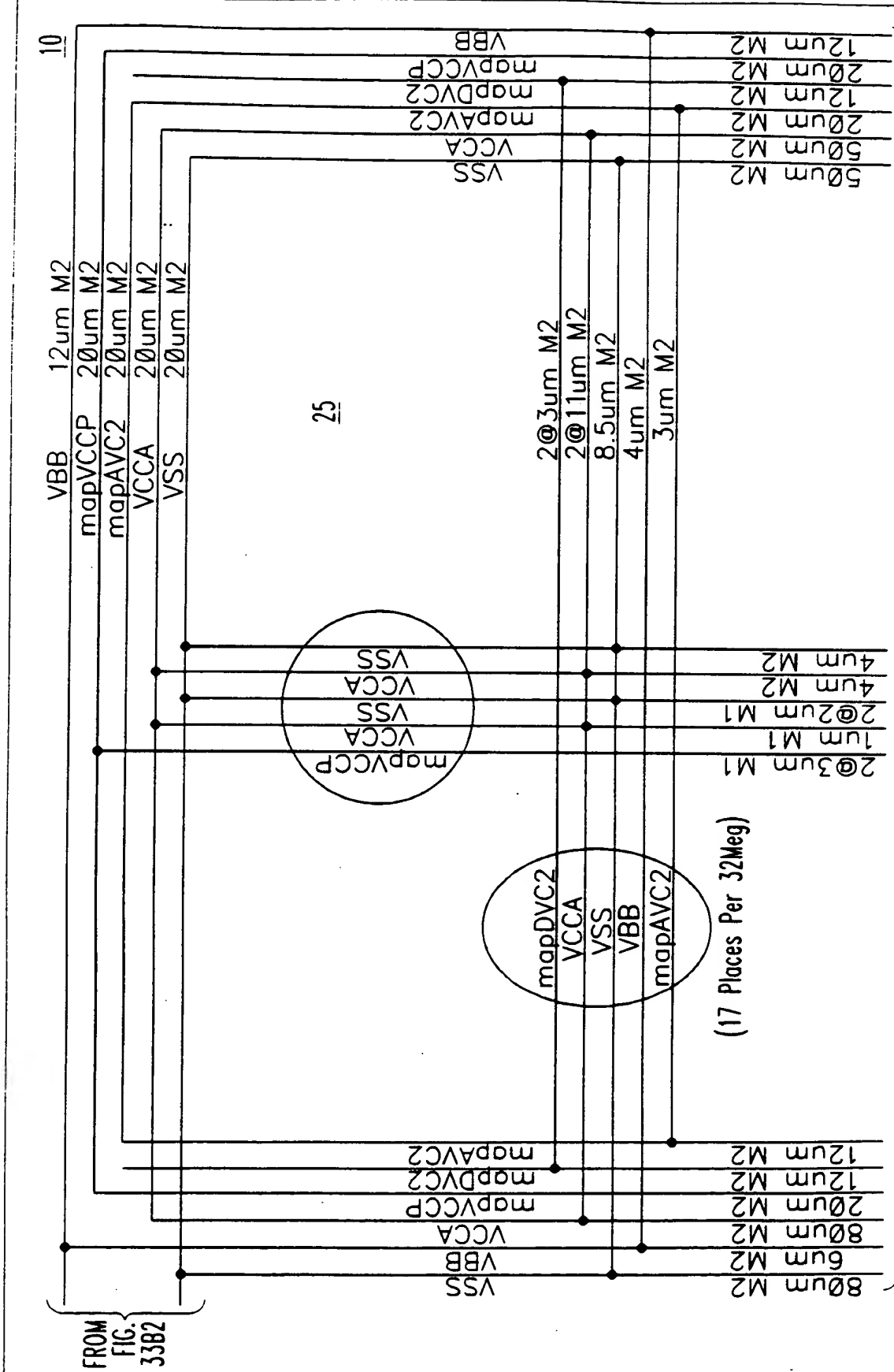


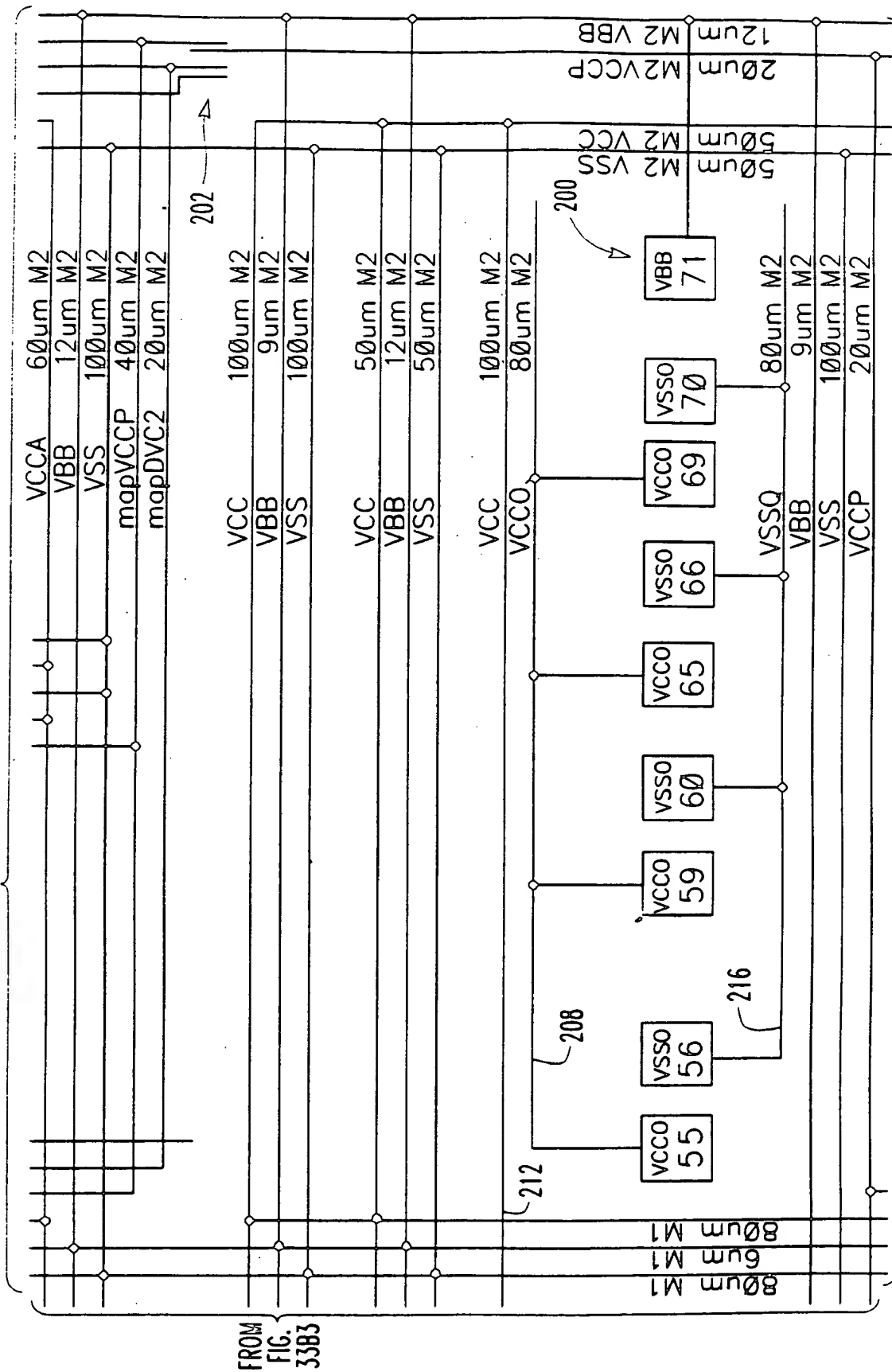
FIG. 33B8



TO FIG. 33C2

FIG. 33C1

FROM FIG. 33C1



TO FIG. 33C3

FIG. 33C2

FROM
FIG.
3386



1163363

FROM FIG. 33C3

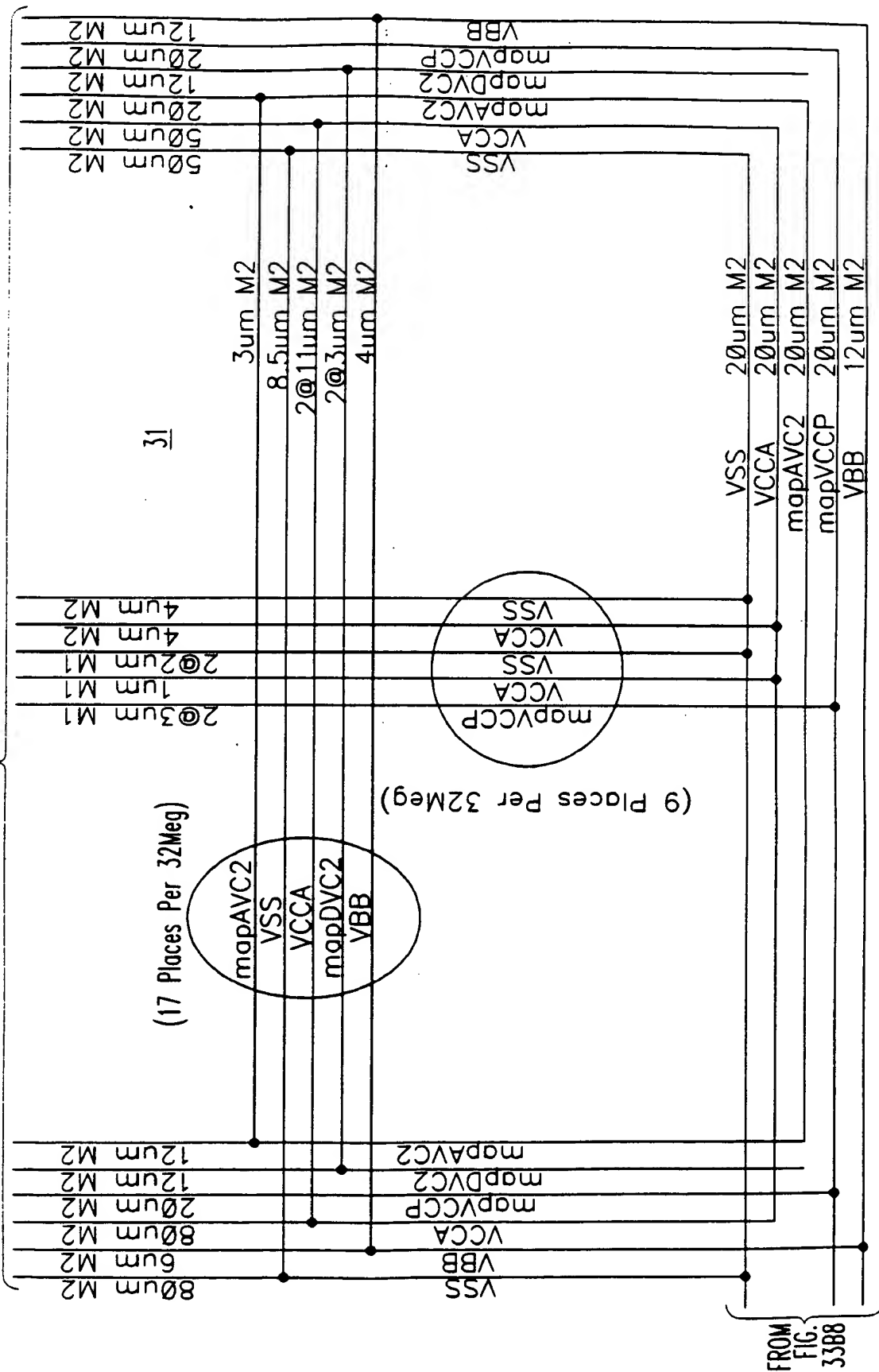


FIG. 33C.1

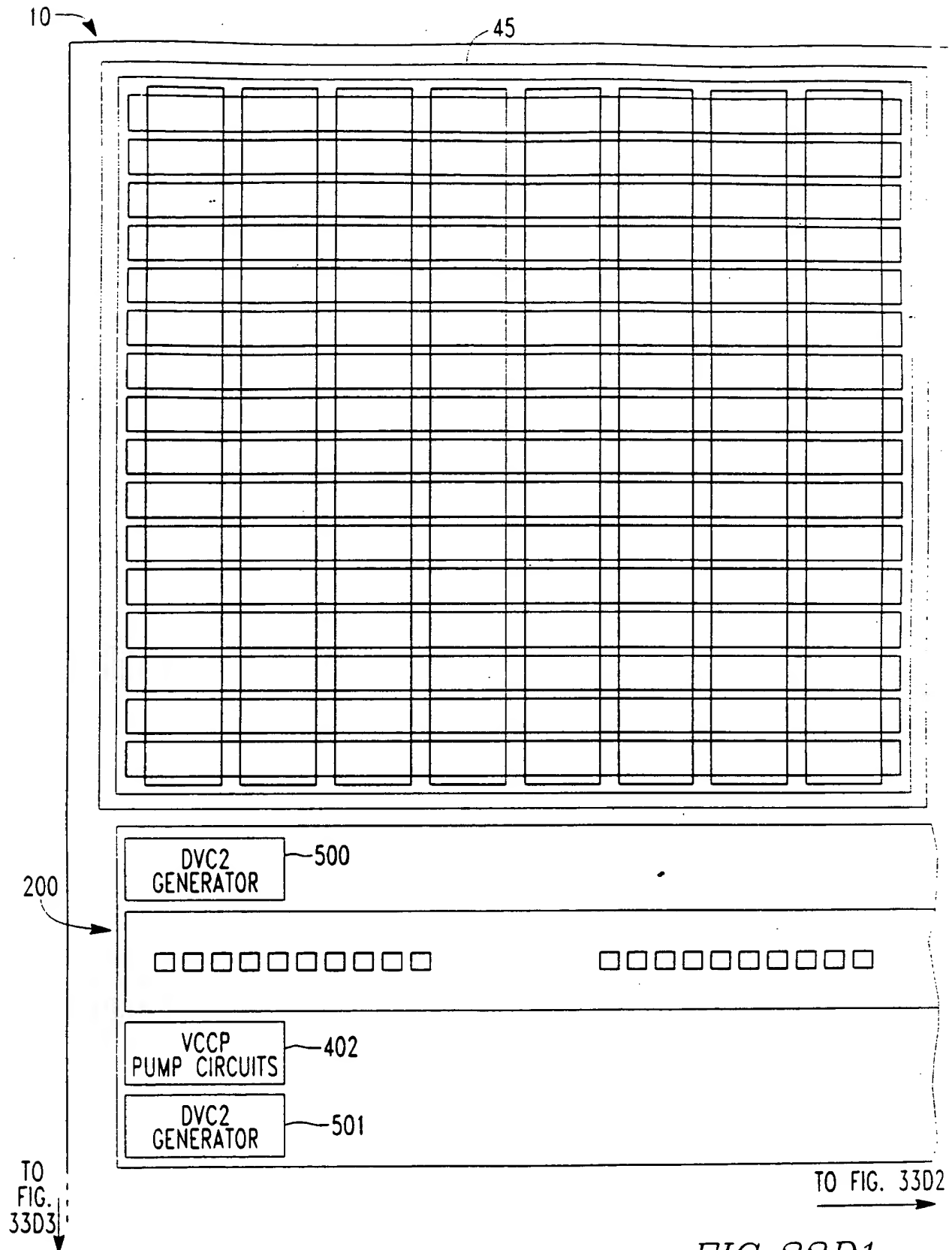
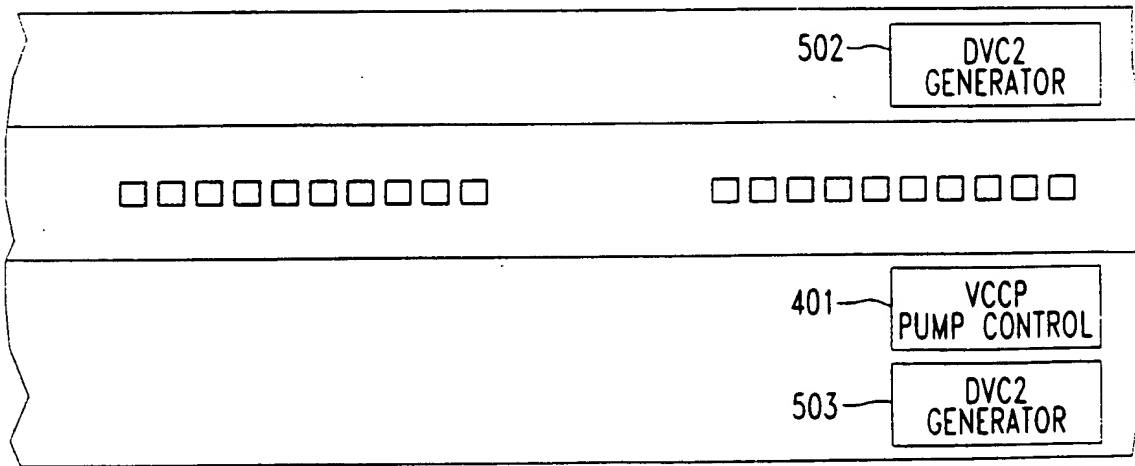
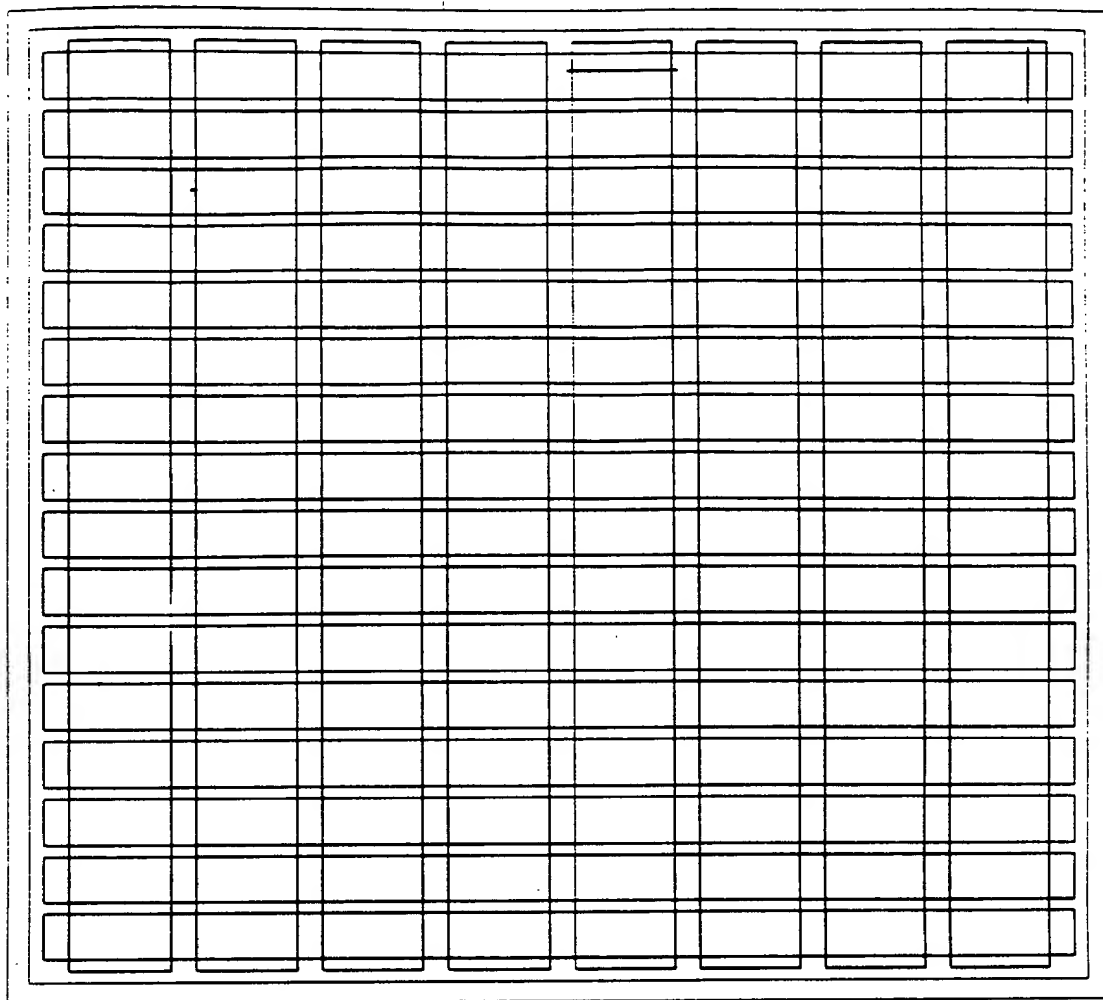


FIG. 33D1

TO FIG. 33D1

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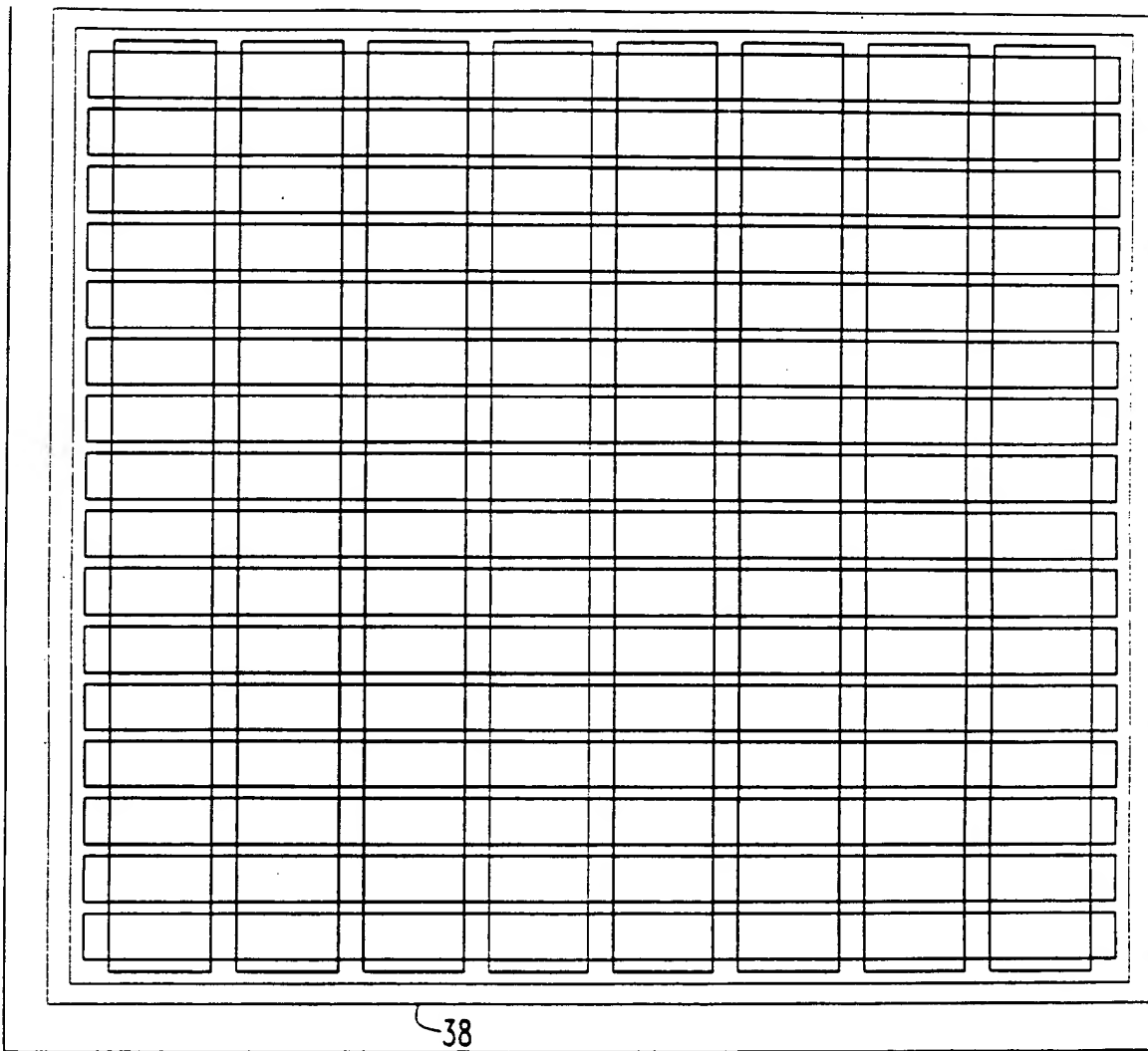


TO
FIG.
▼ 33D4

FIG. 33D2

TO
FIG.
33D1

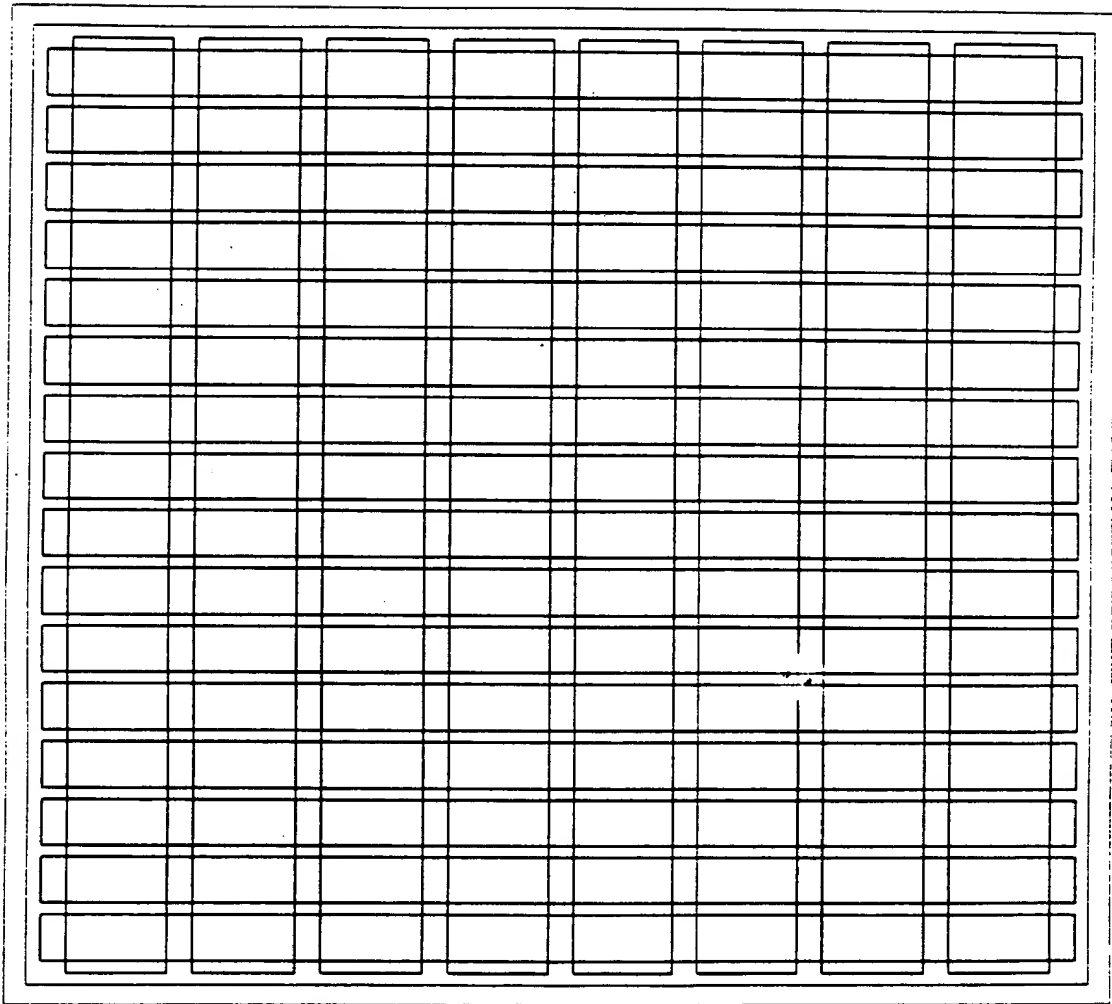
(SEE FIG. 33E1)



TO FIG. 33D4

FIG. 33D3

TO
FIG.
33D2



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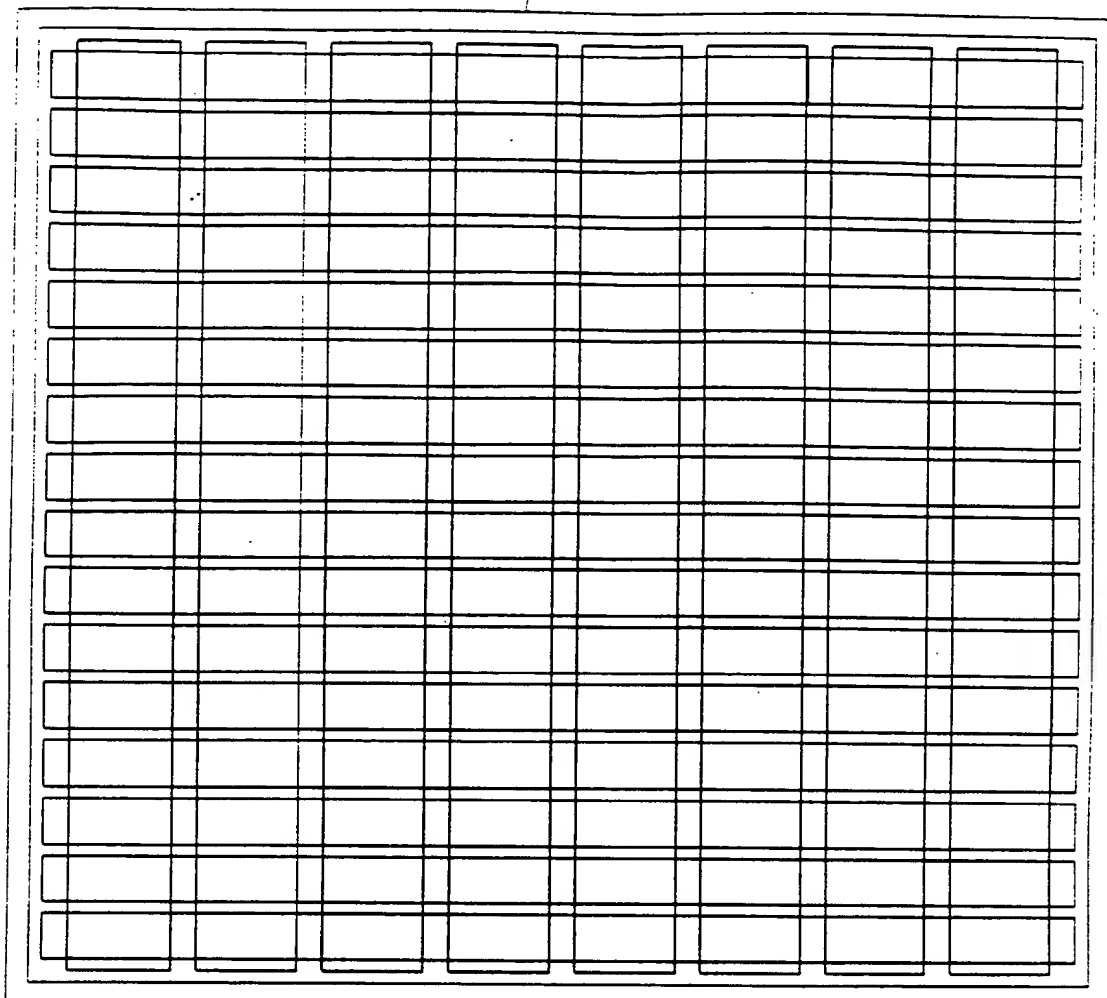
TO FIG. 33D3

FIG. 33D4

(SEE FIG. 33D2)

25

TO FIG. 33E2



DVC2
GENERATOR 504

□ □ □ □ □ □ □ □ □ □

□ □ □ □ □ □ □ □ □ □

VCCP
REGULATOR 220

DVC2
GENERATOR 505

TO
FIG.
33E3

FIG. 33E1

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TO FIG. 33E1

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10

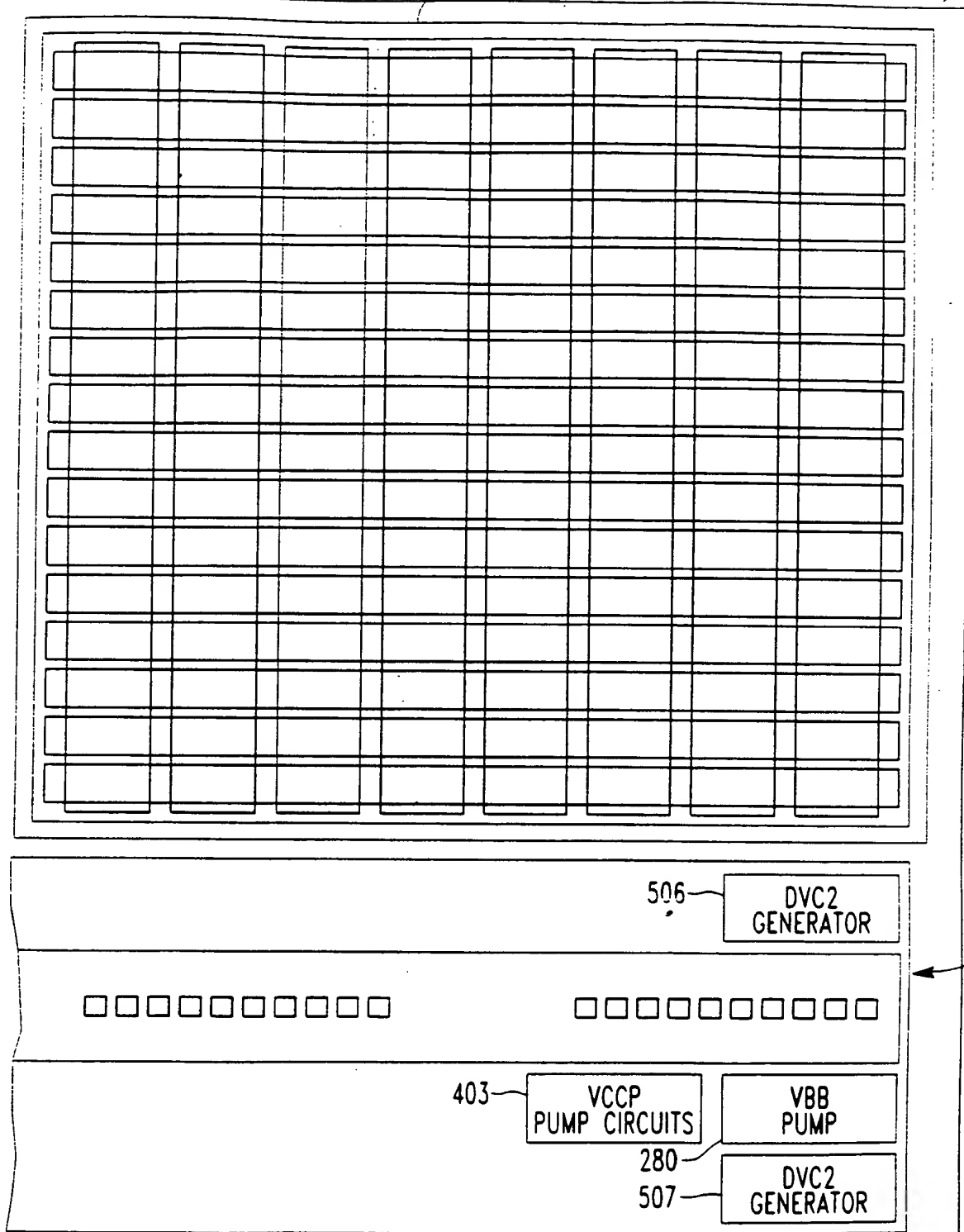
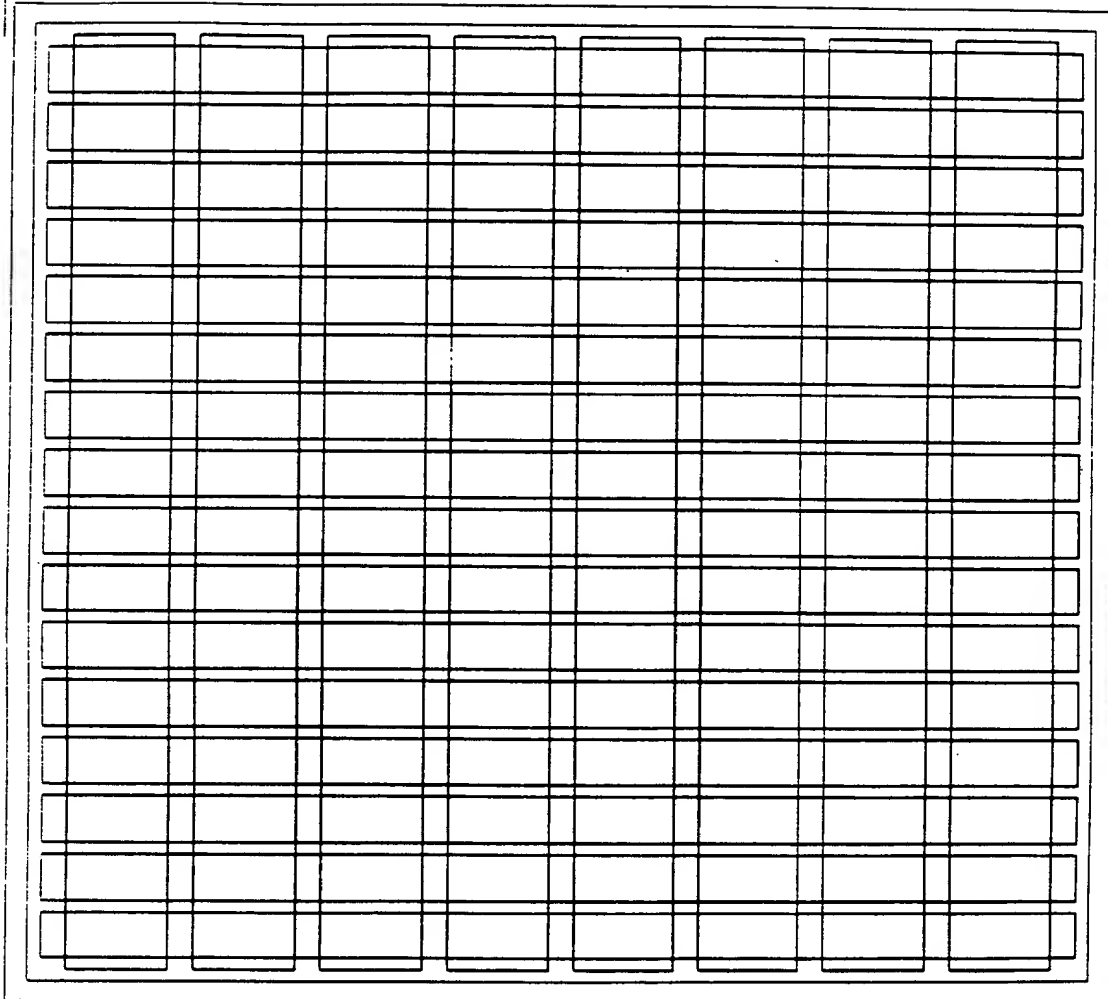


FIG. 33E2

TO
FIG.
33E4

TO
FIG.
33E1

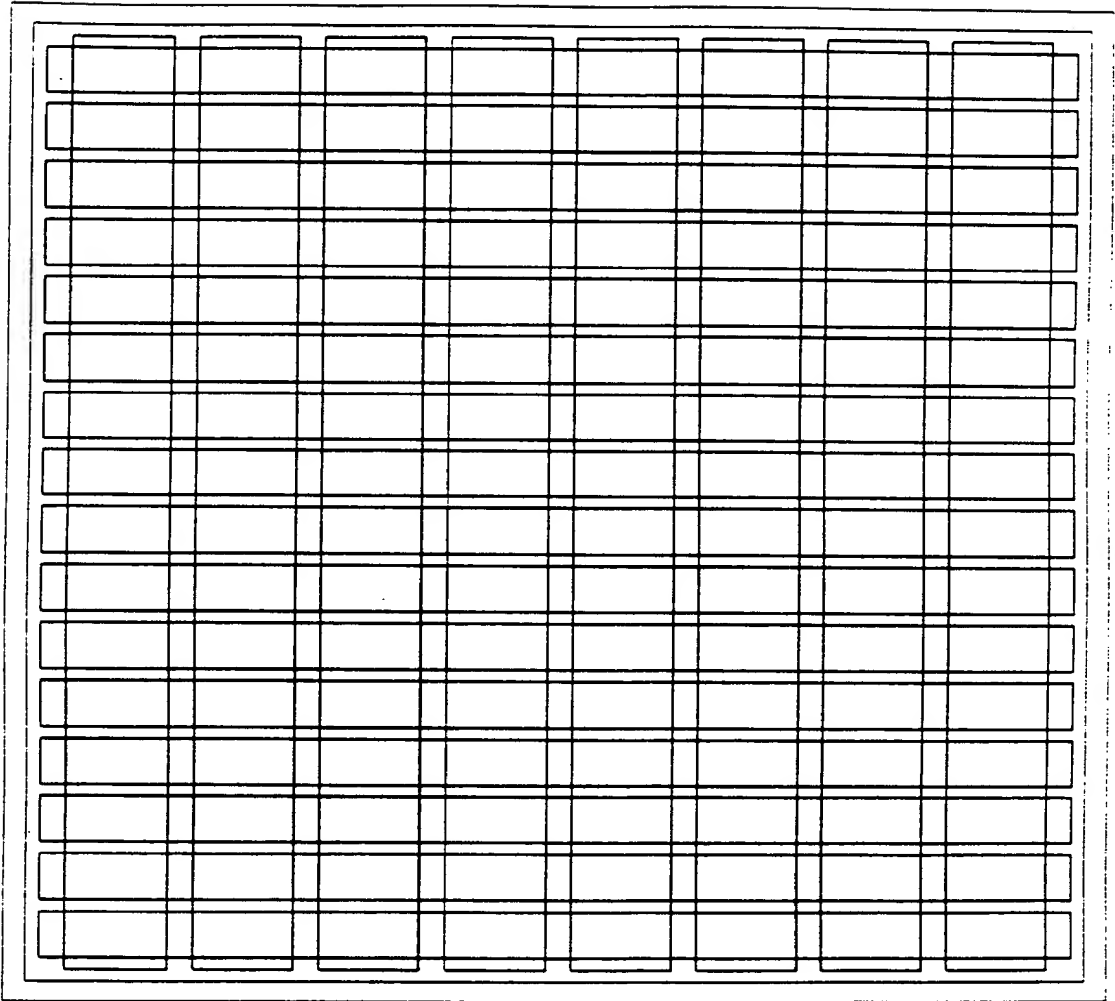


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TO FIG. 33E4

FIG. 33E3

FIG. 33E4

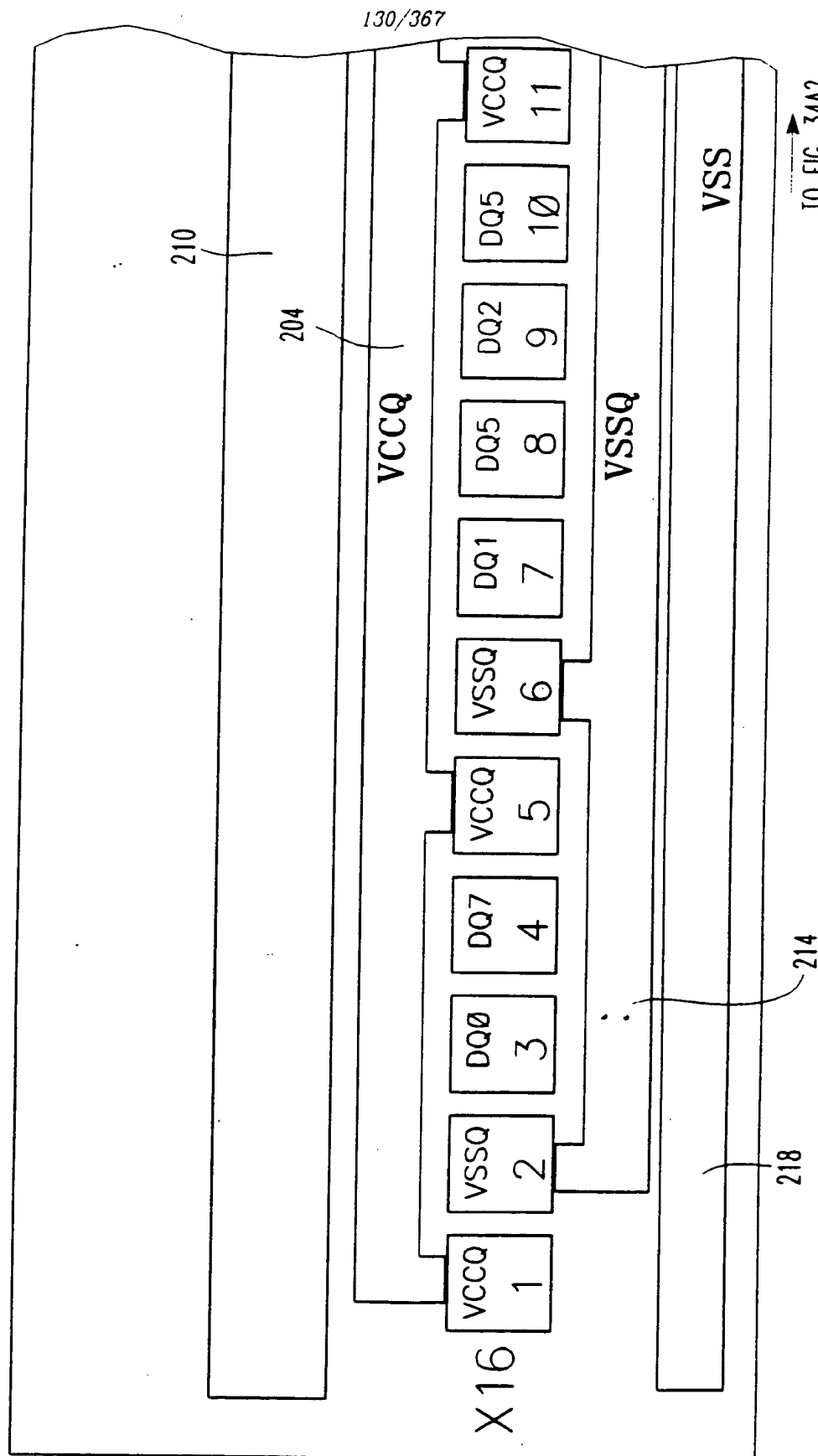


33

TO
FIG.
33E2

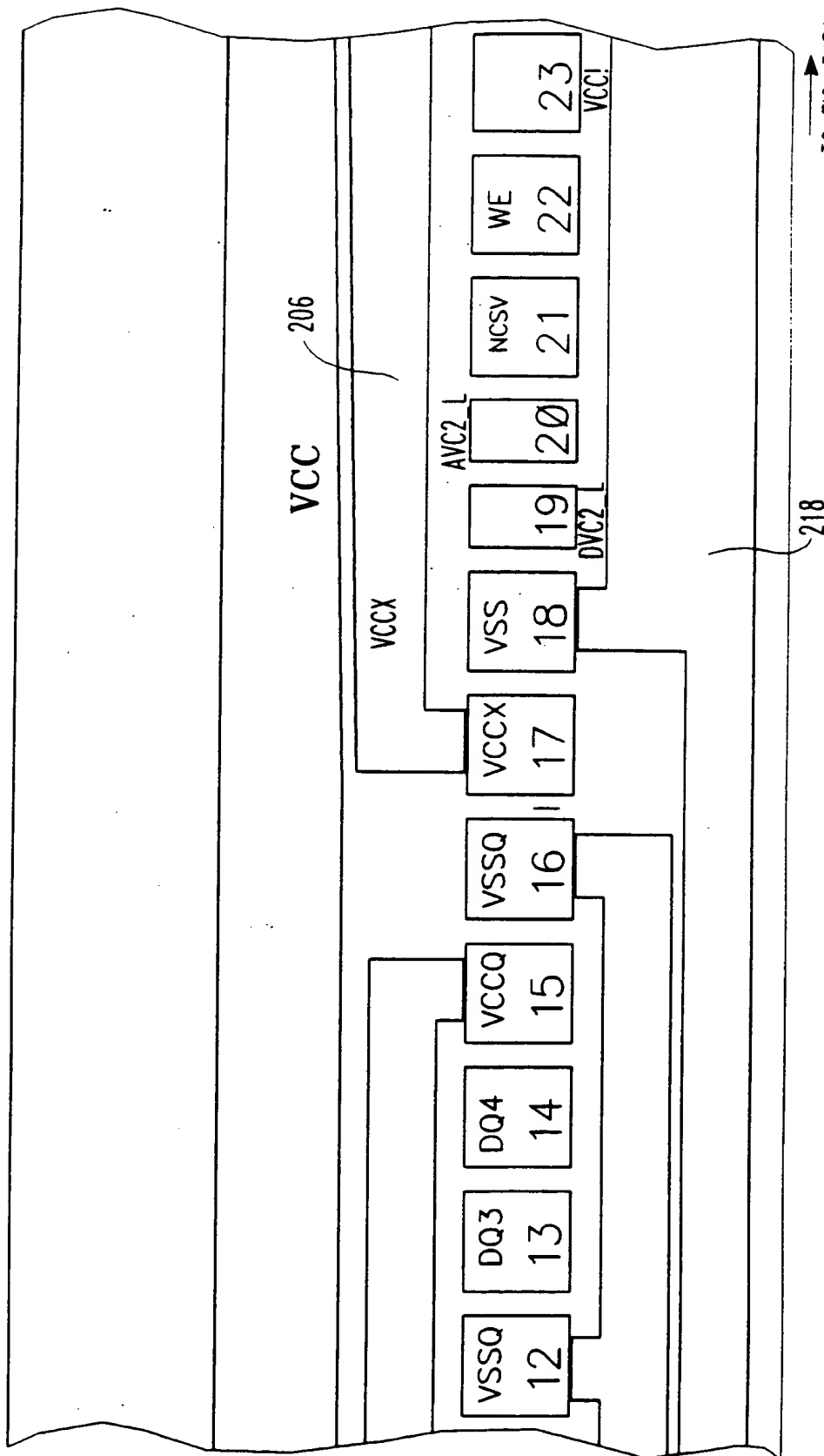
TO FIG. 33E3

FIG. 33E4



10 FIG. 34A2

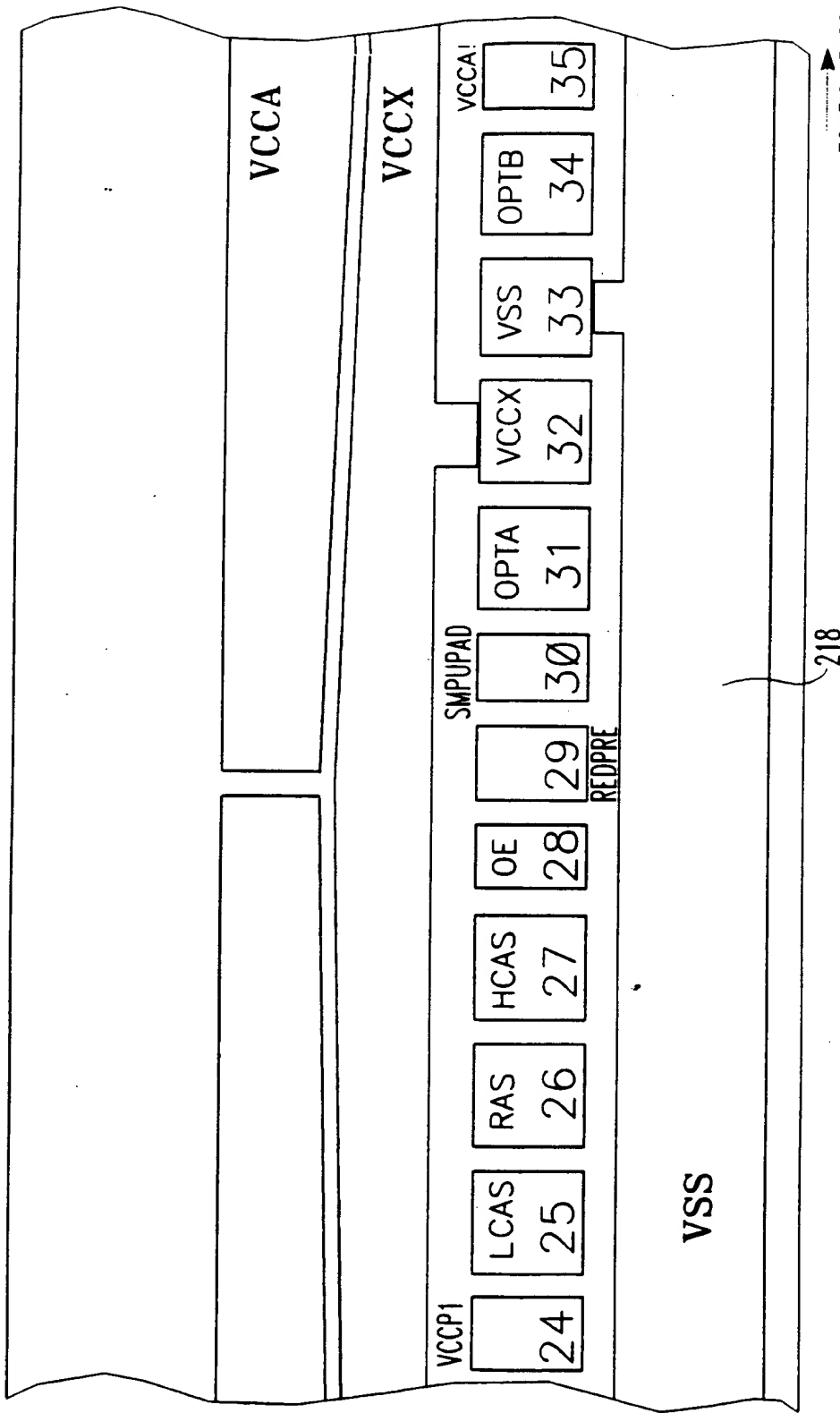
FIG. 34A1



TO FIG. 34B1

FIG. 34A2

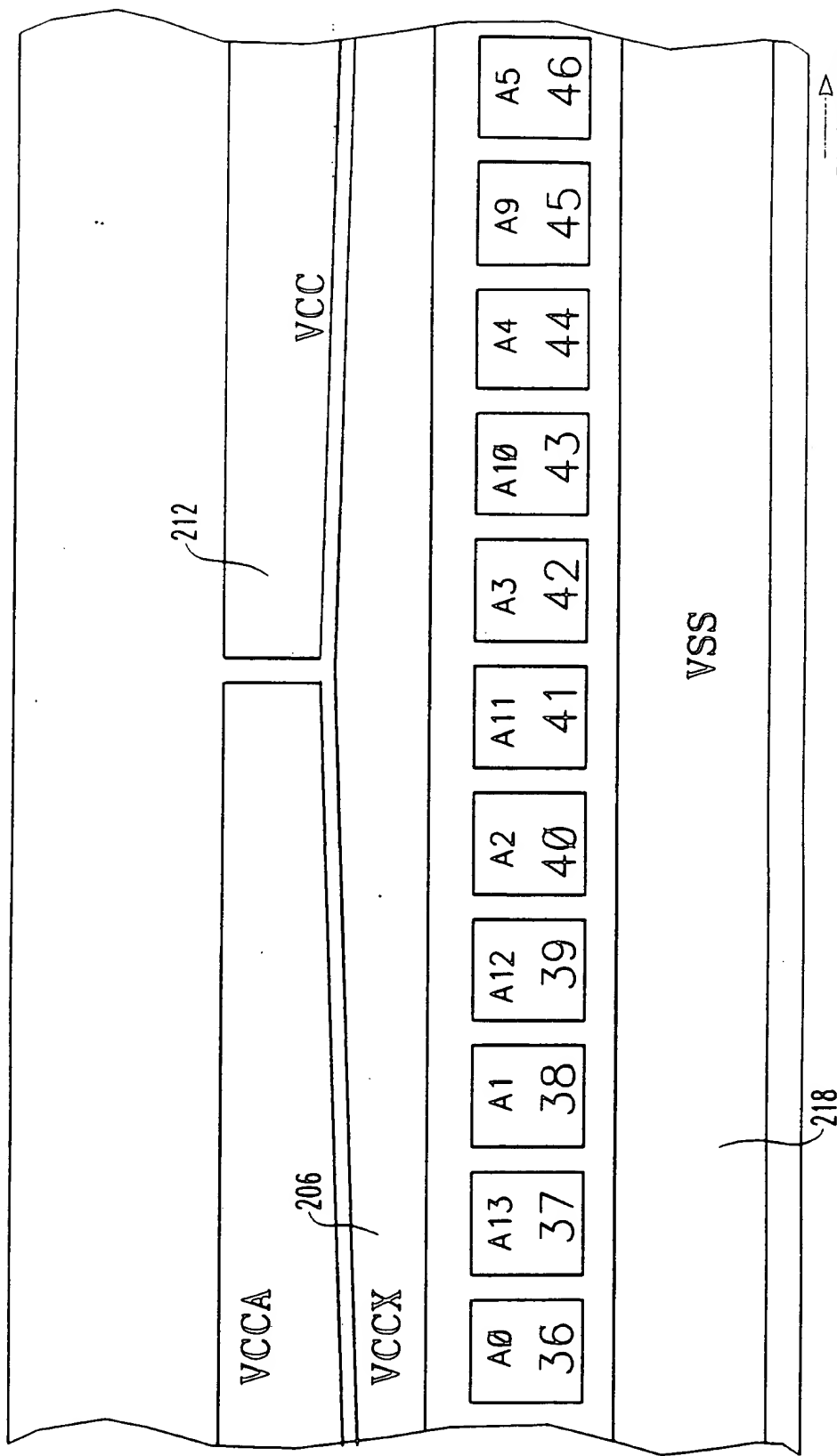
TO FIG. 34A2



TO FIG. 34B2

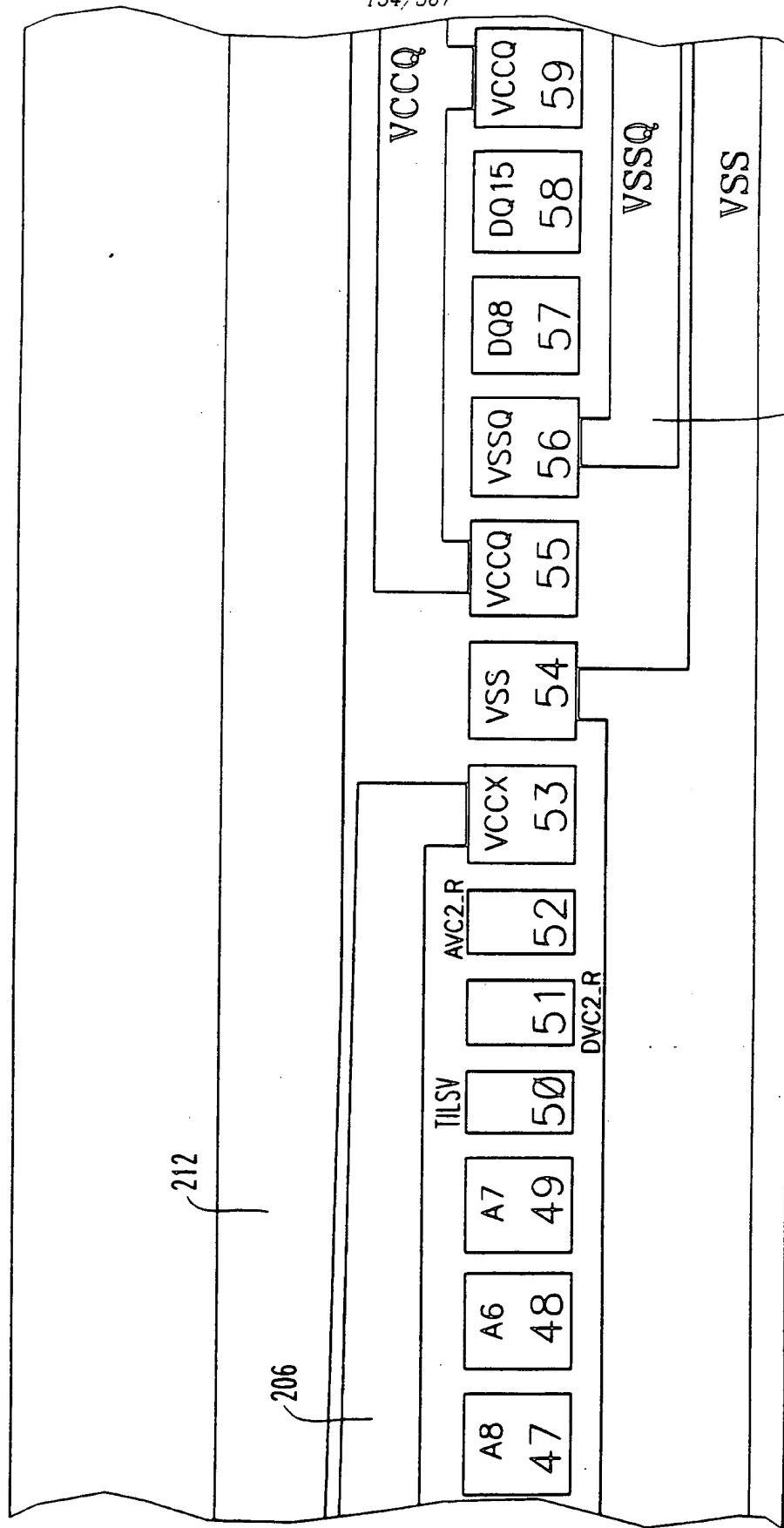
FIG. 34B1

TO FIG. 34B1



TO FIG. 34C1
FIG. 34B2

10 FIG. 34B2



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10 FIG. 34C2

216

FIG. 34C1

TO FIG. 34C1

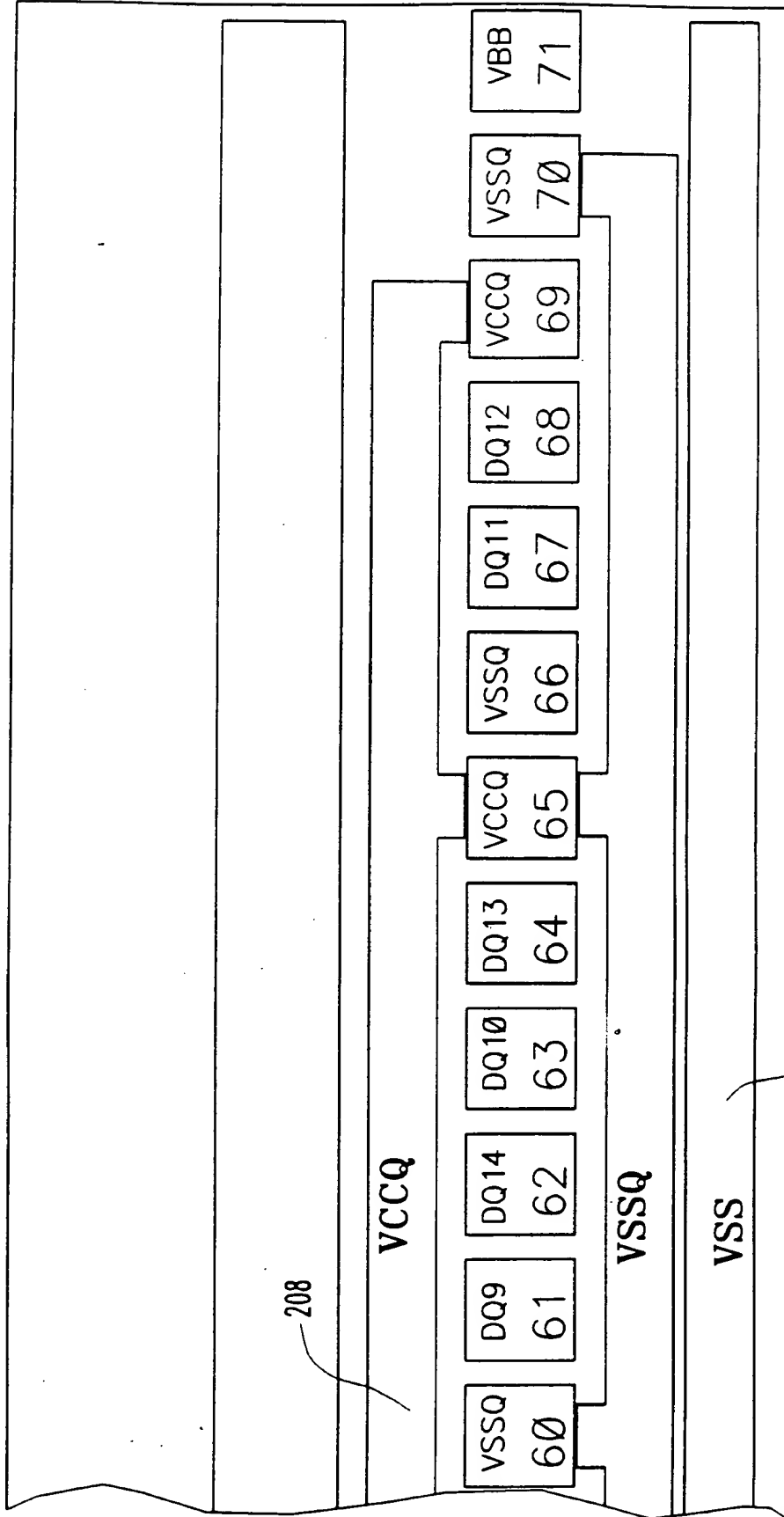
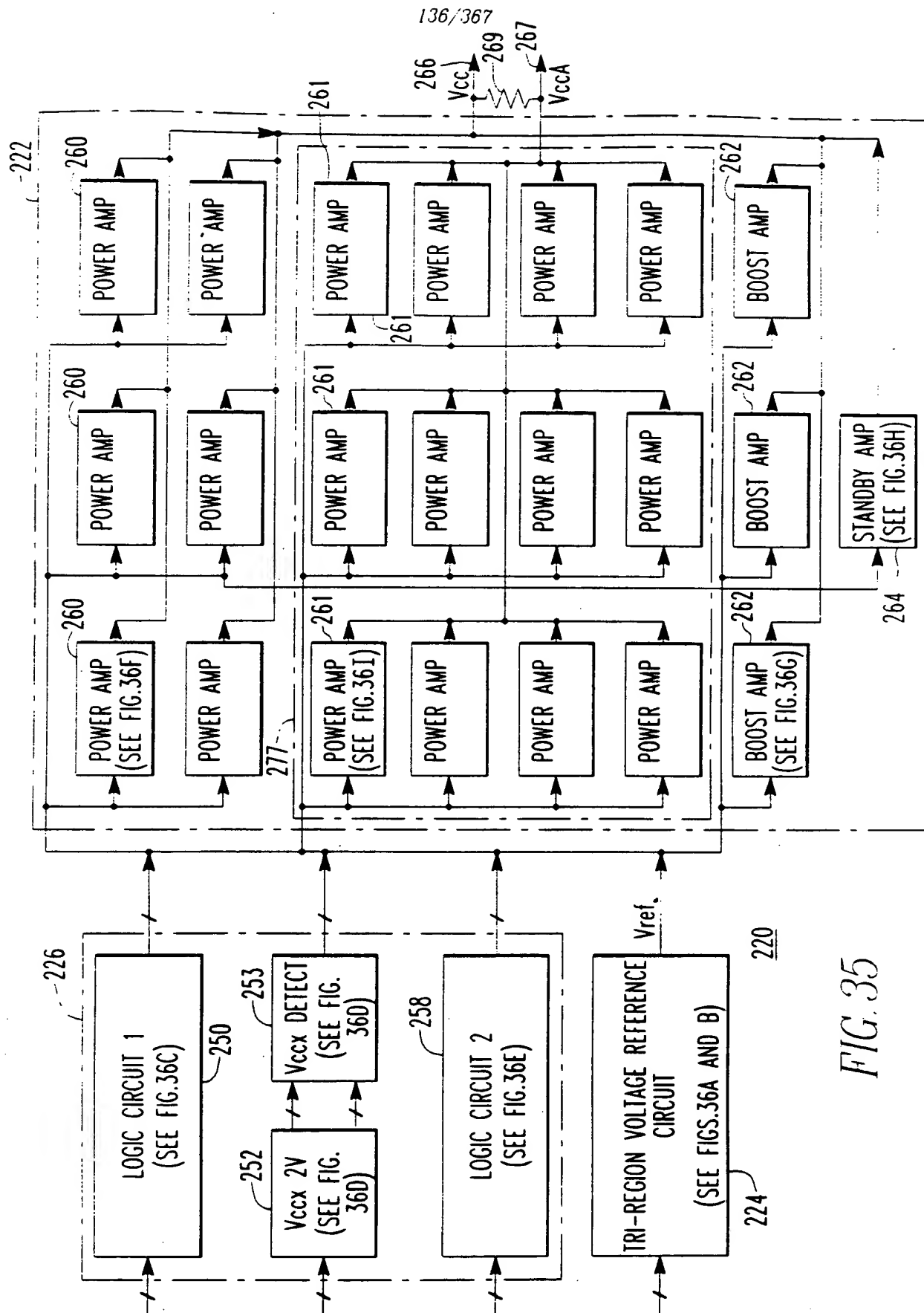


FIG. 34C2



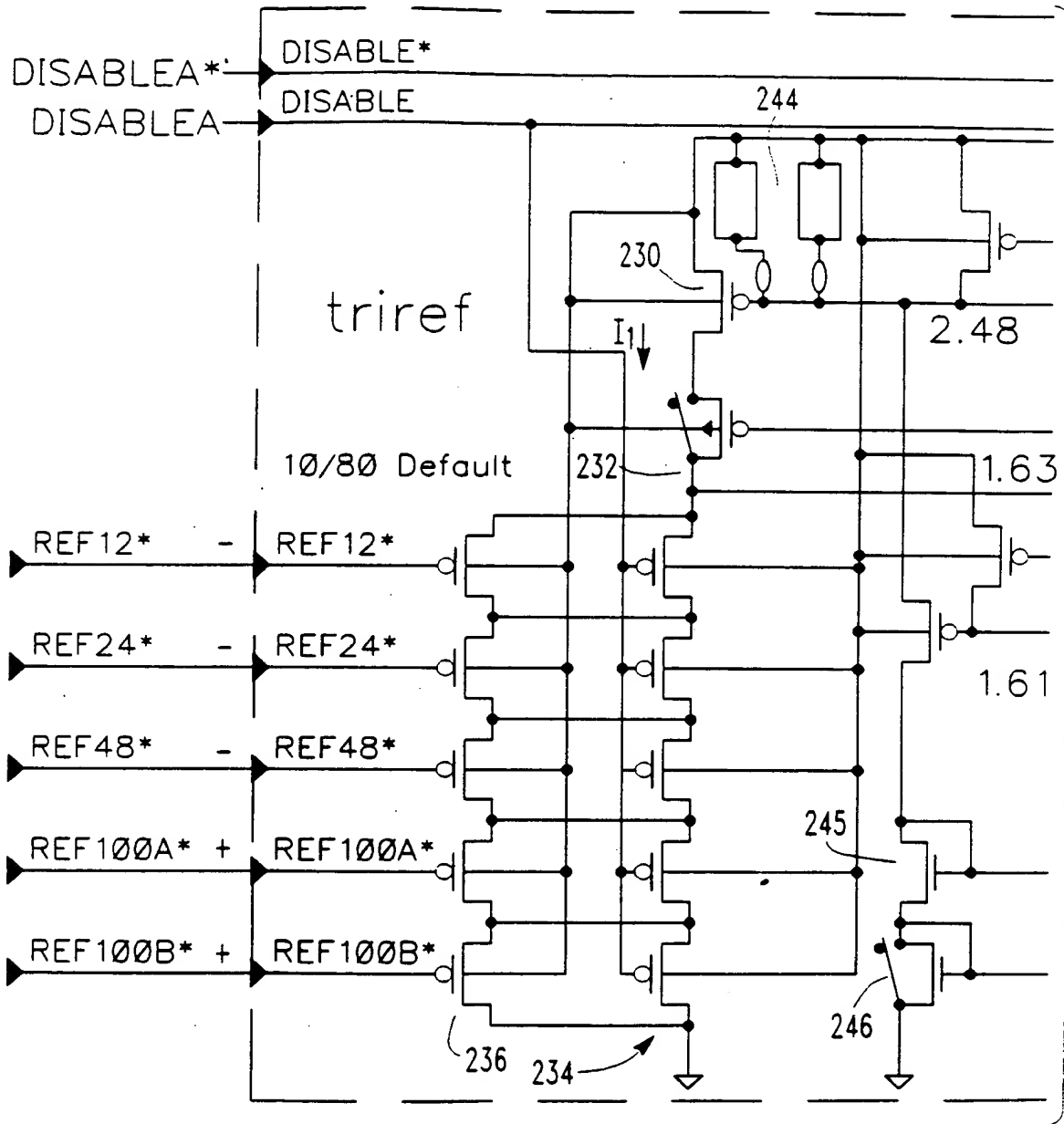
TO
FIG.
36A2

FIG. 36A1

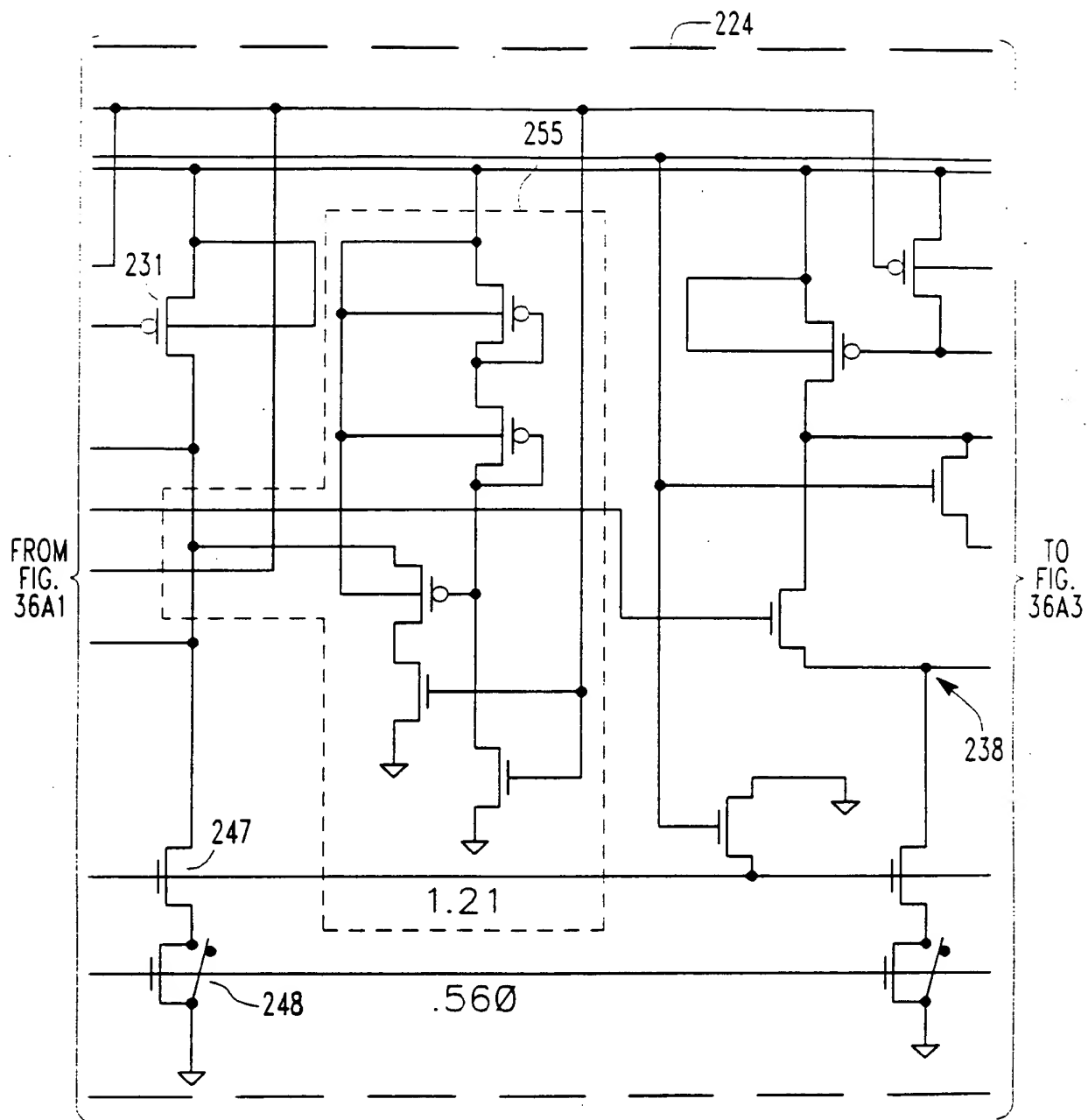


FIG. 36A2

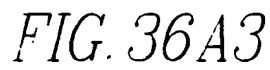


FIG. 36A3

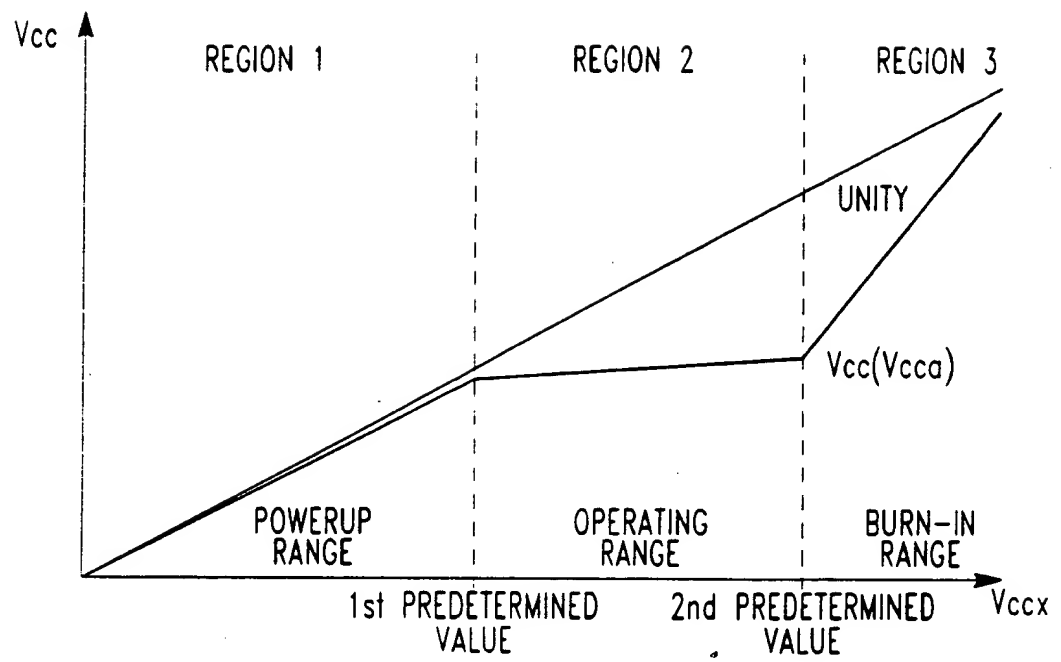


FIG. 36B

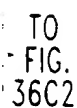
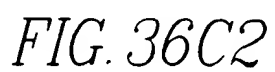


FIG. 36C1



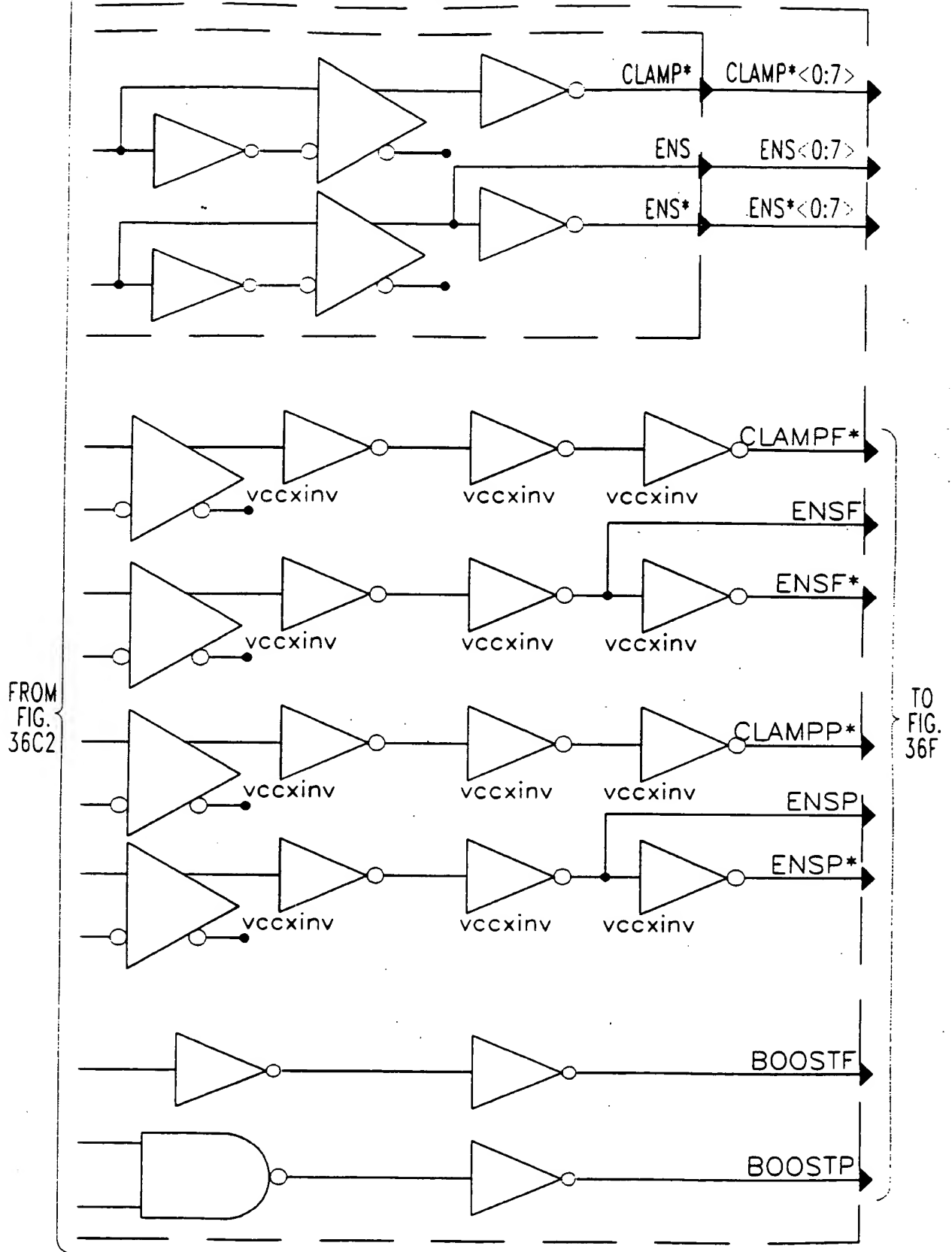


FIG. 36C3

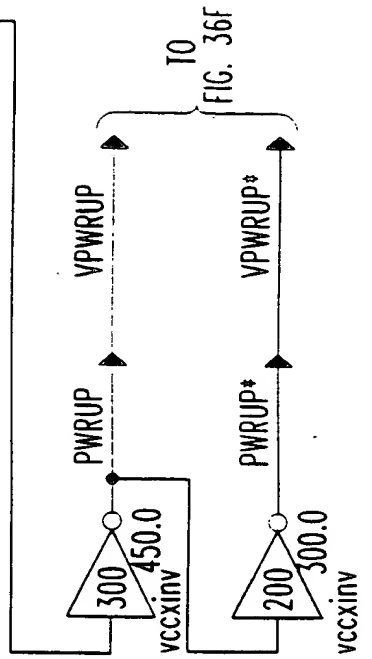
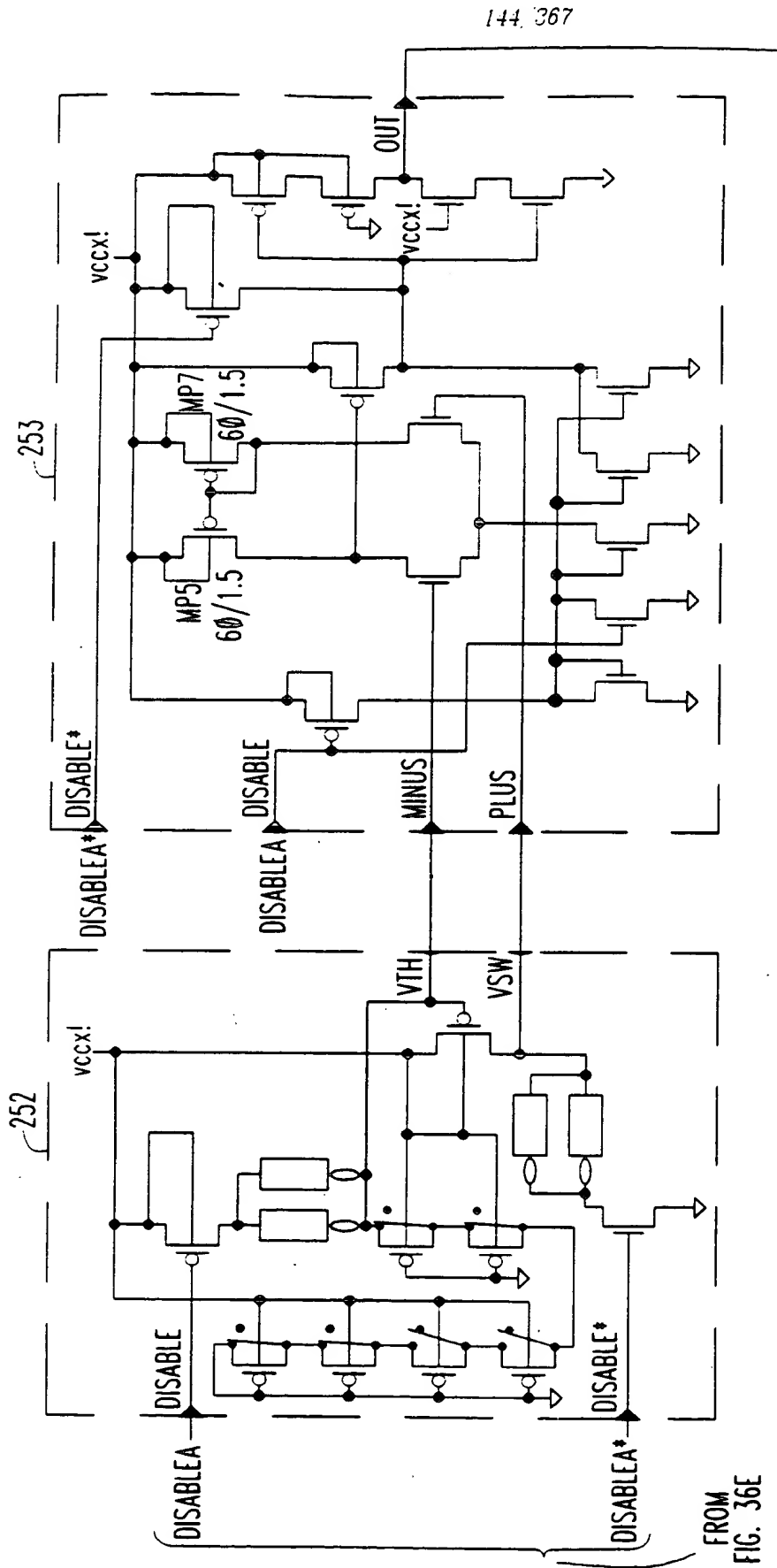
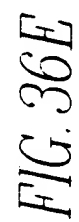
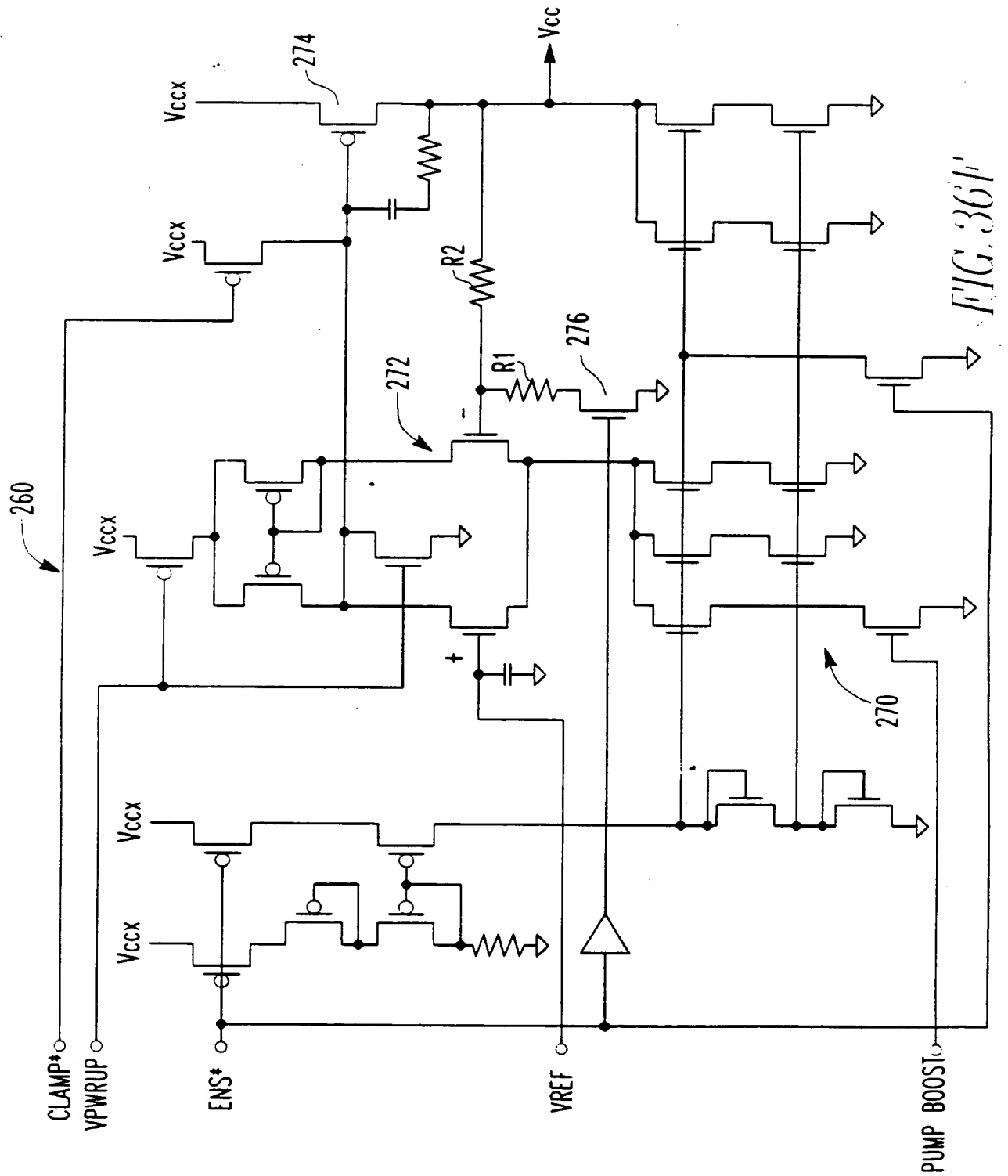
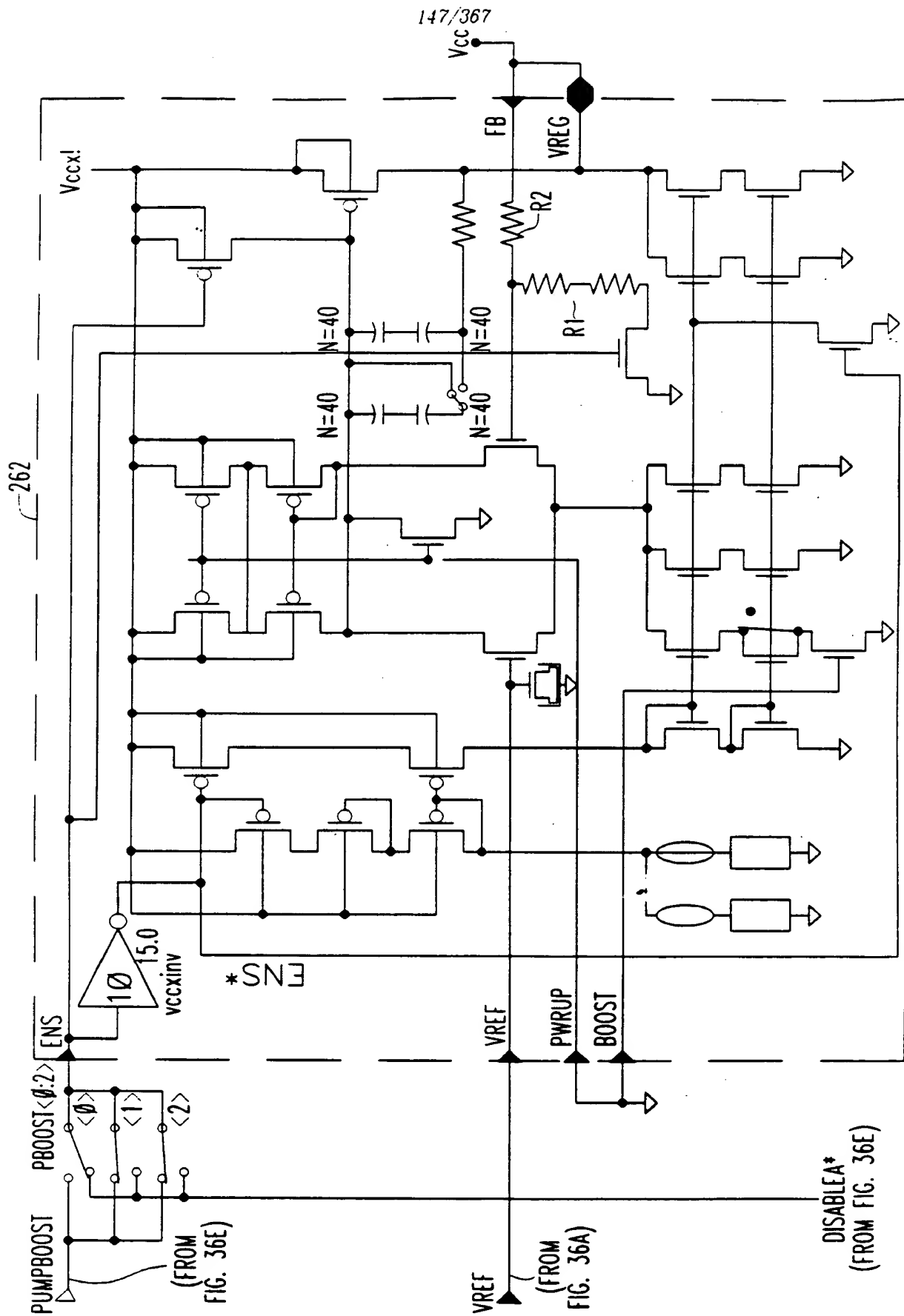


FIG. 36D







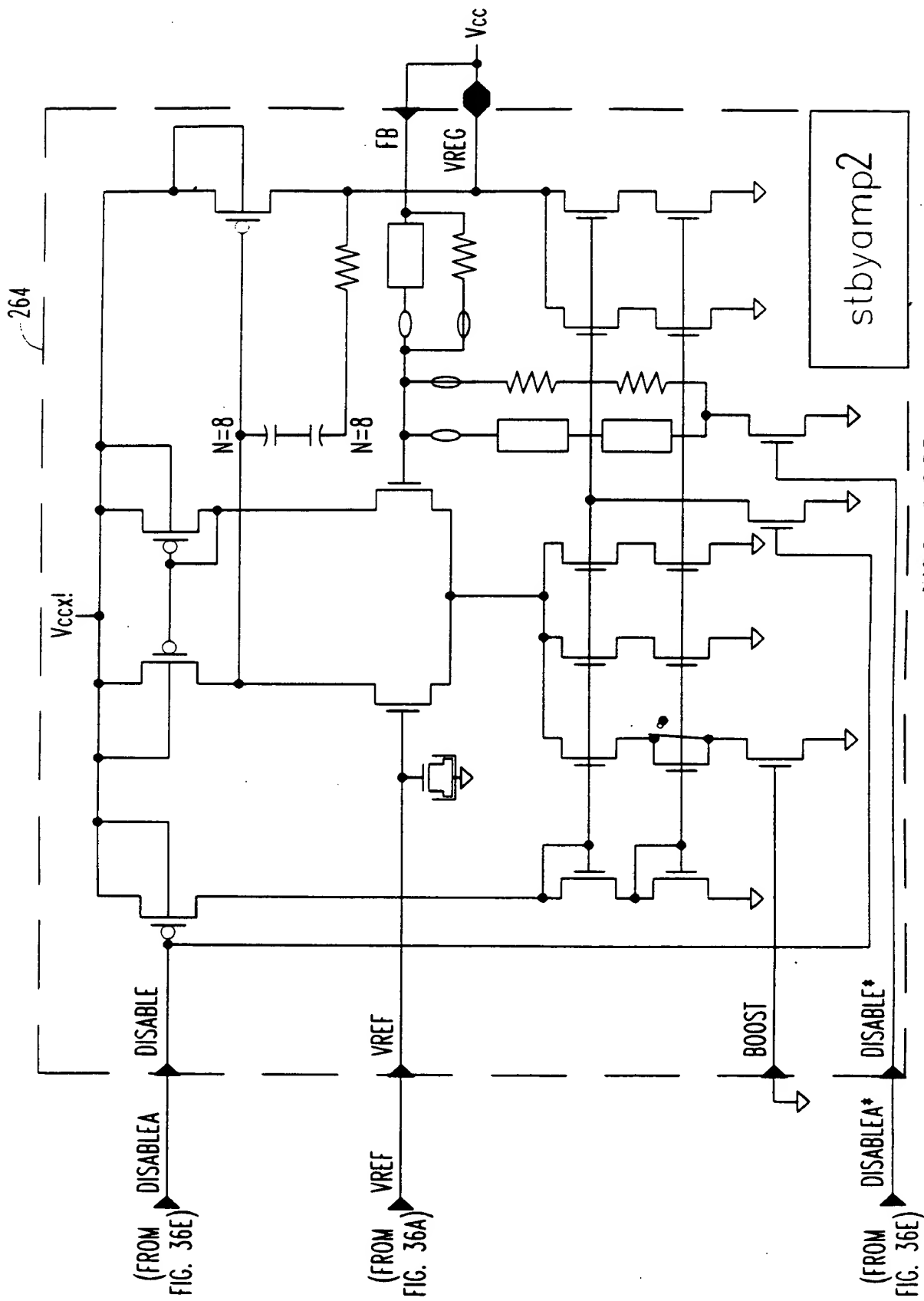


FIG. 36H

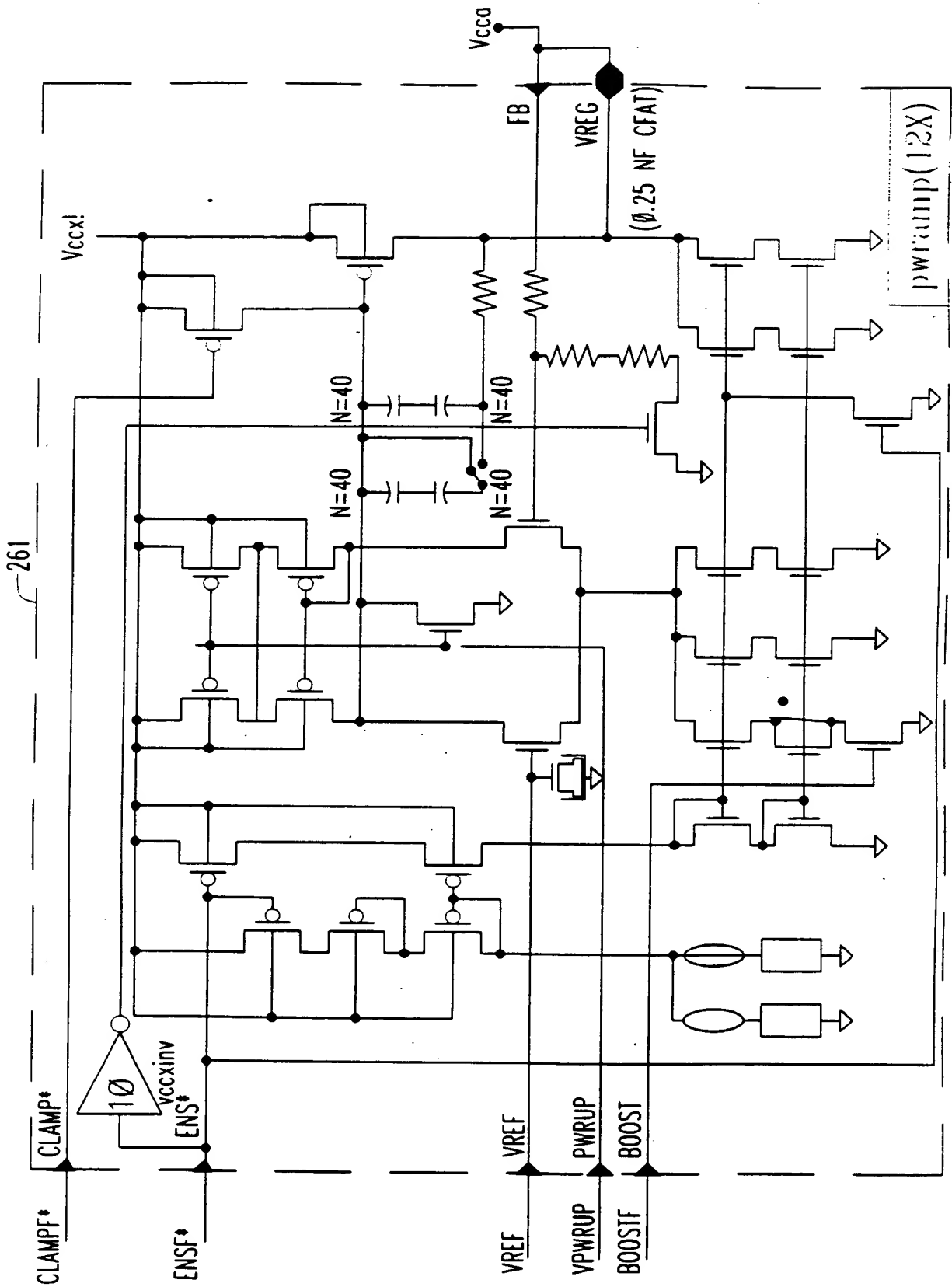


FIG. 361

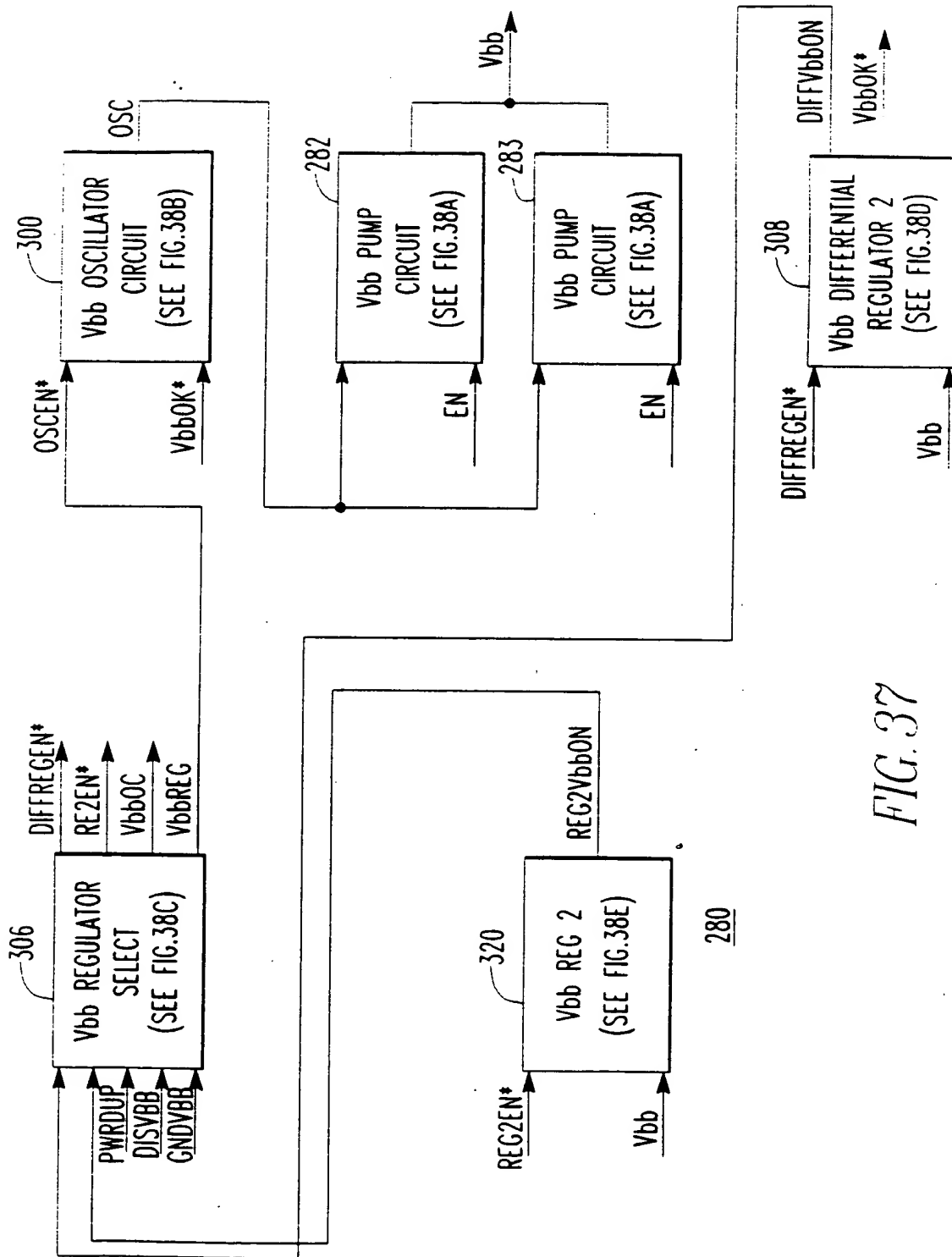


FIG. 37

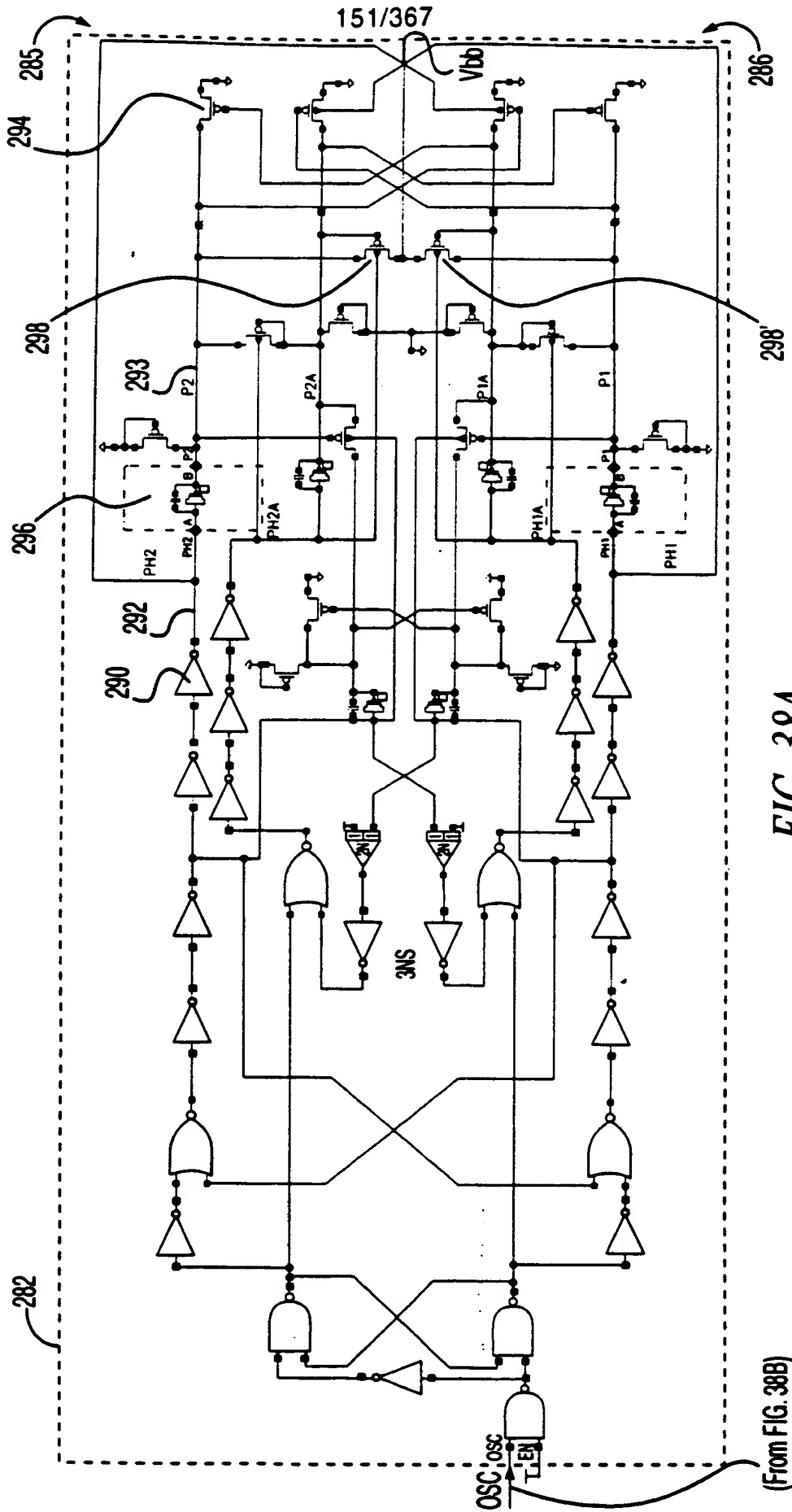
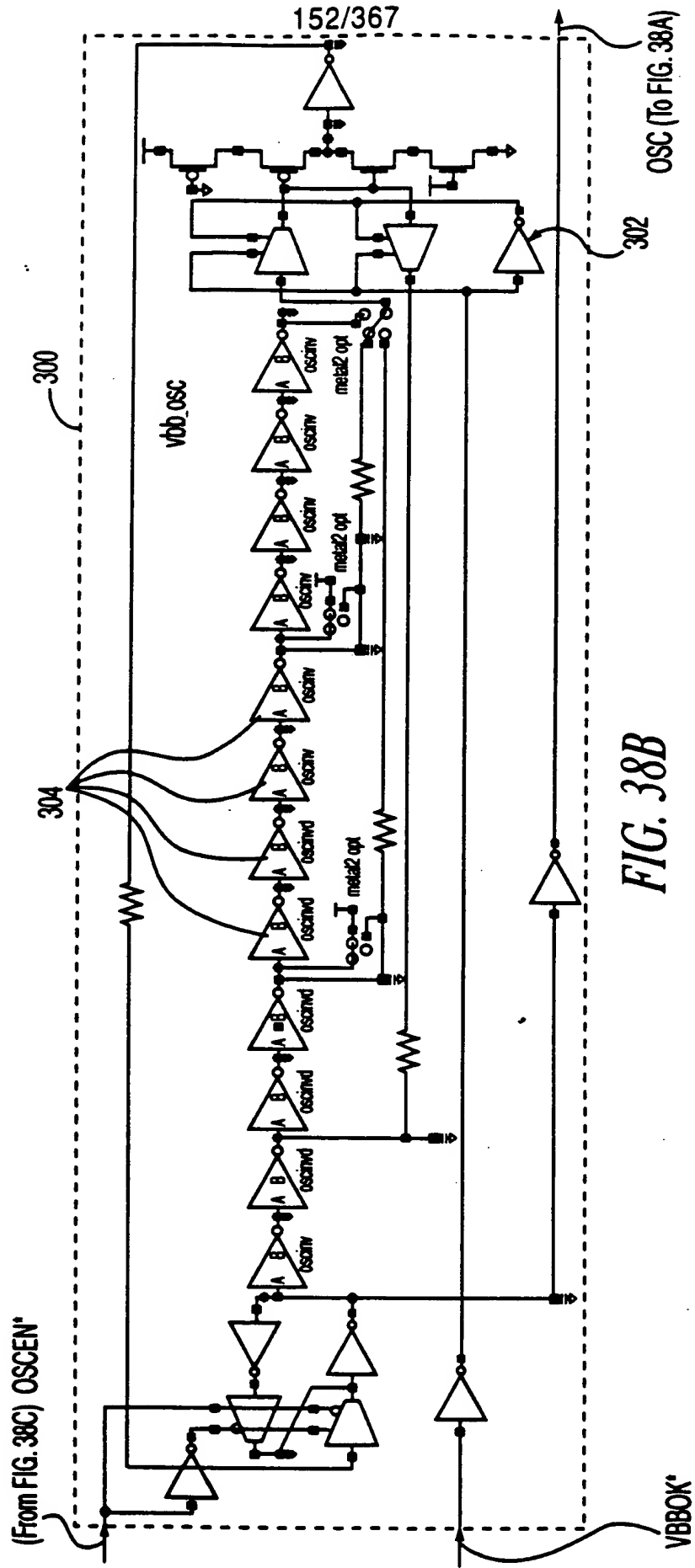


FIG. 38A

(From FIG. 38B)





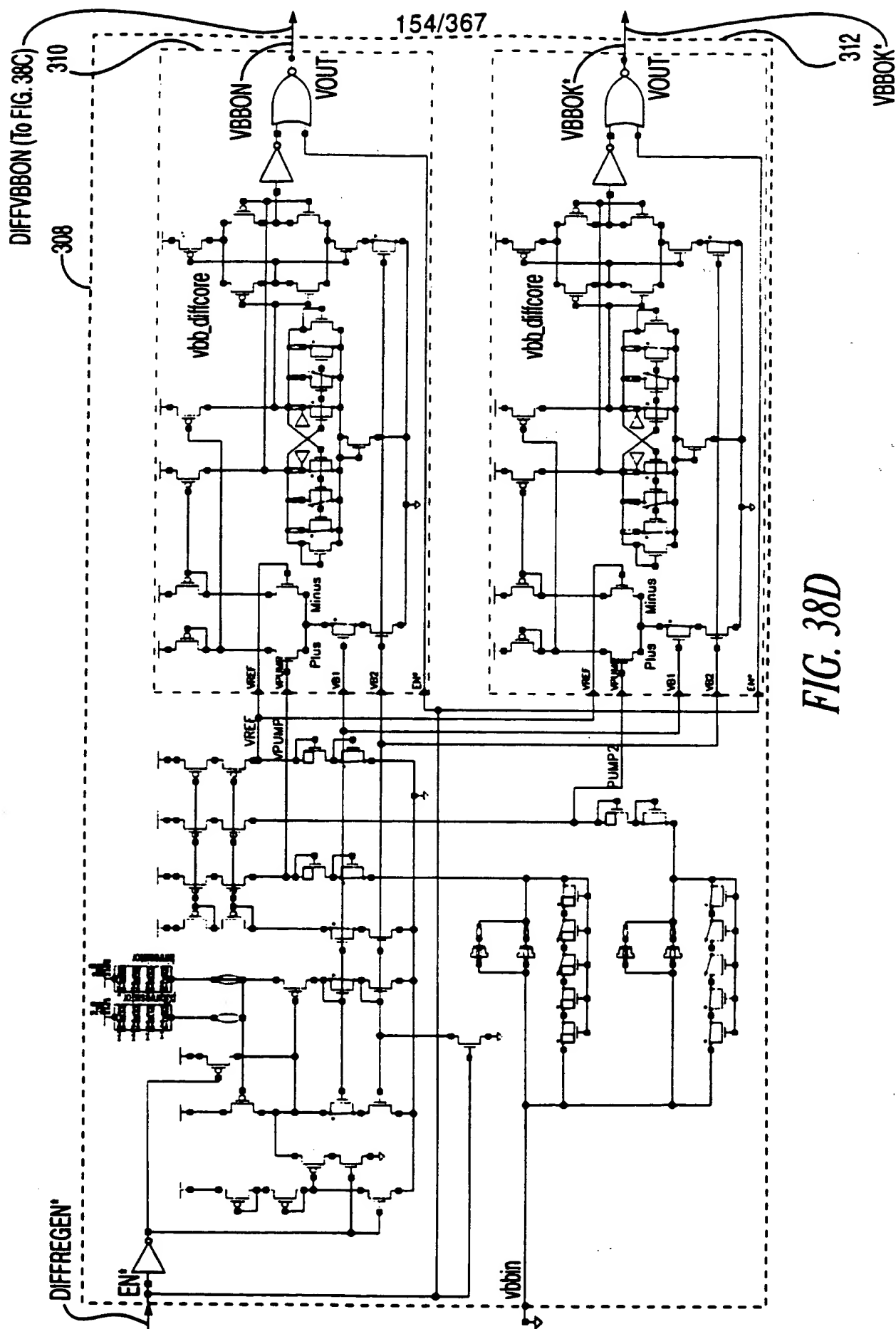


FIG. 38D

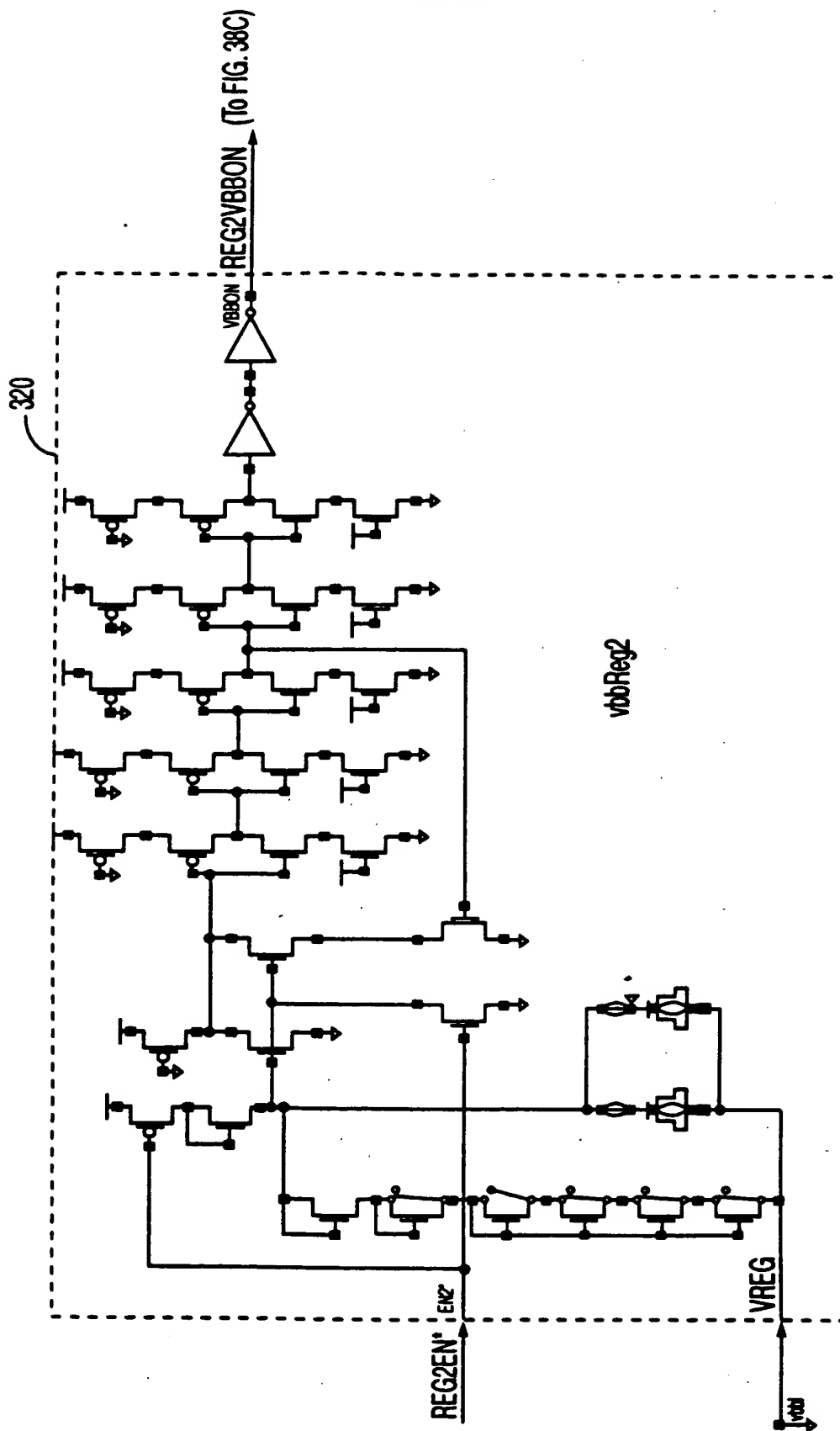


FIG. 38E

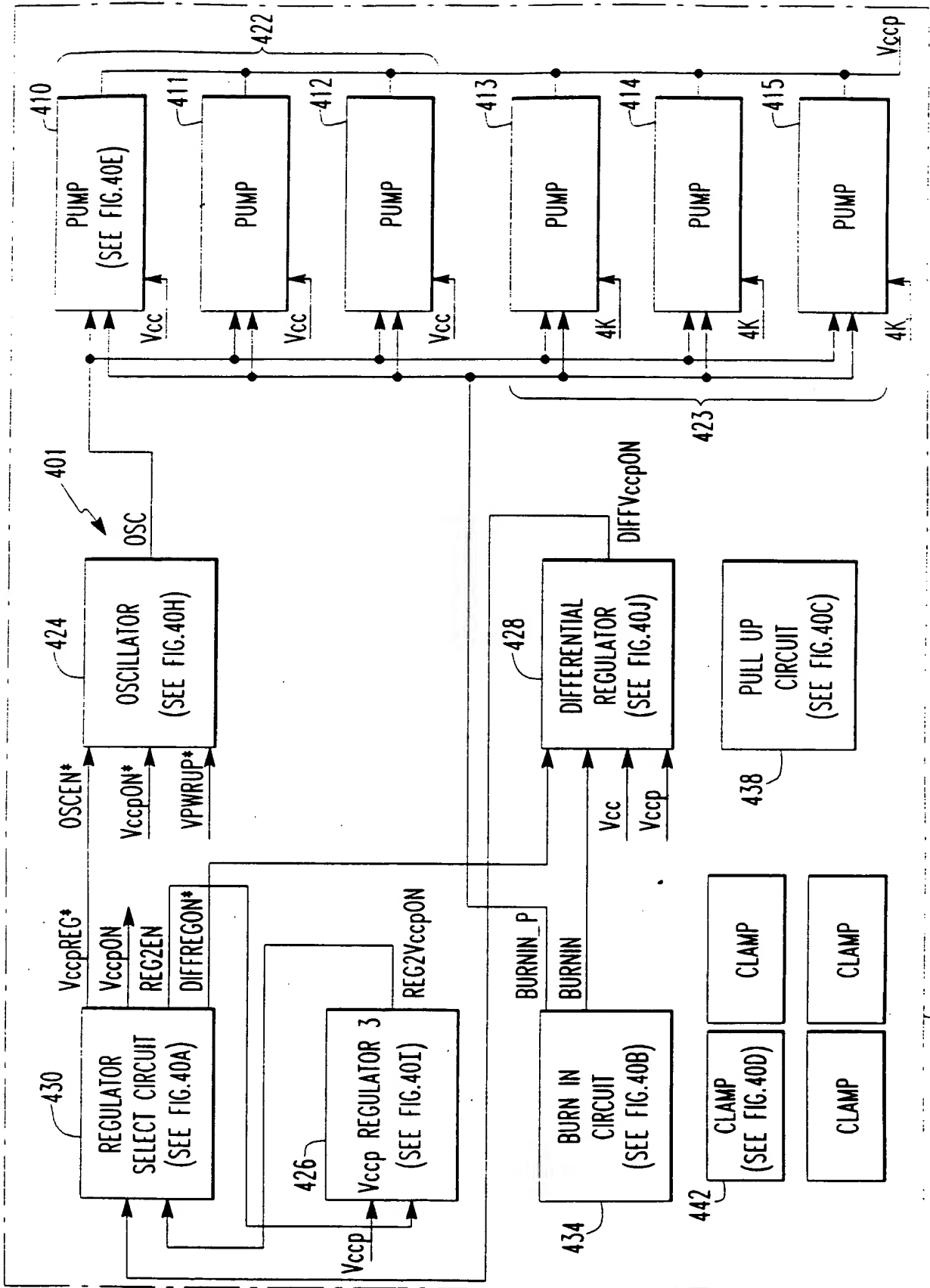


FIG. 39

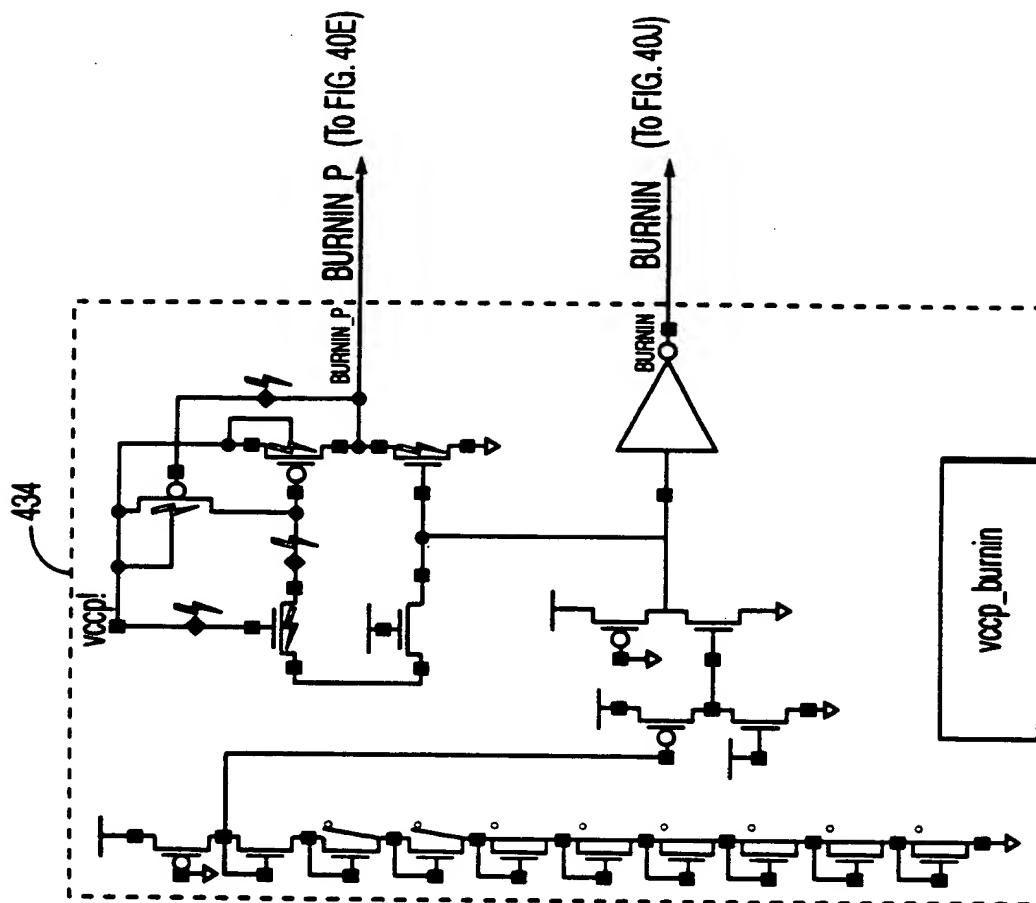


FIG. 40B

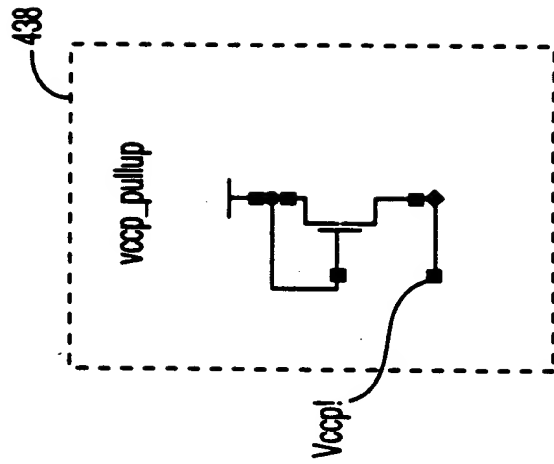


FIG. 40C

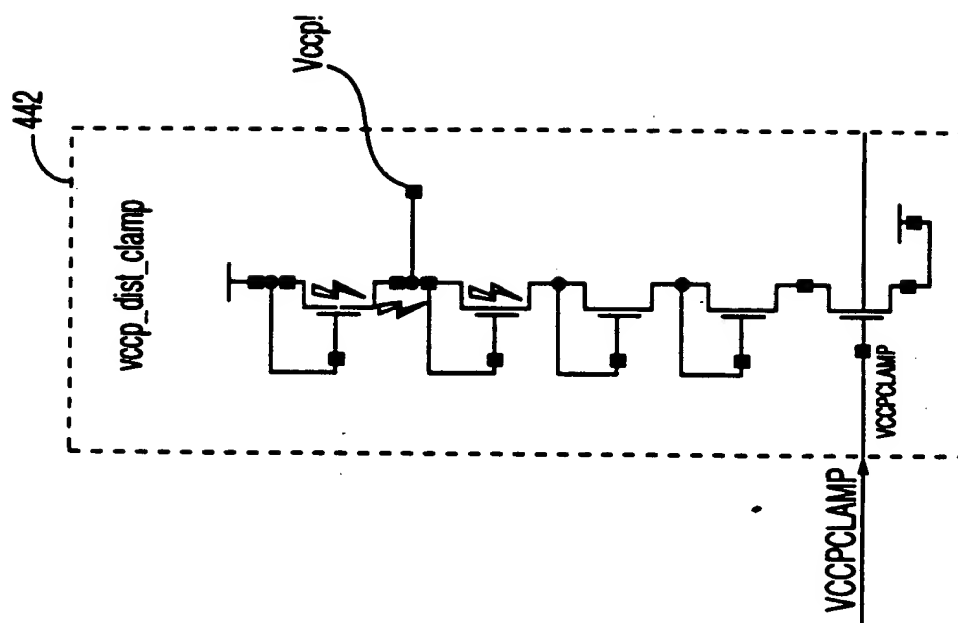
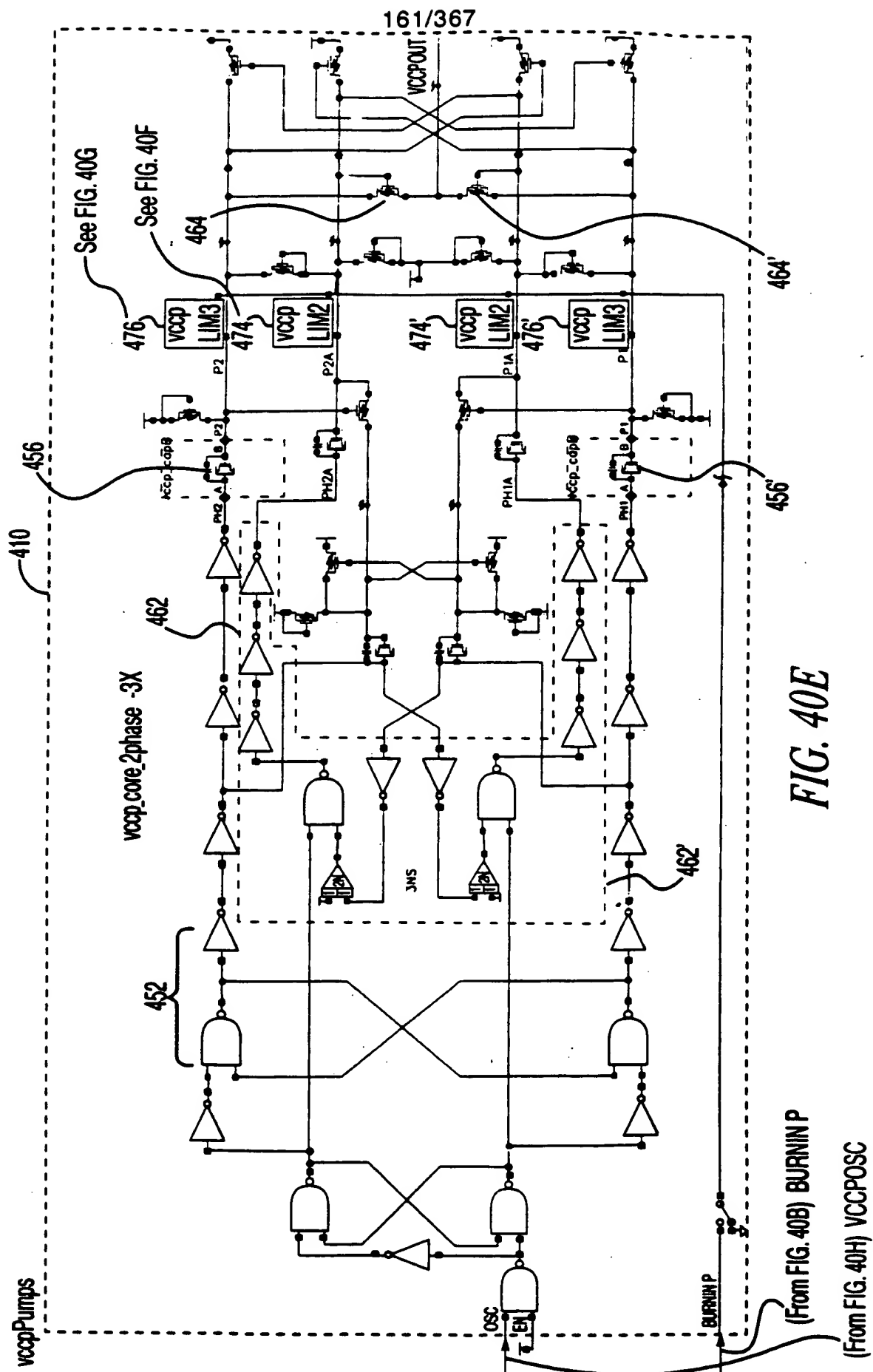


FIG. 40D



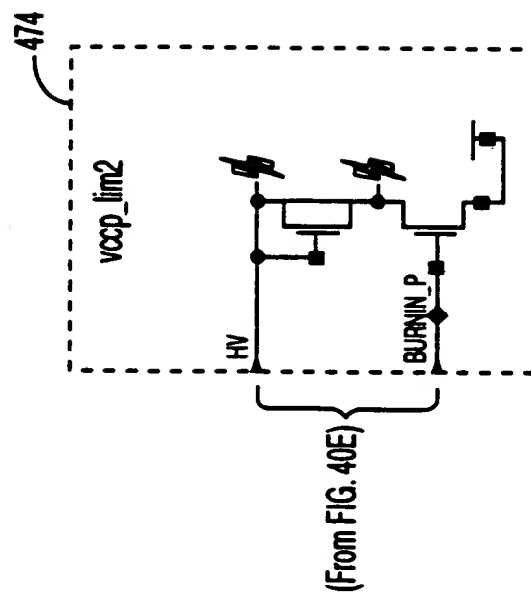


FIG. 40F

FIG. 40F

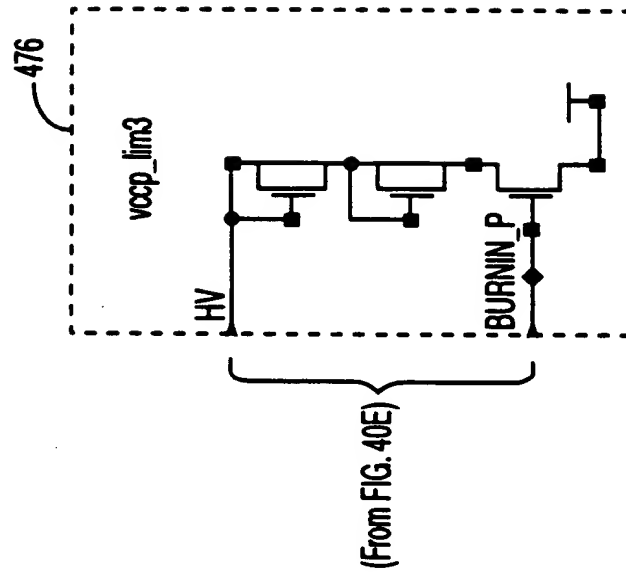


FIG. 40G

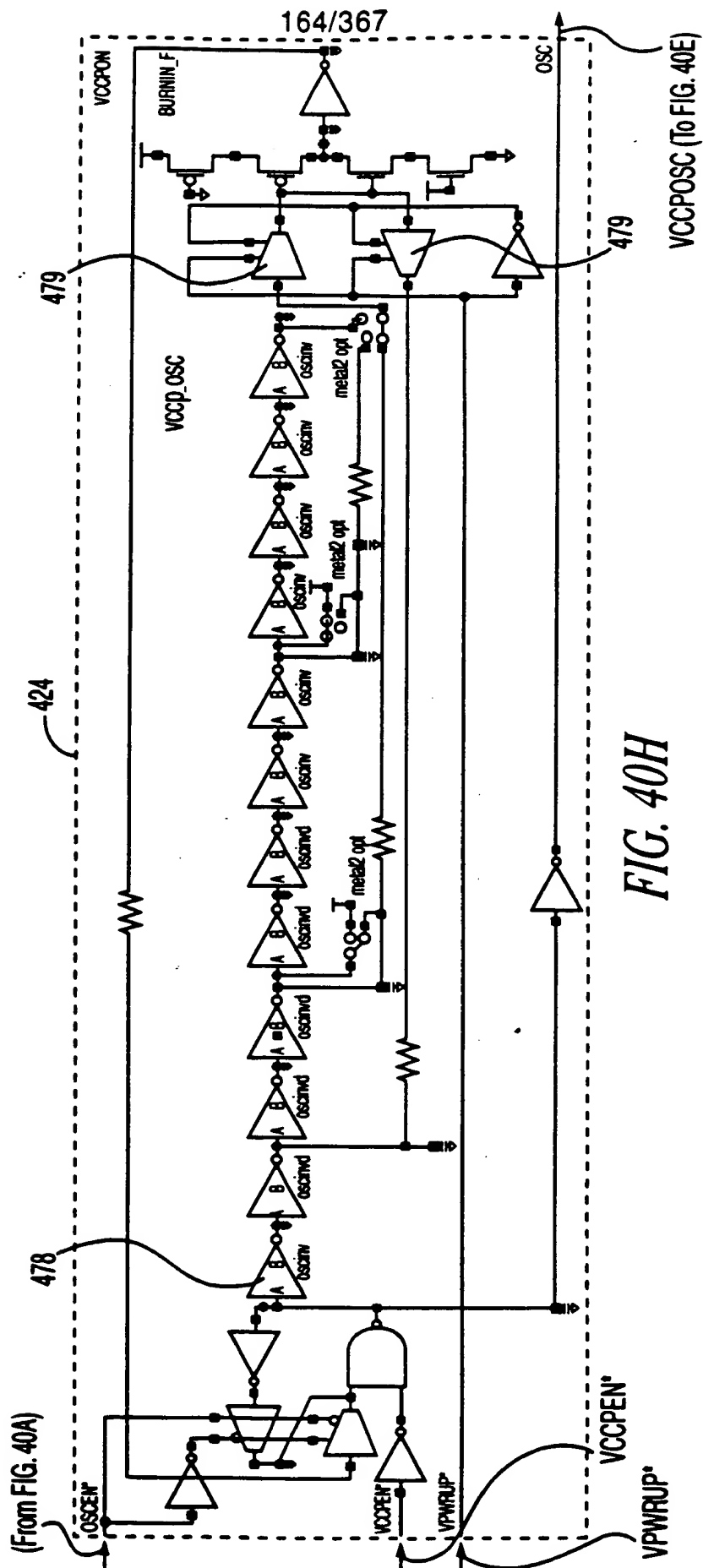


FIG. 40H

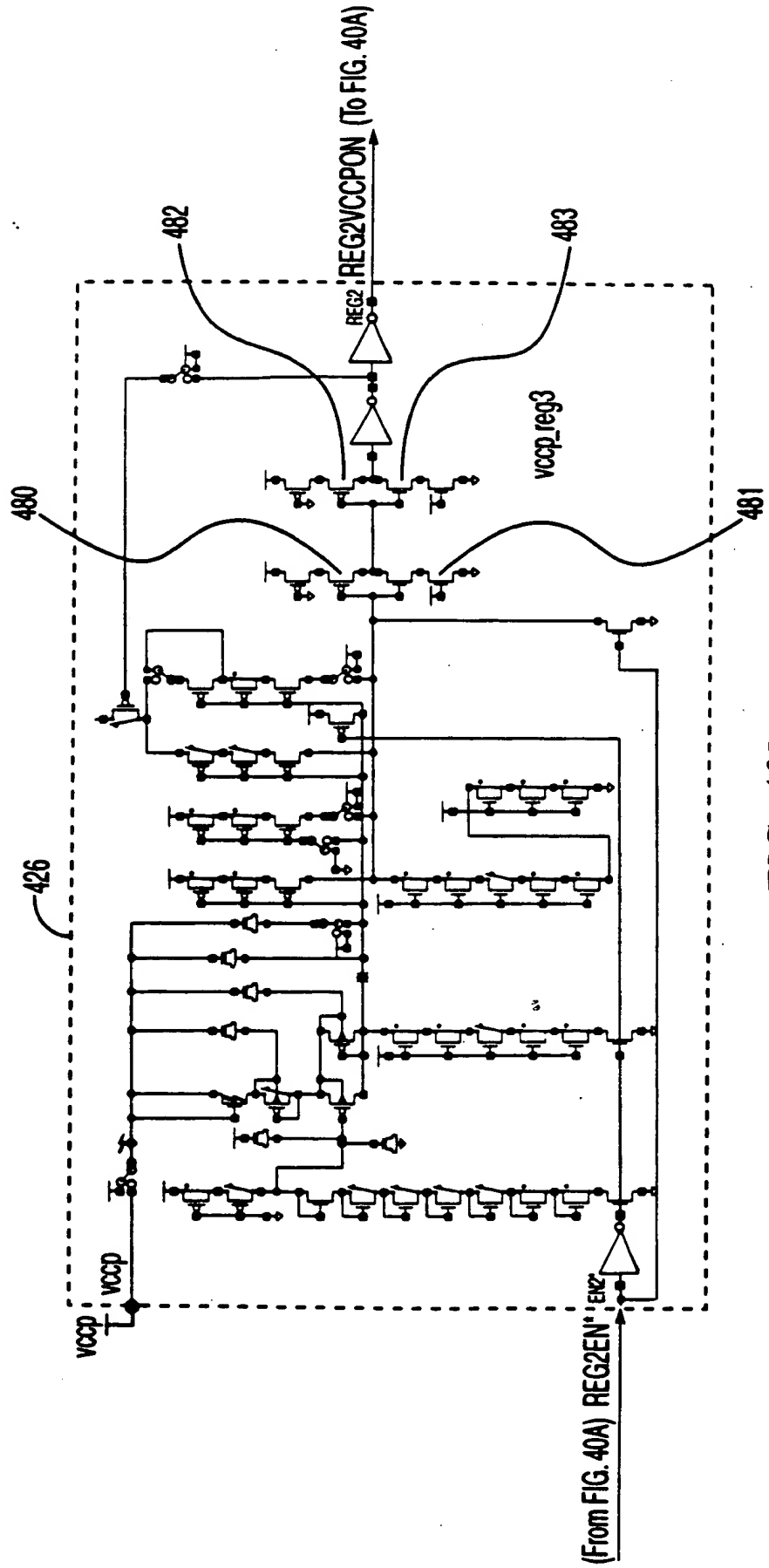


FIG. 401

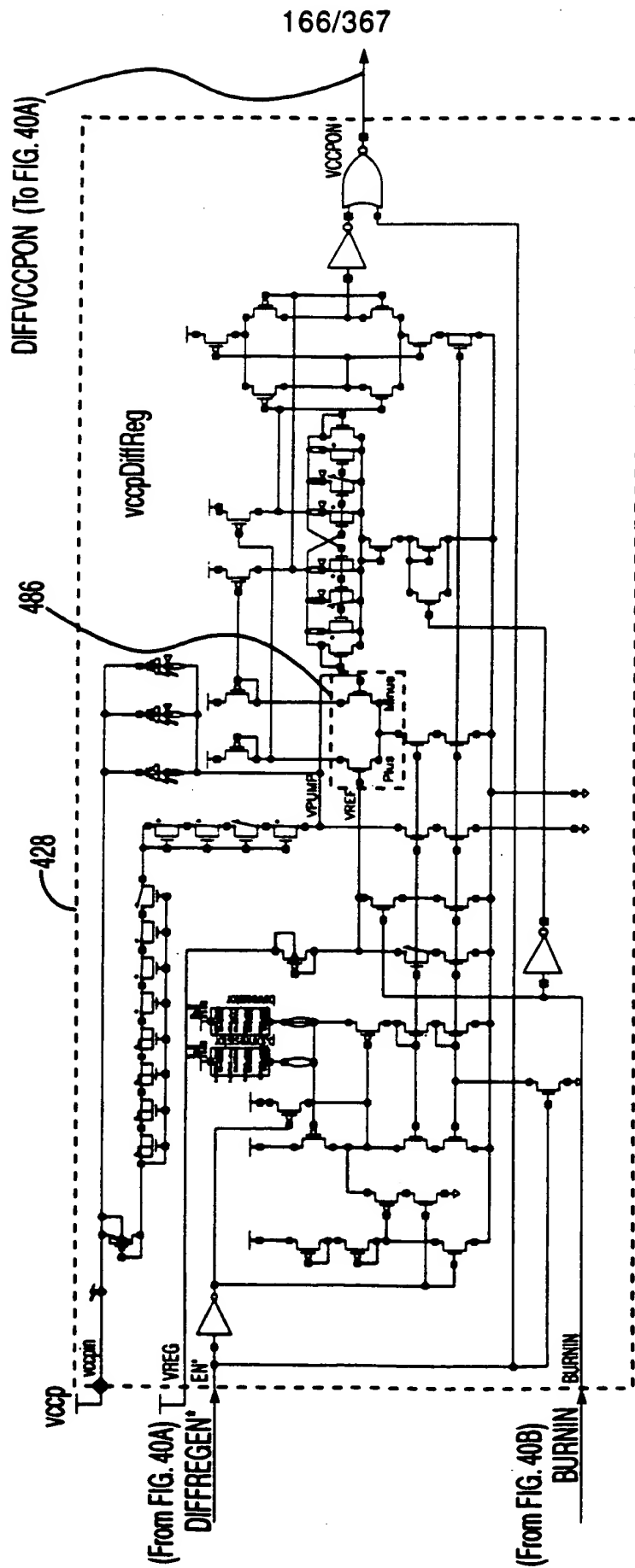
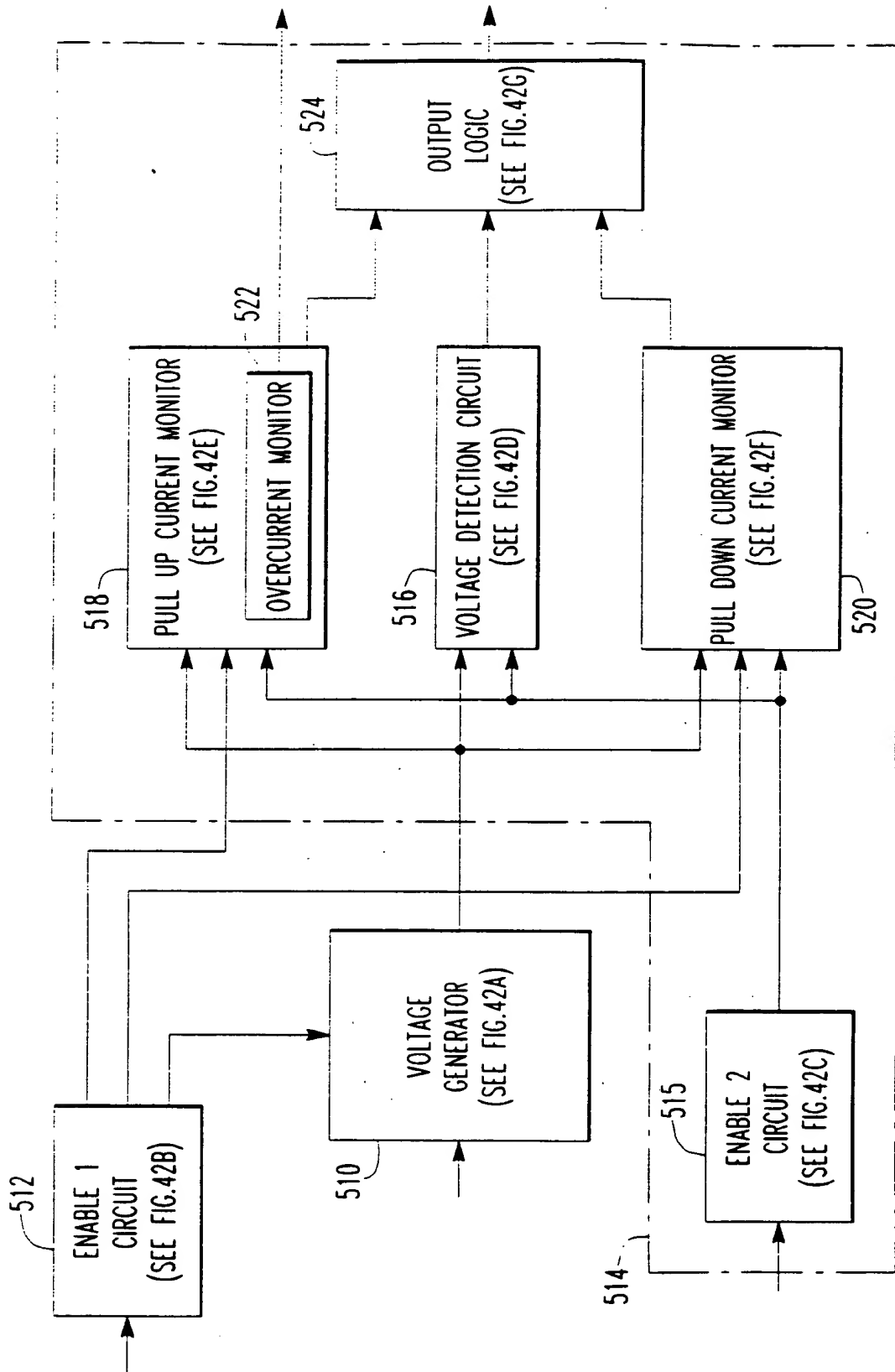


FIG. 40J

FIG. 41



500

FIG. 41

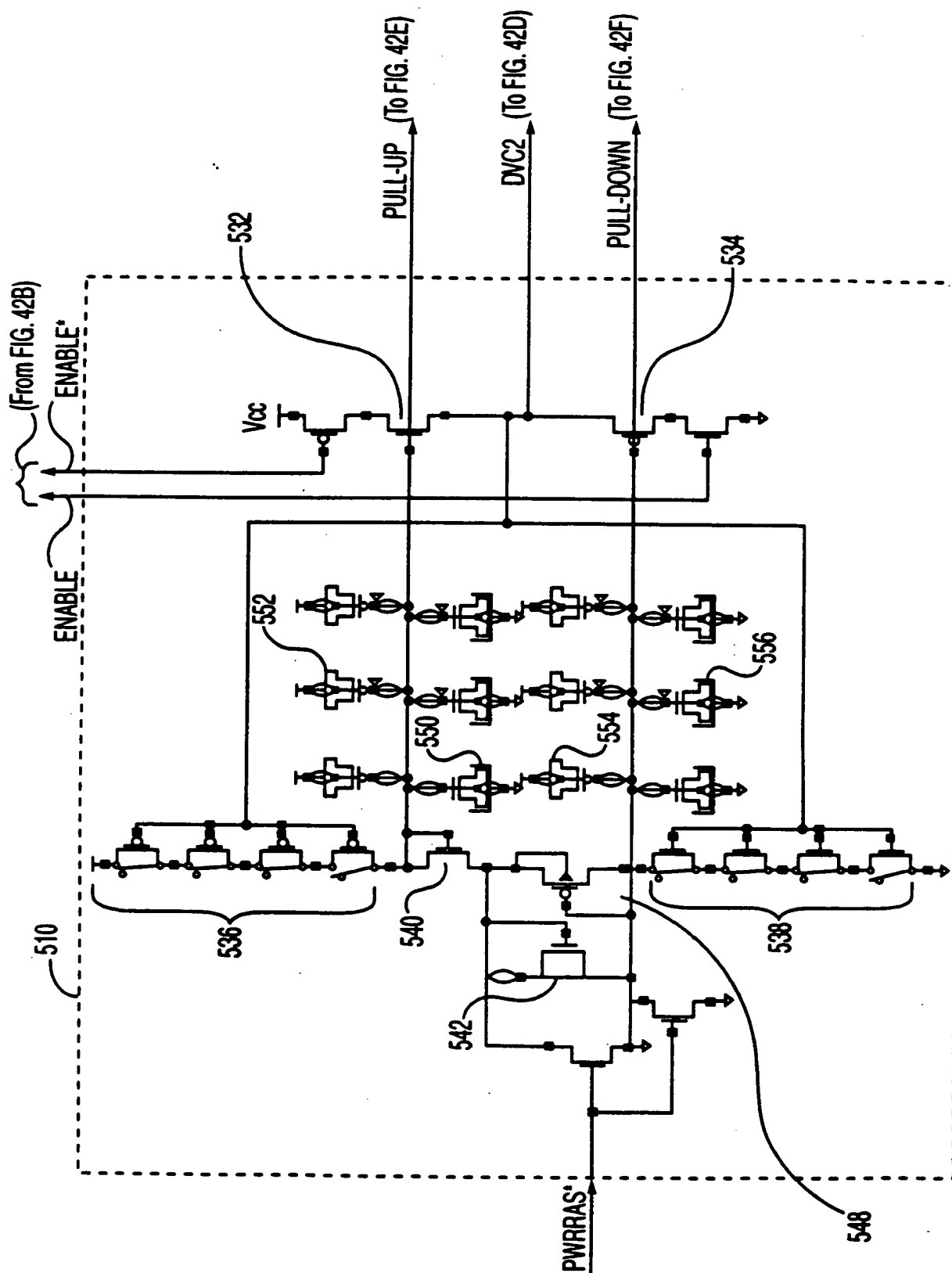


FIG. 42A

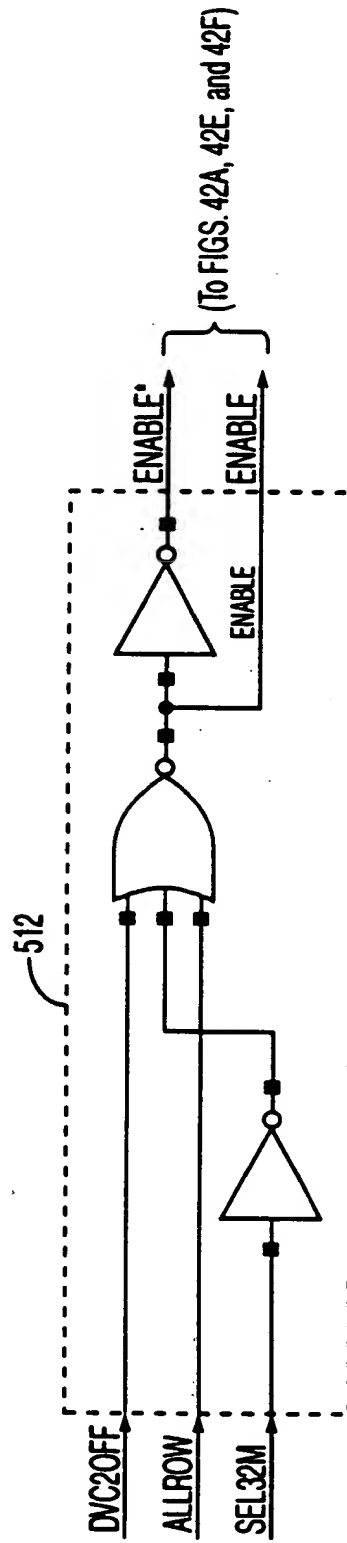


FIG. 42B

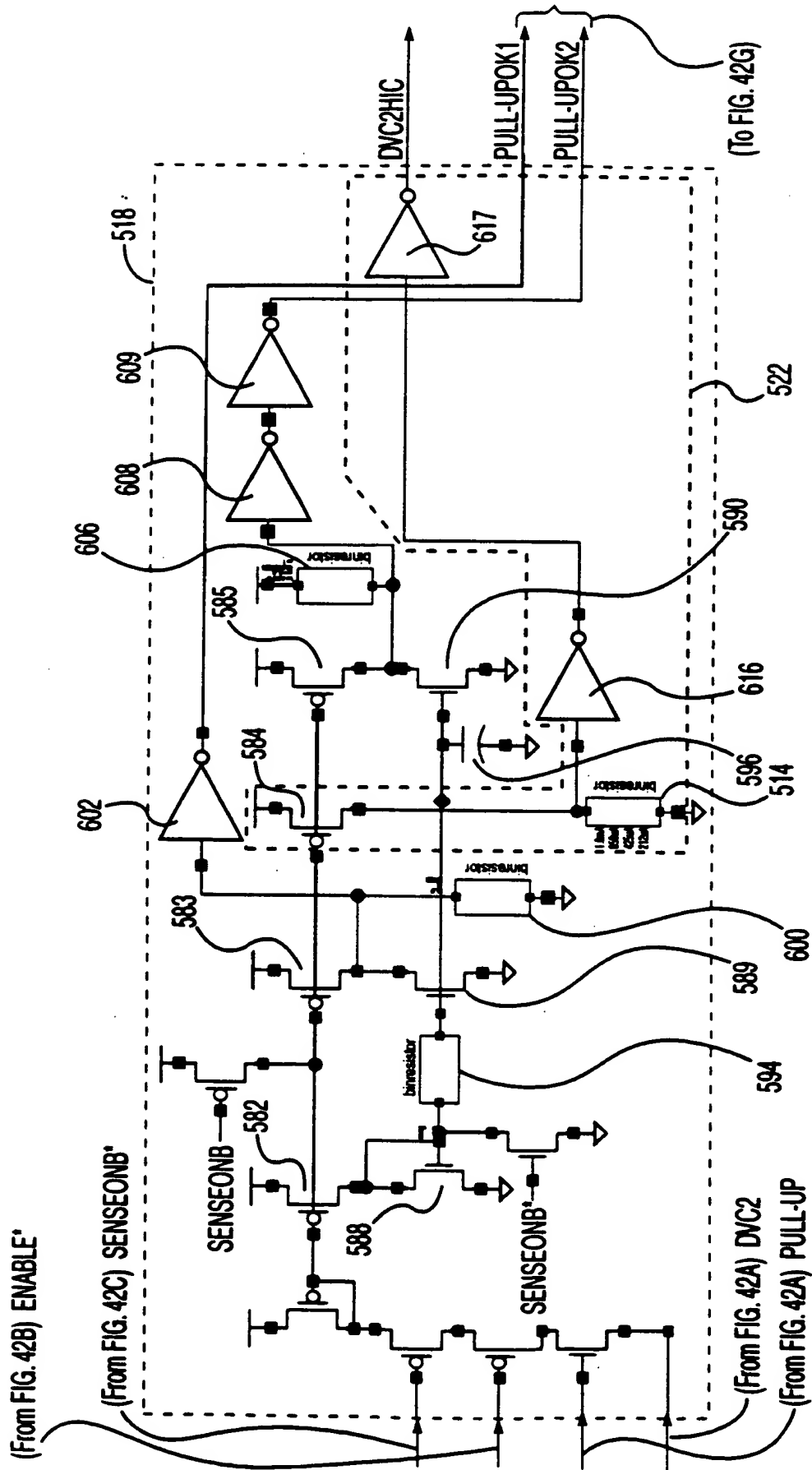


FIG. 42E

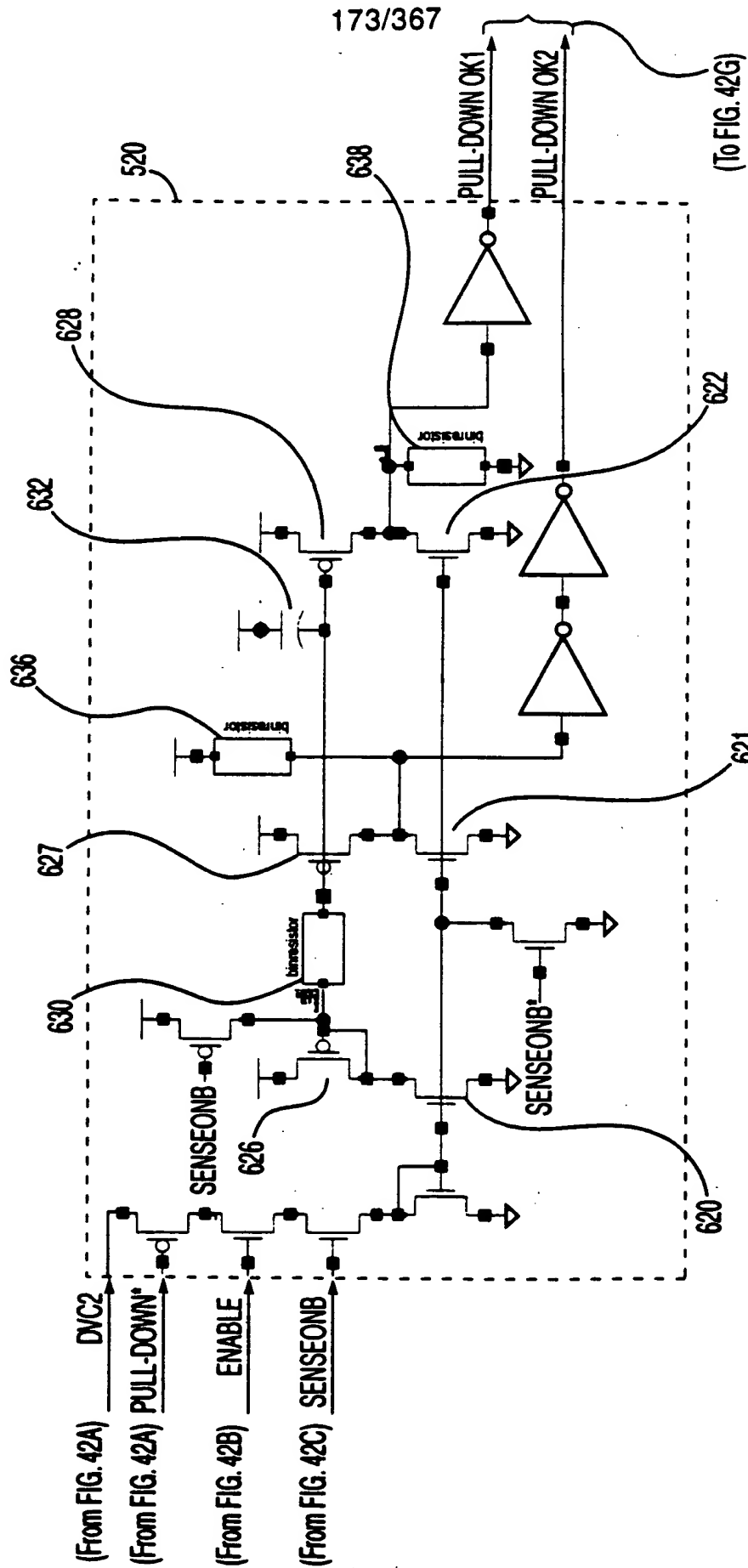


FIG. 42F

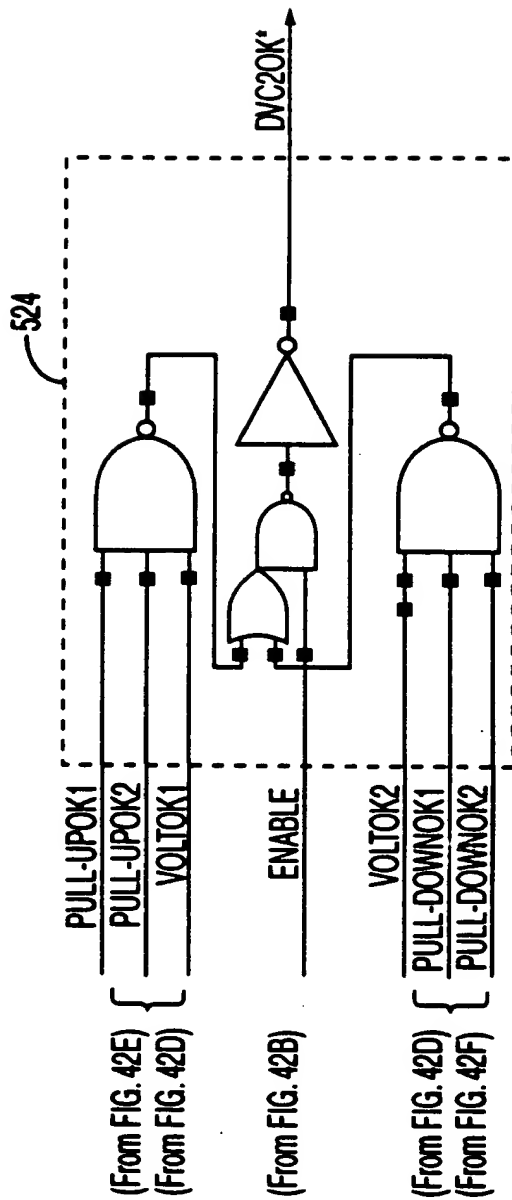


FIG. 42G

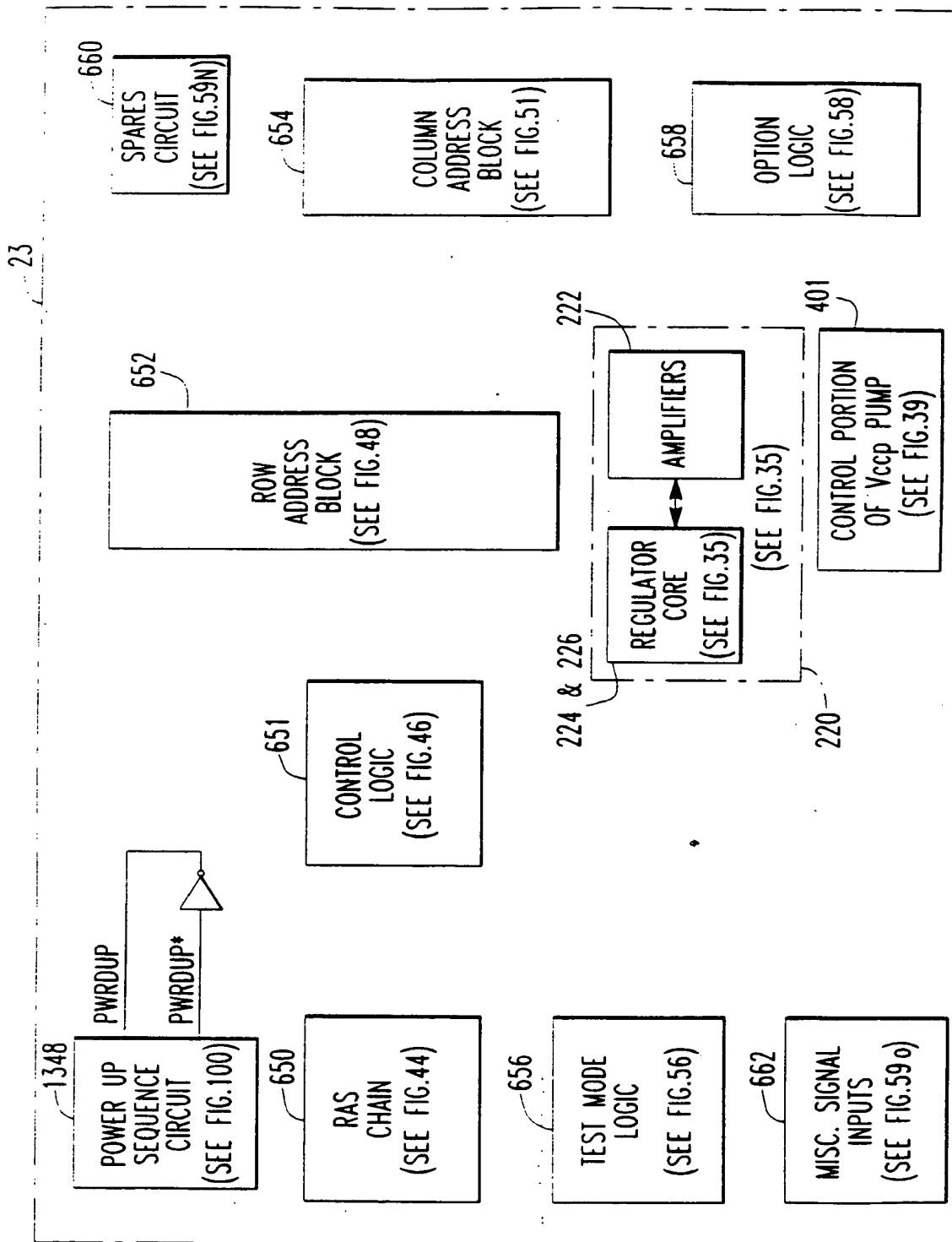
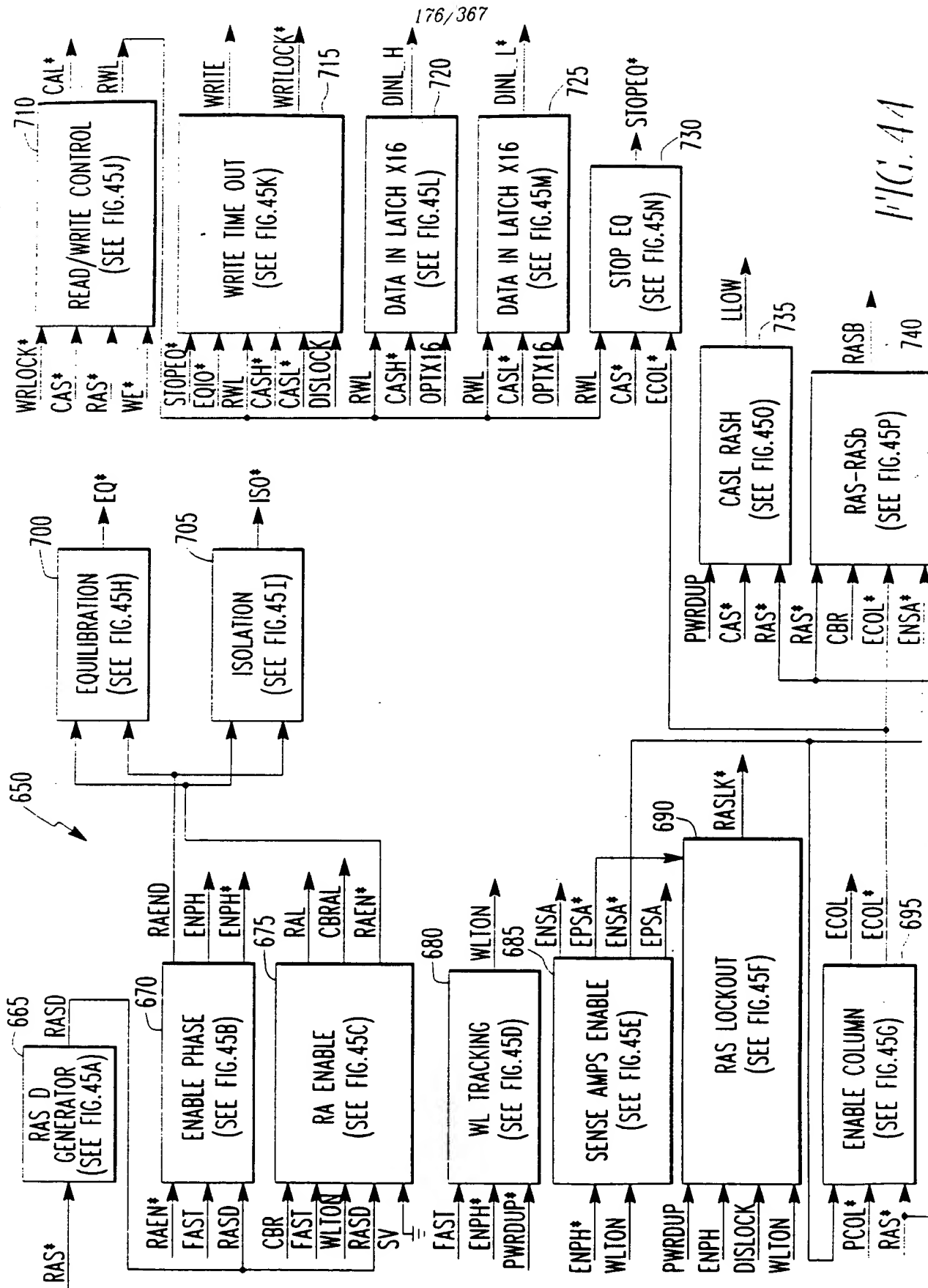


FIG. 43



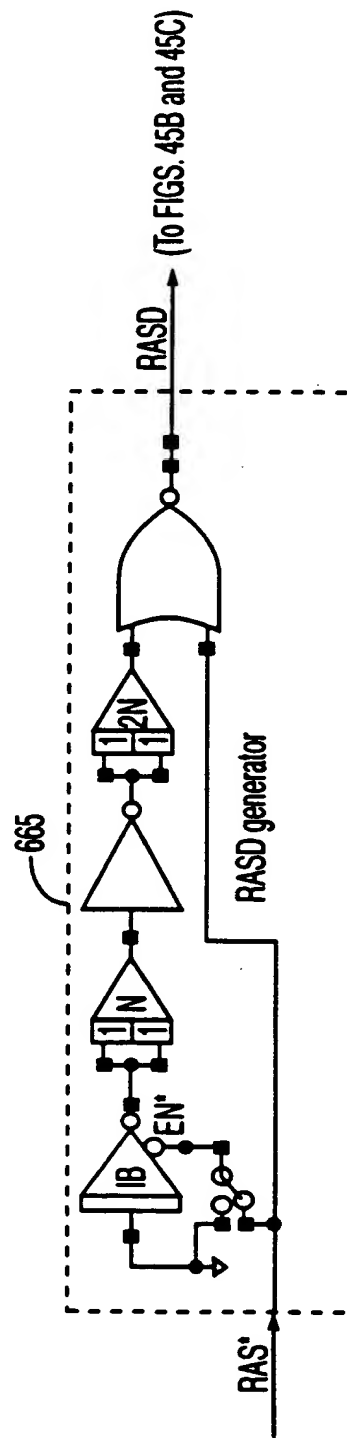


FIG. 45A





FIG. 45C

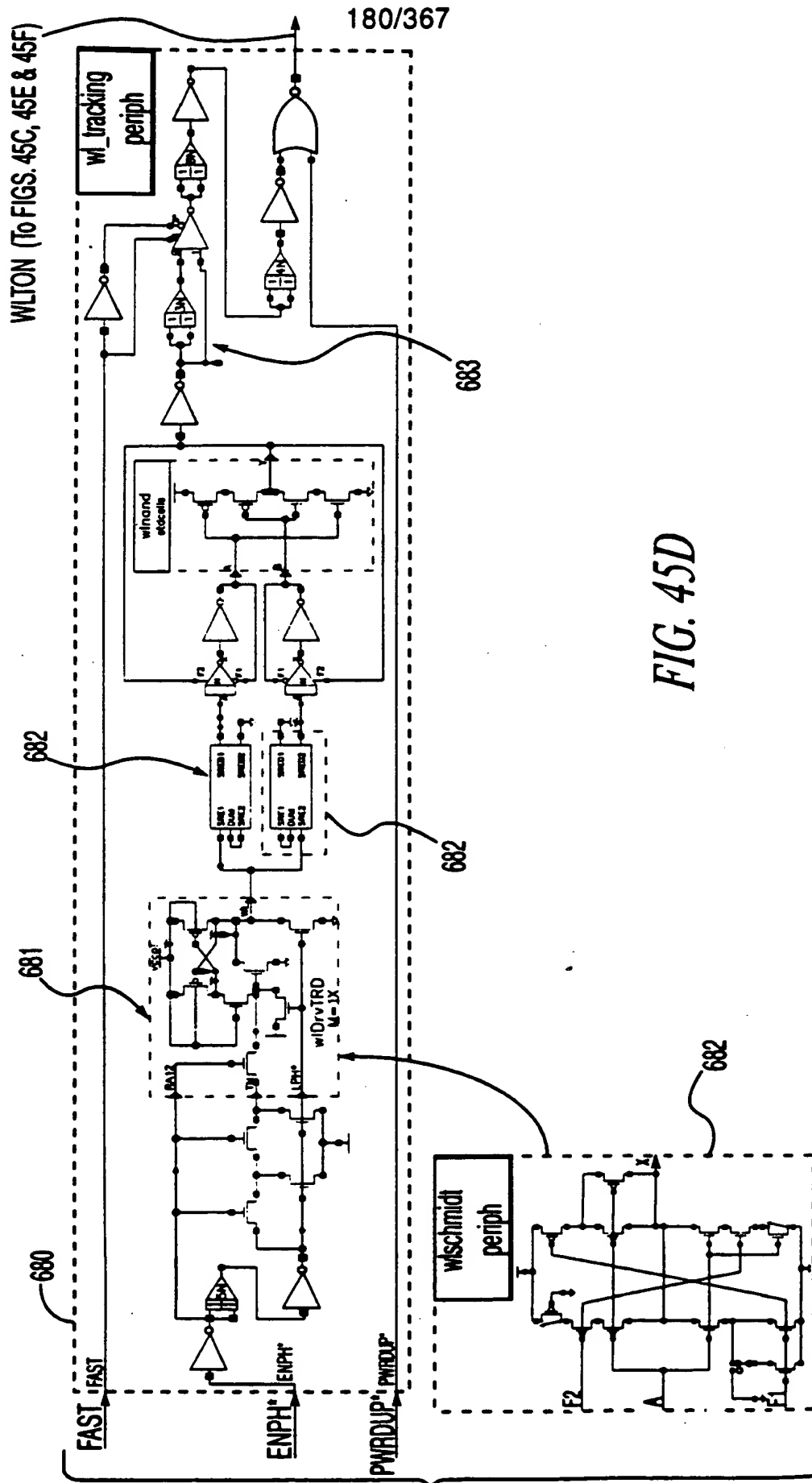


FIG. 45D

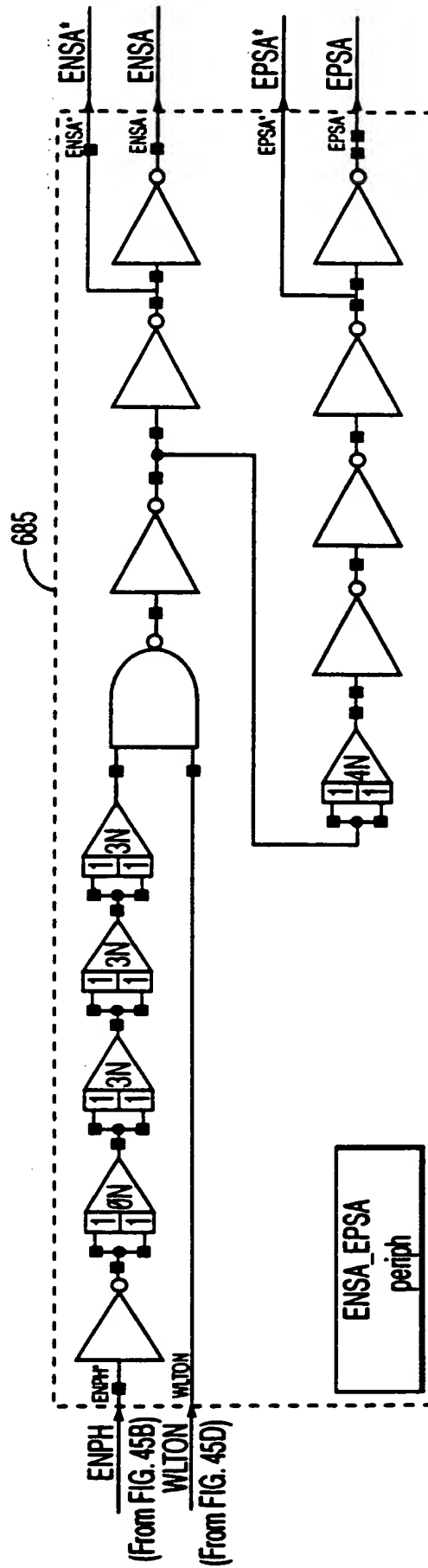


FIG. 45E

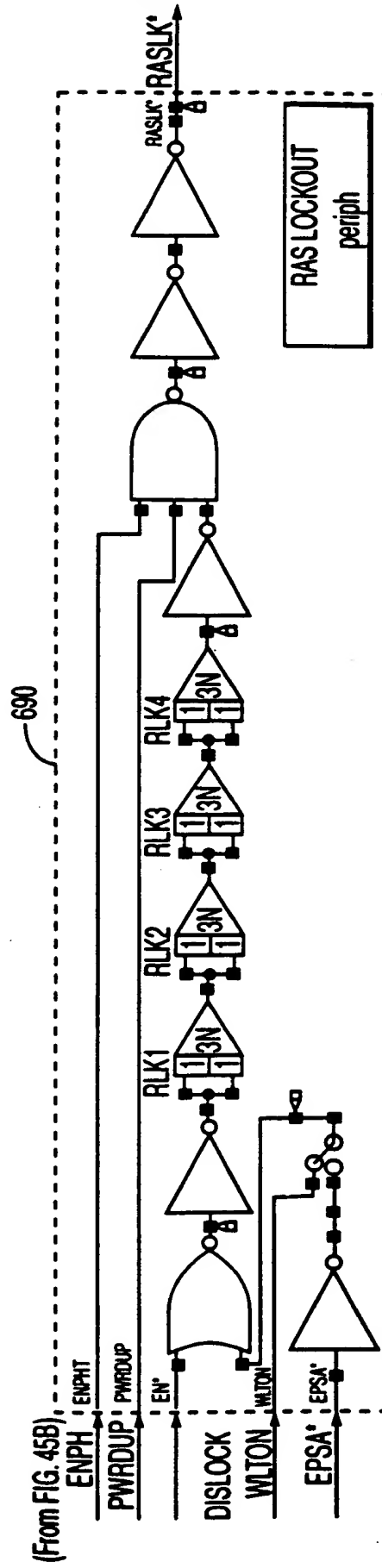


FIG. 45F

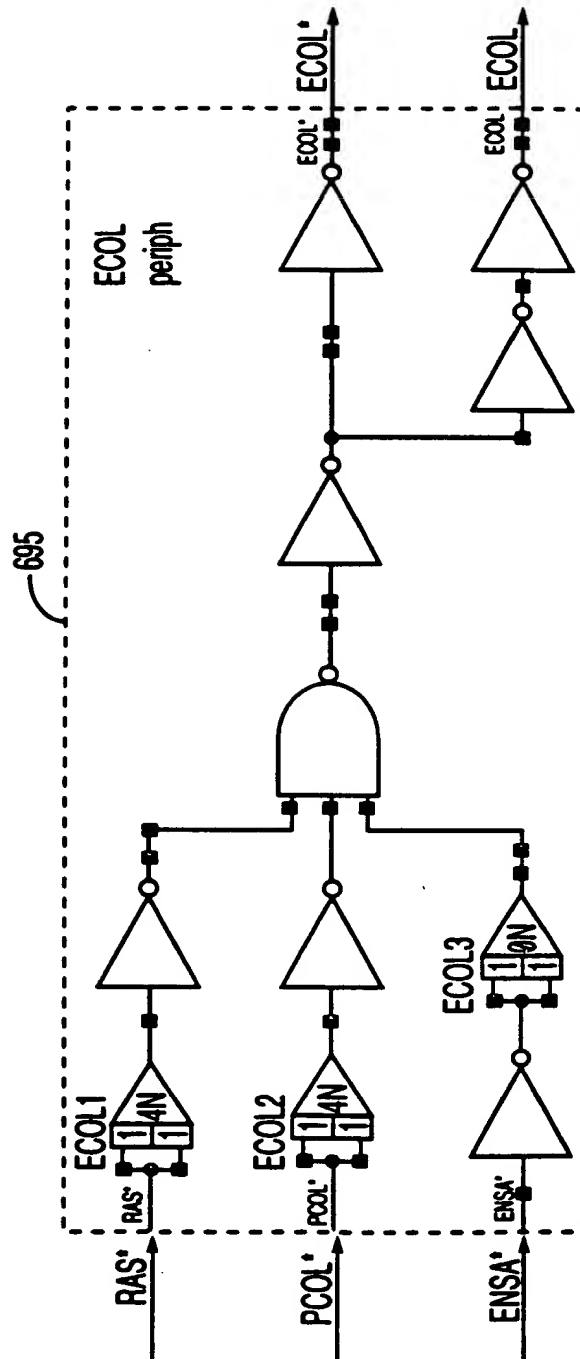


FIG. 45G



FIG. 45H

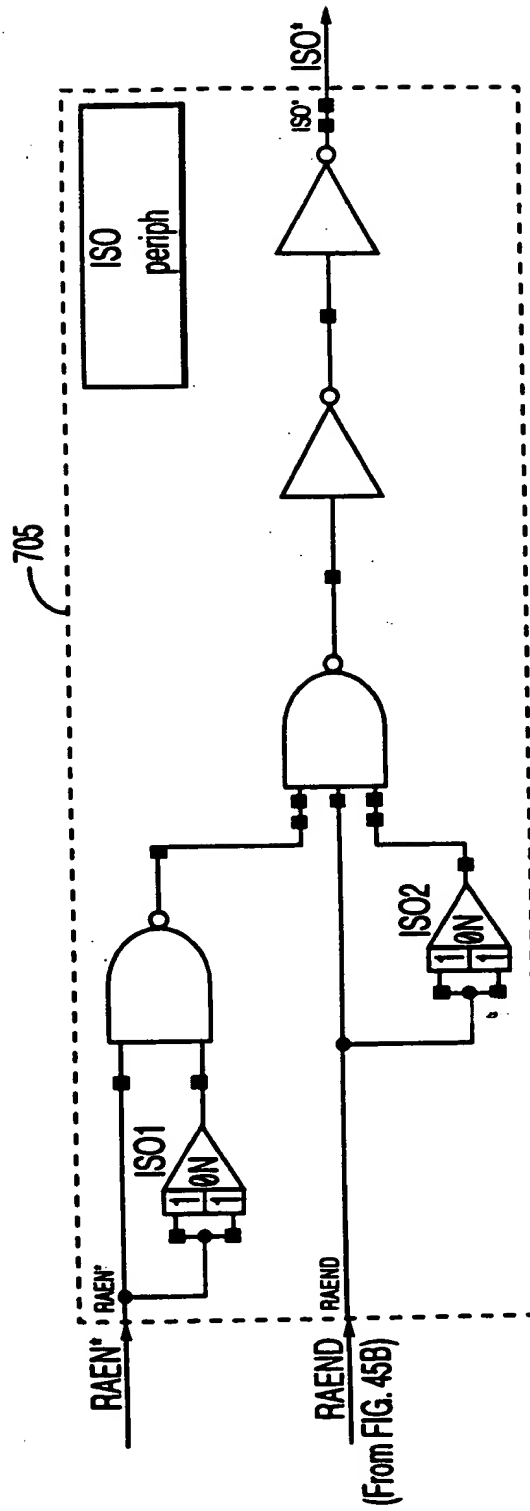


FIG. 45I



FIG. 45J

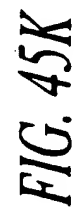


FIG. 45K

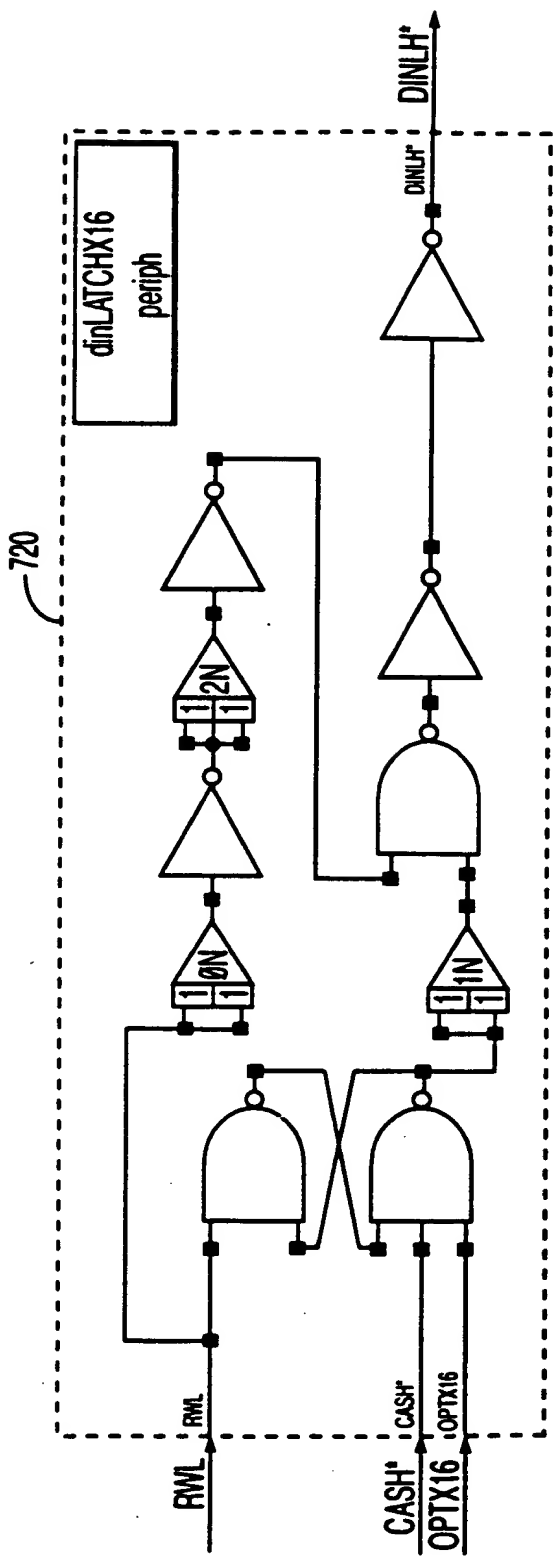


FIG. 45L

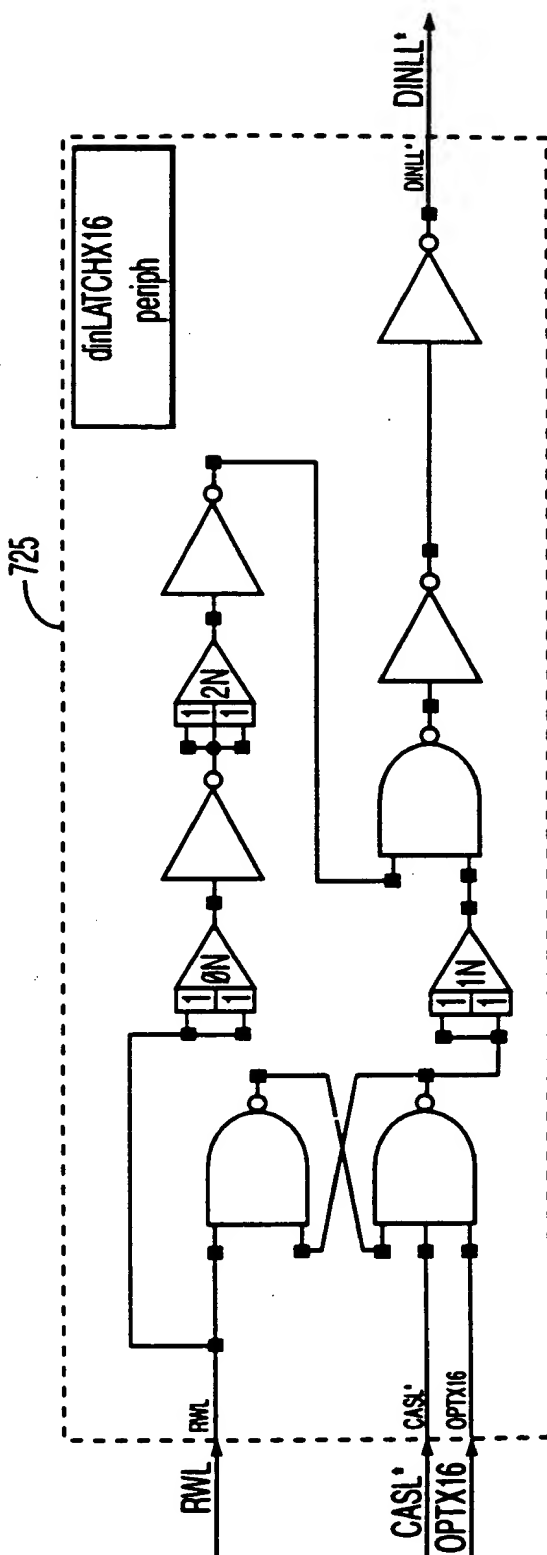


FIG. 45M

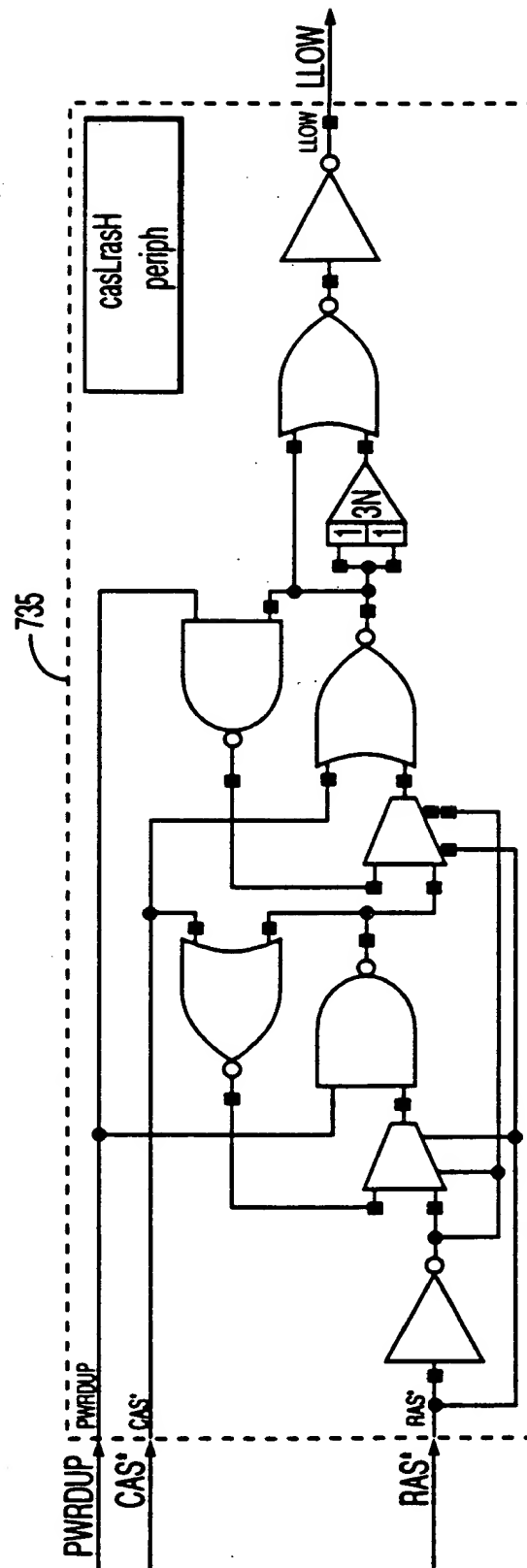


FIG. 450

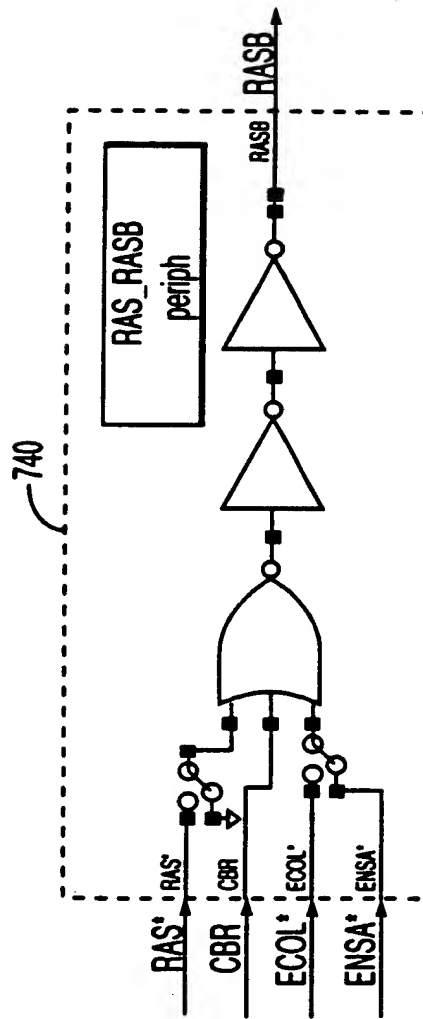


FIG. 45P

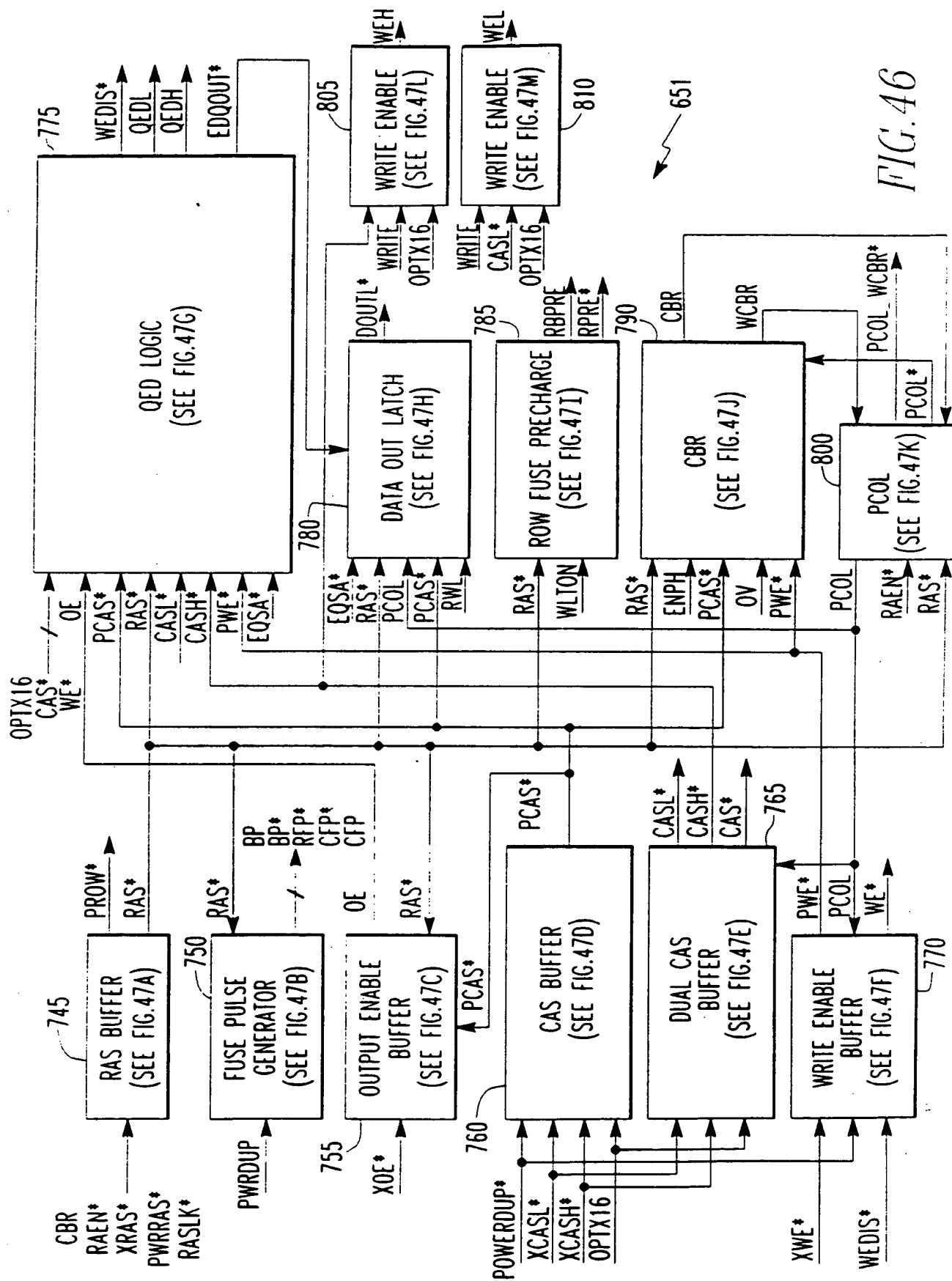


FIG. 46

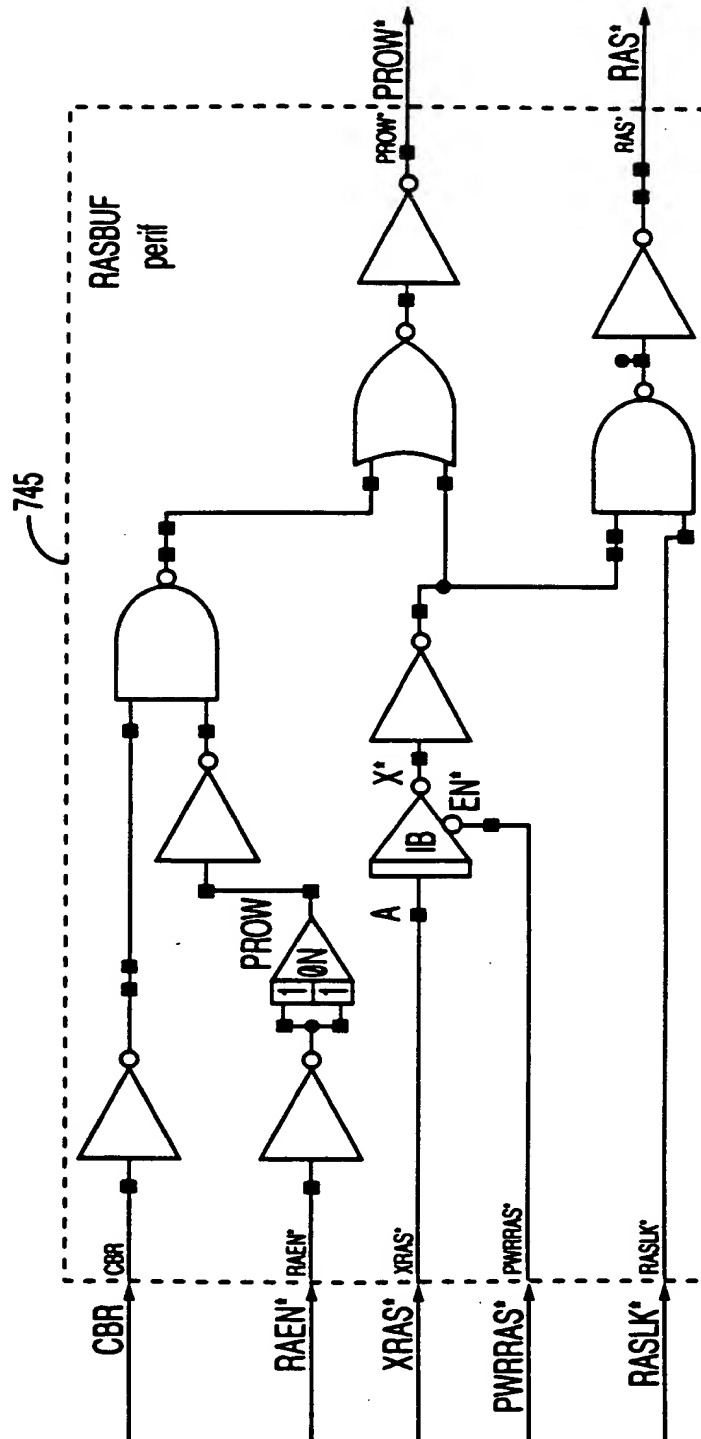


FIG. 47A

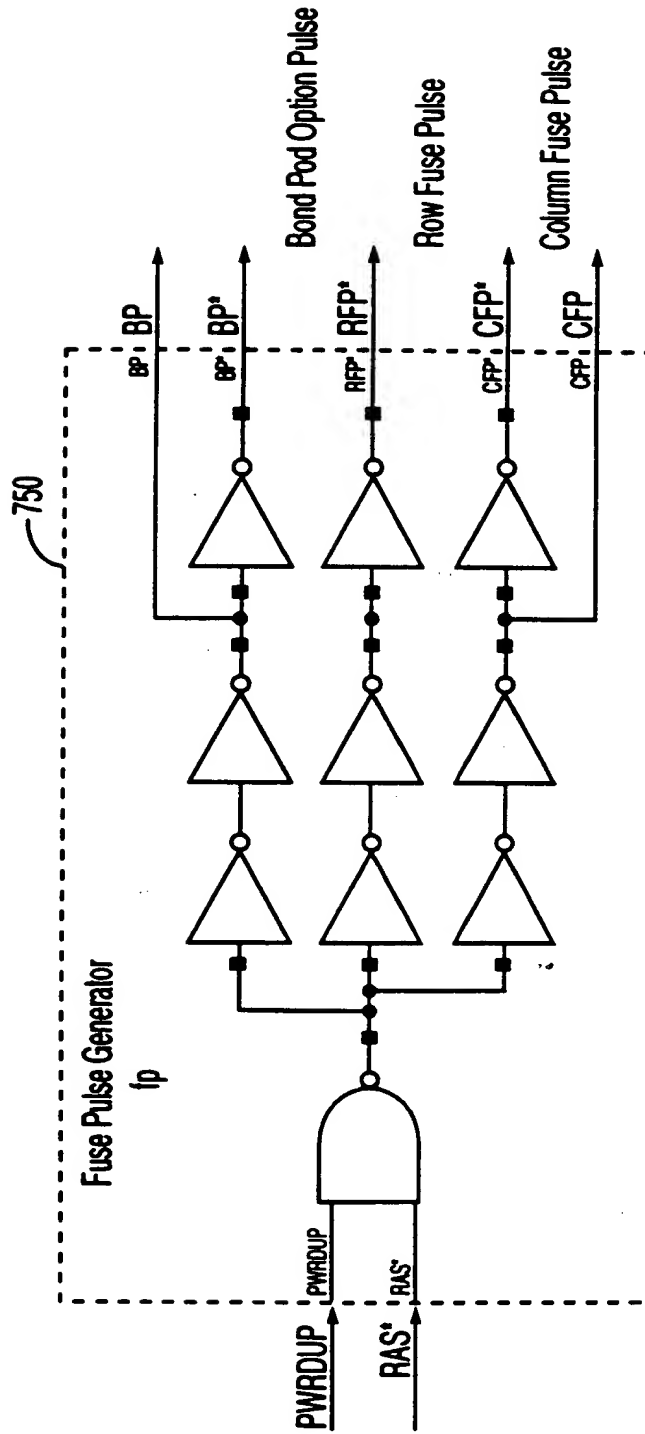
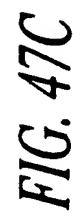


FIG. 47B



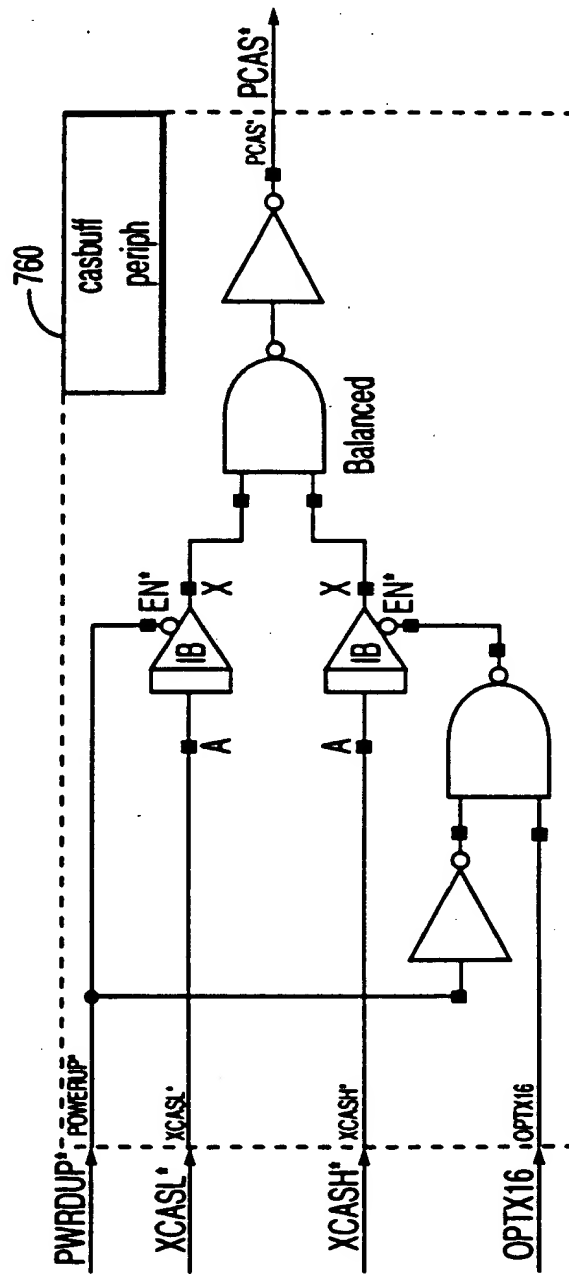


FIG. 47D

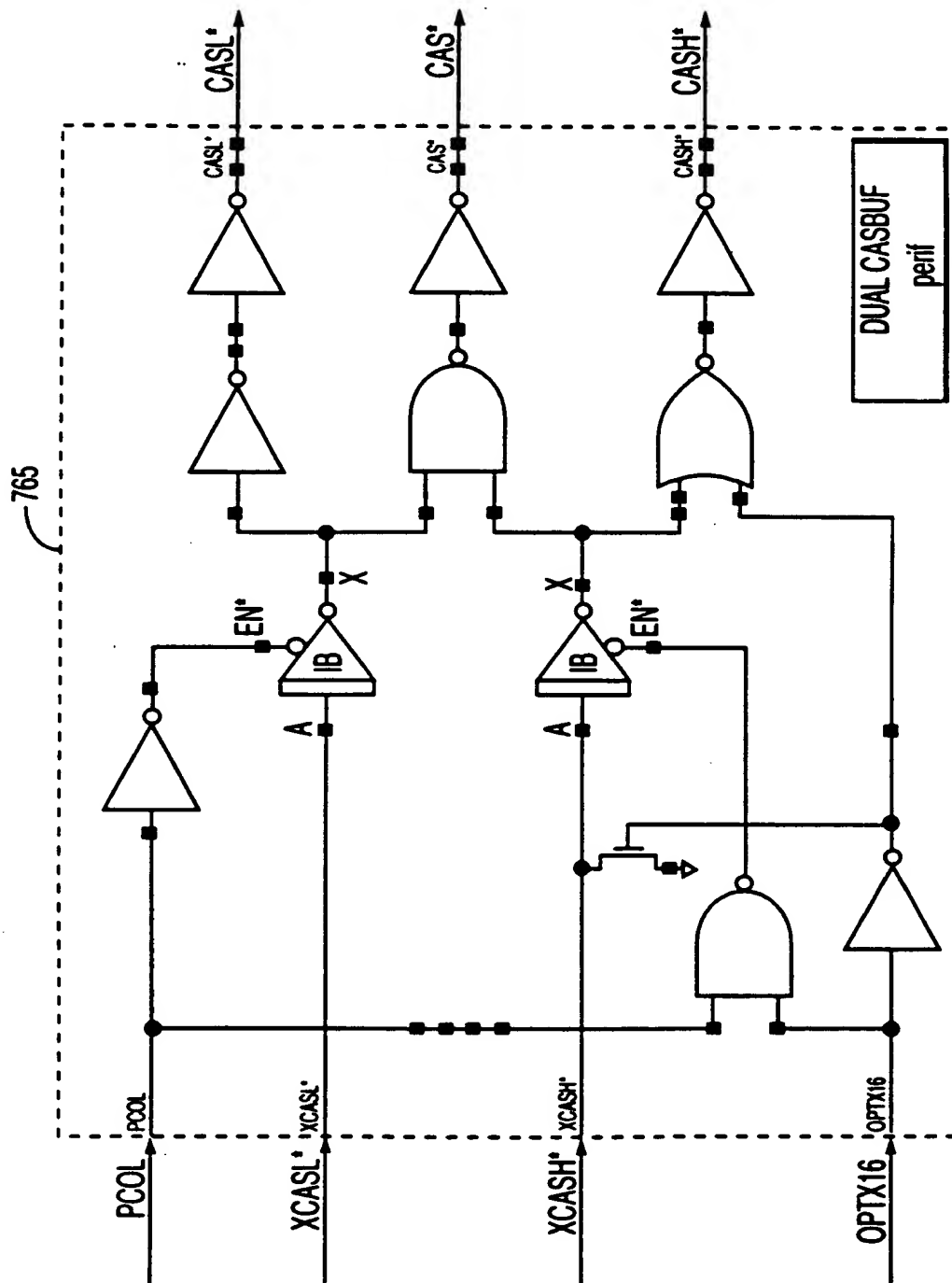


FIG. 47E

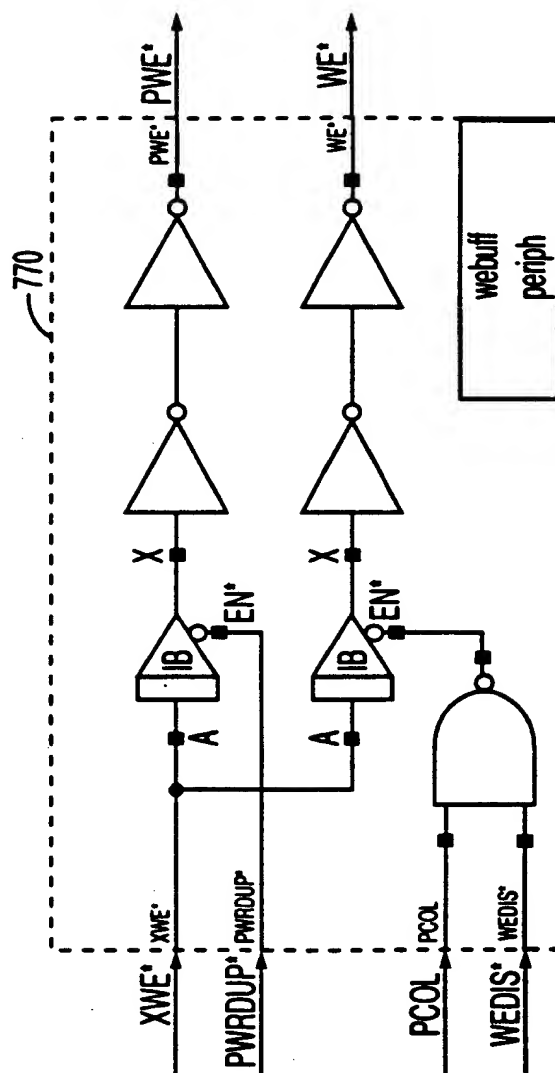


FIG. 47F

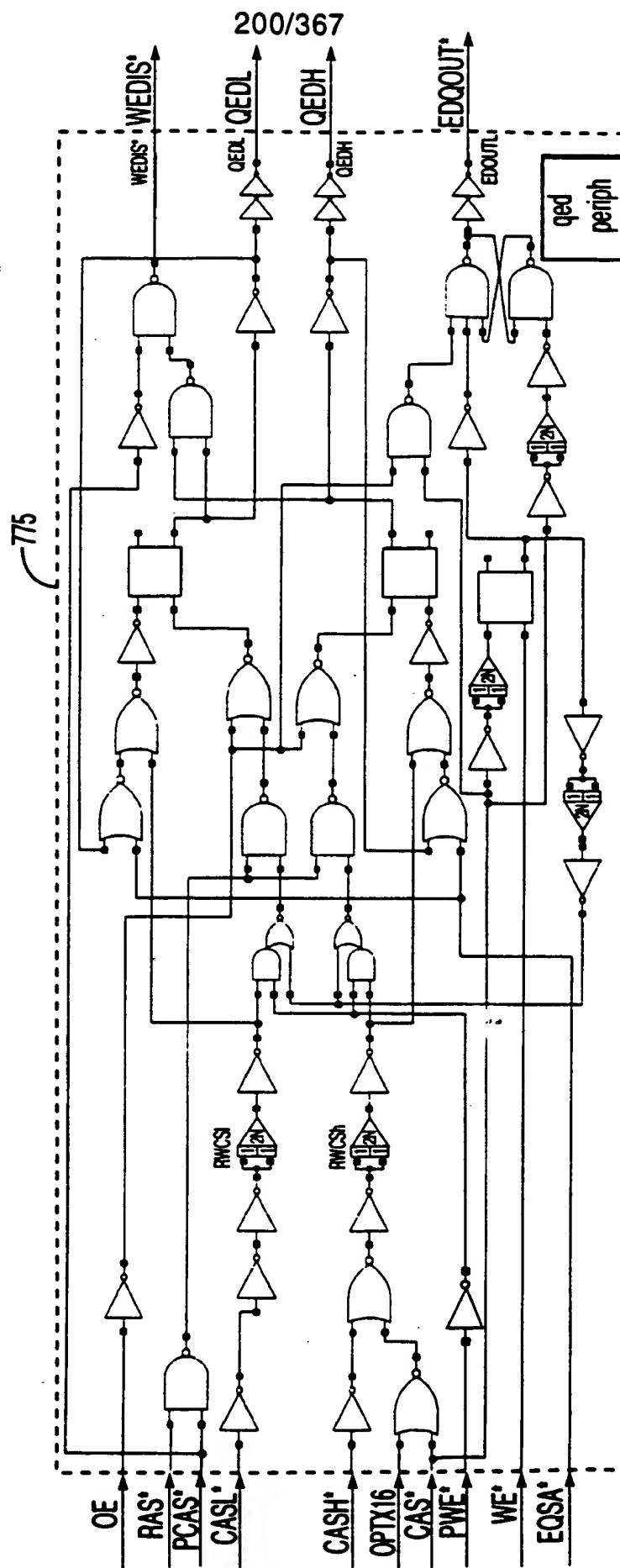


FIG. 47G

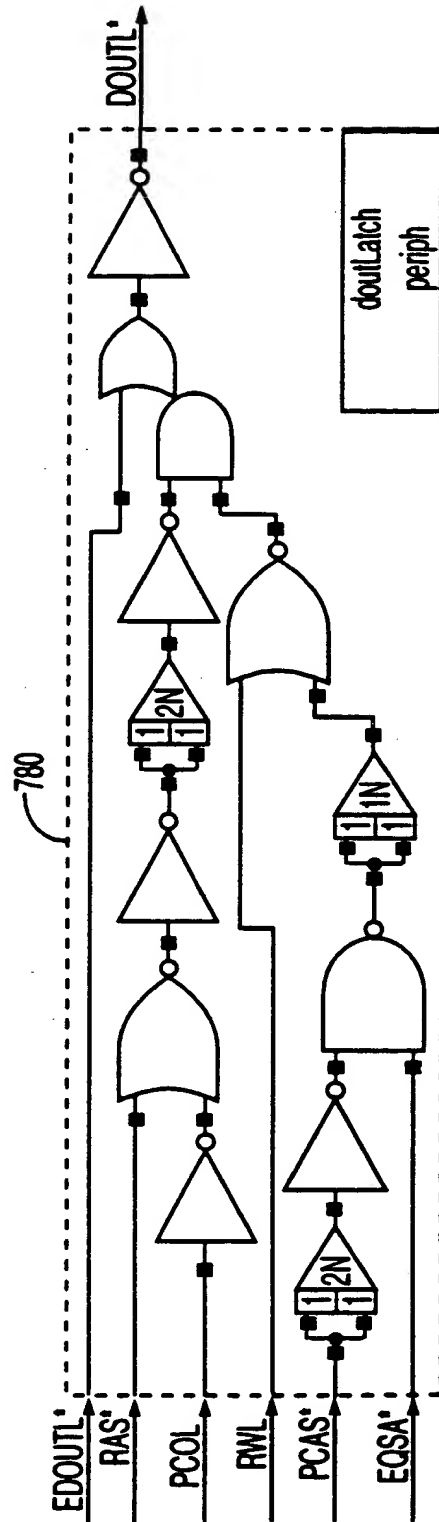


FIG. 47H



FIG. 471

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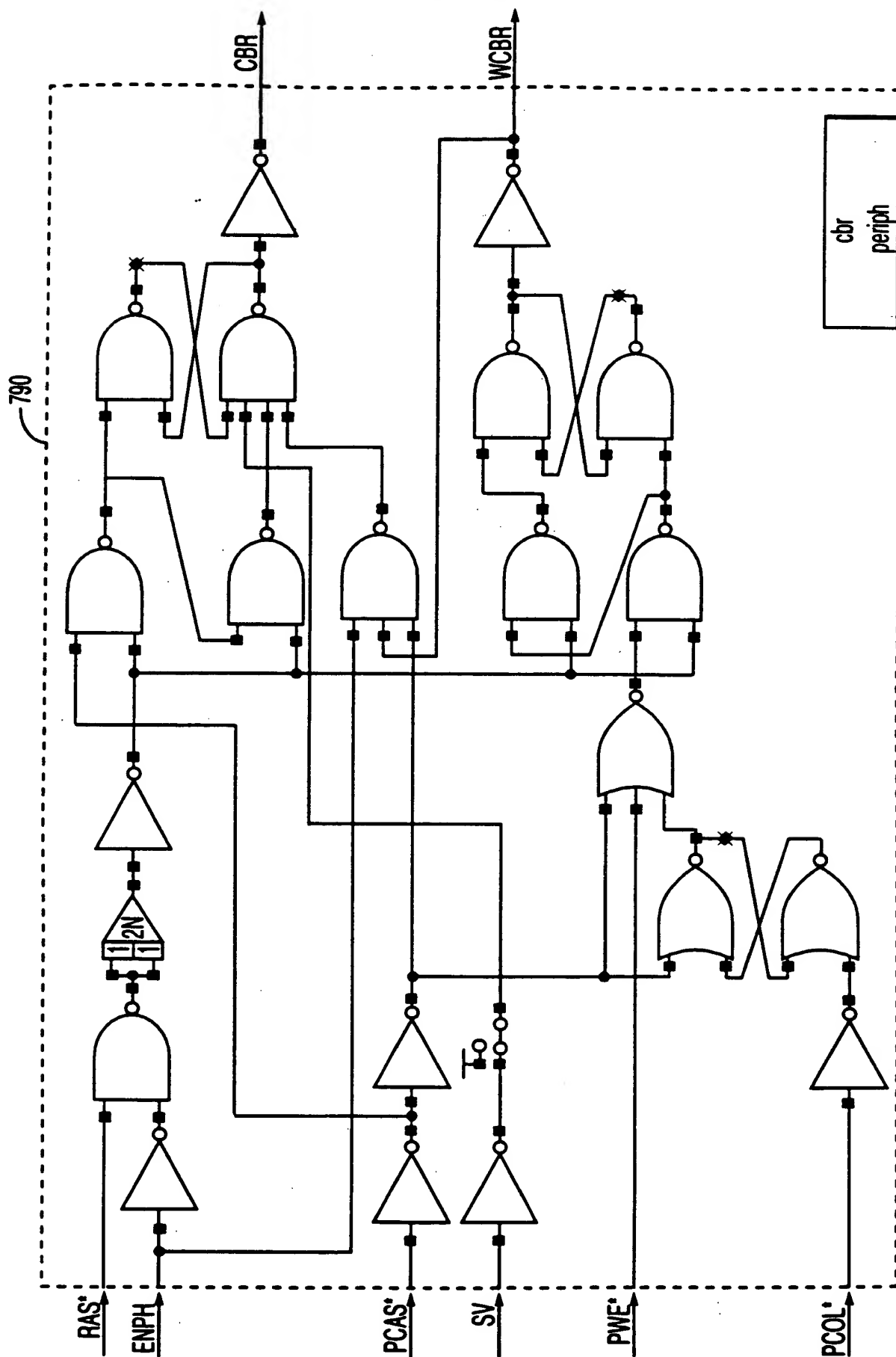


FIG. 47J



FIG. 47K

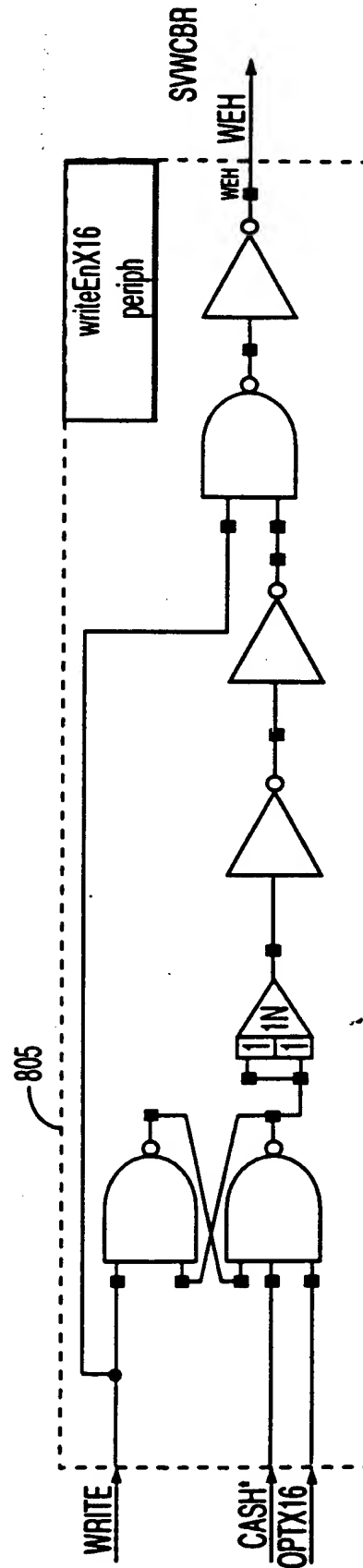


FIG. 47L



FIG. 47M

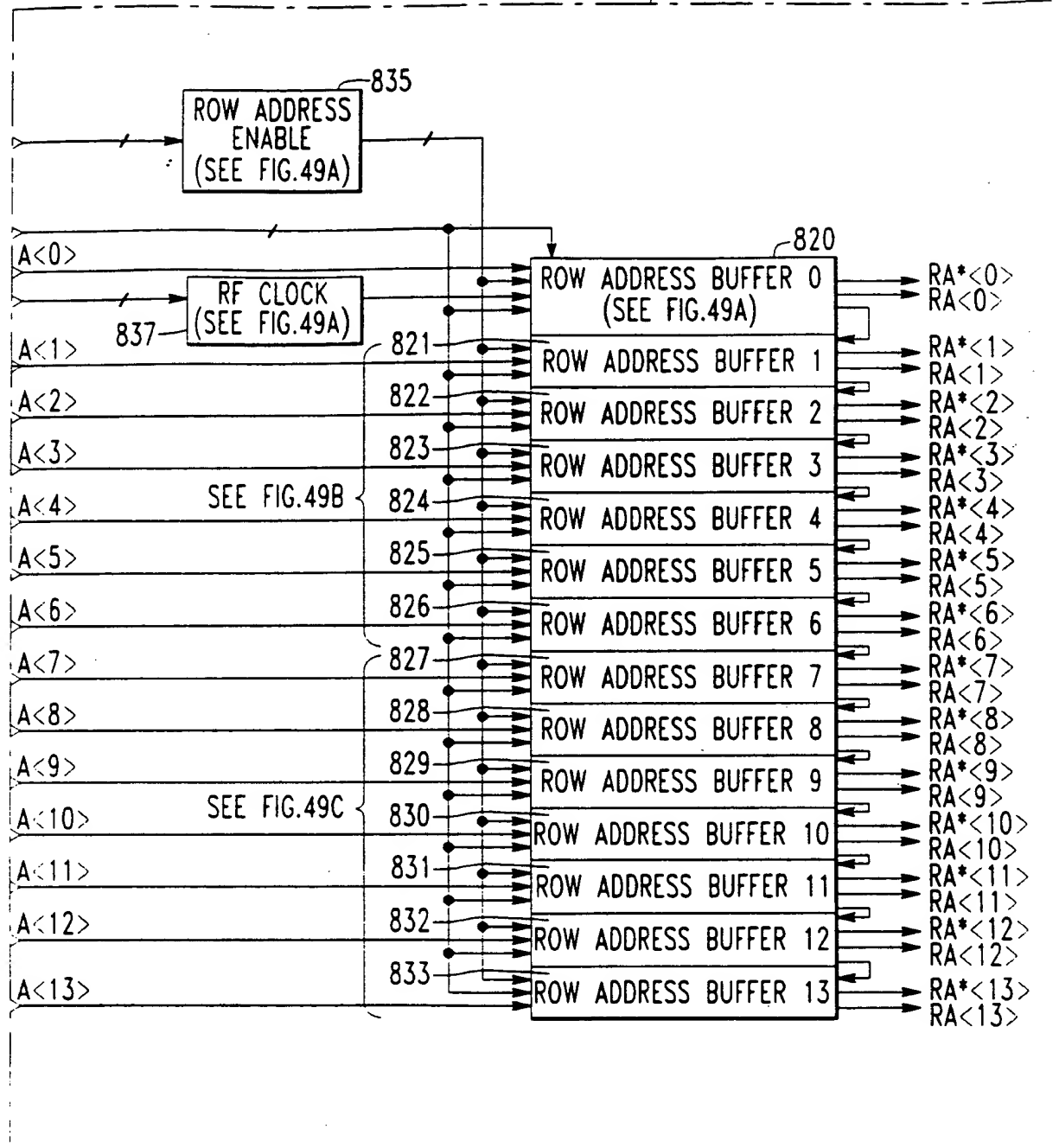


FIG. 48A

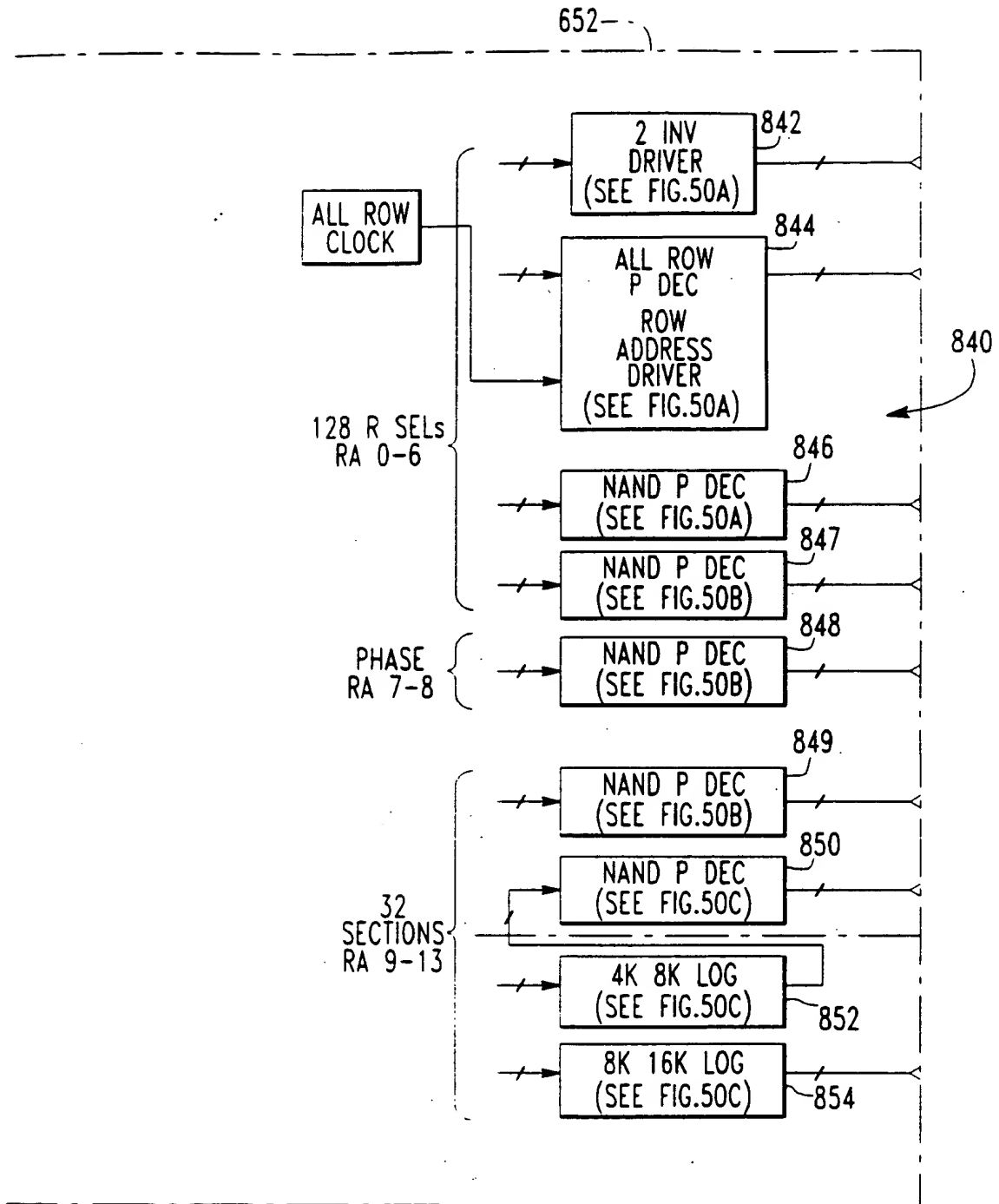


FIG. 48B

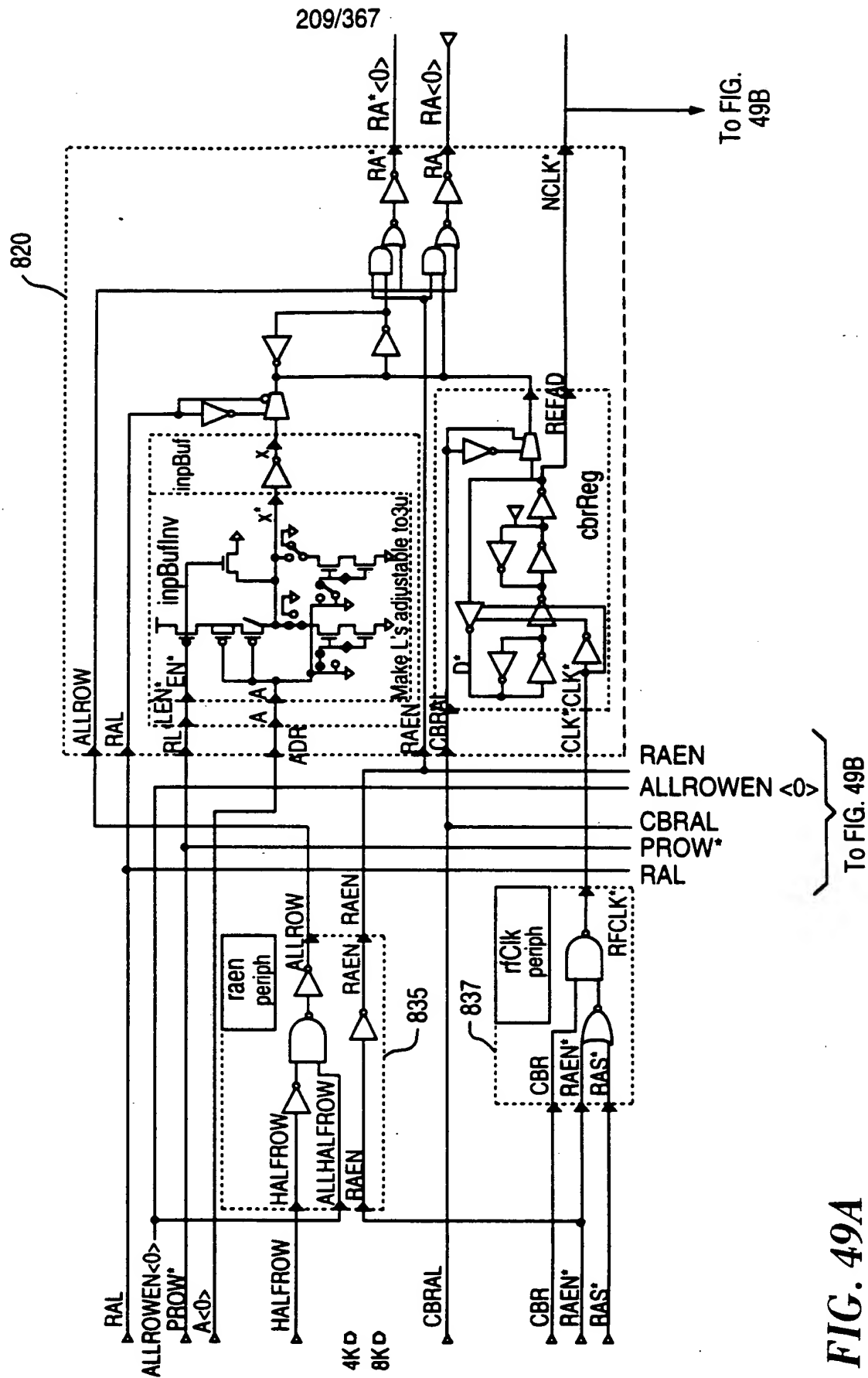
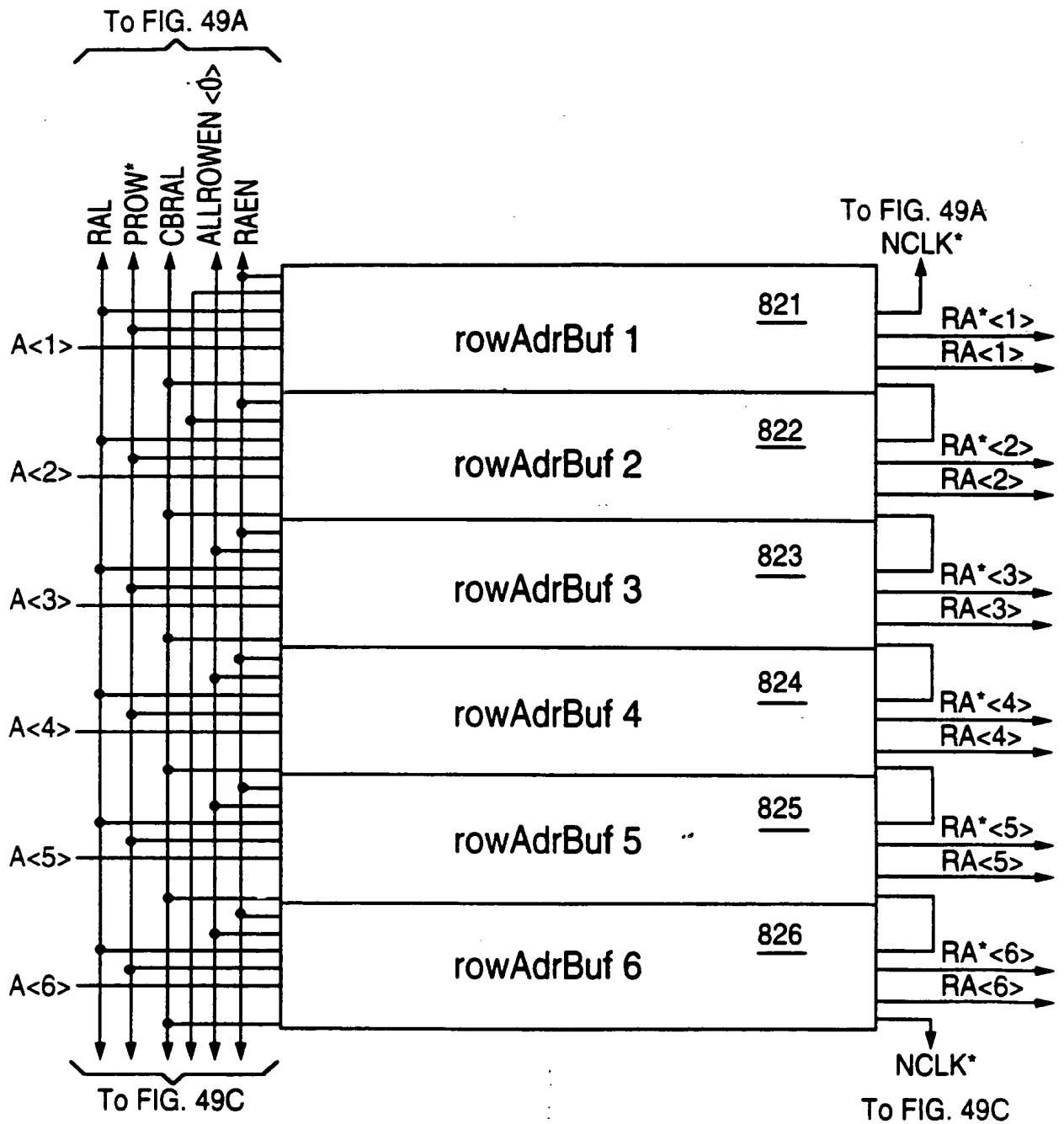
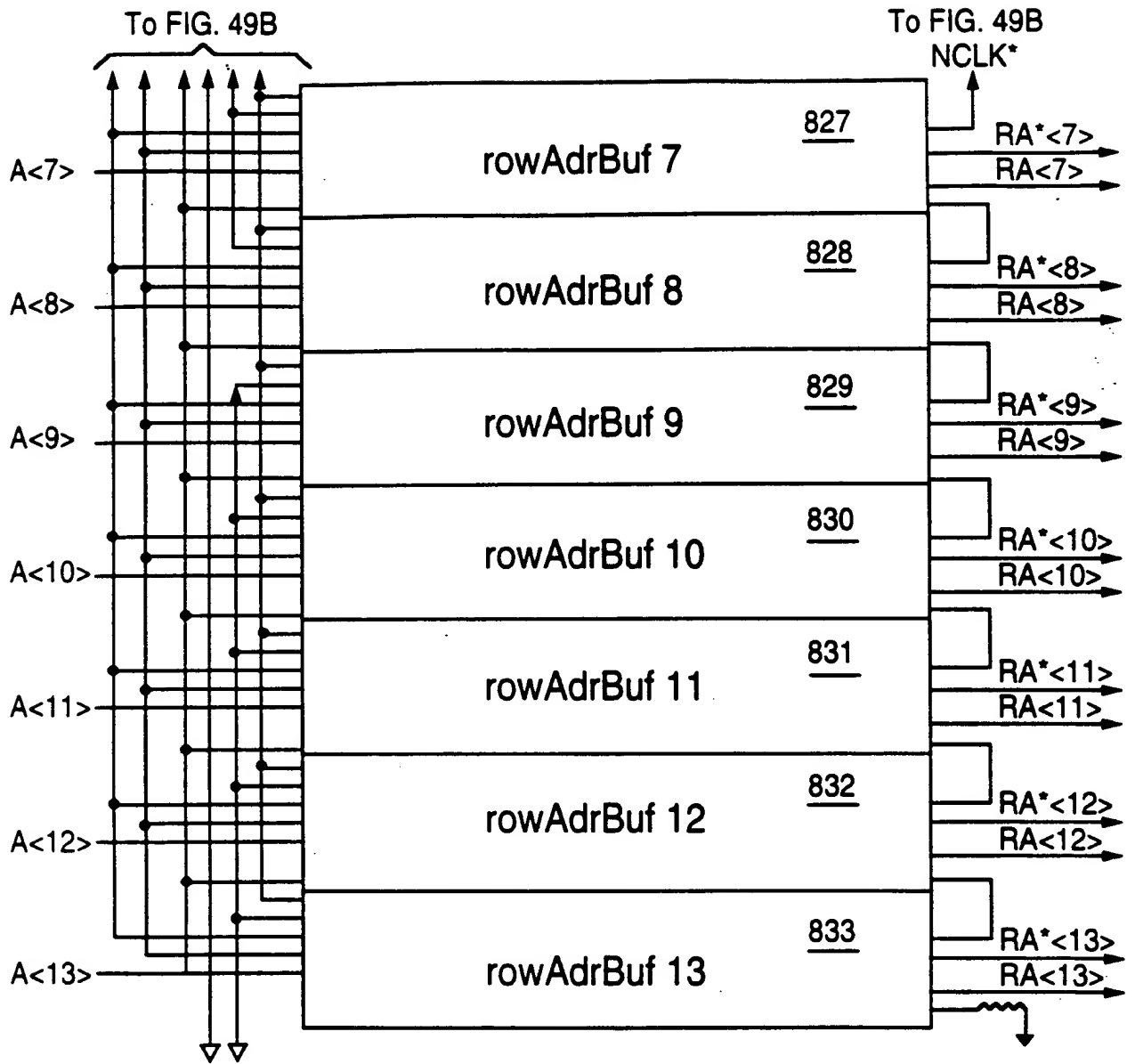


FIG. 49A

**FIG. 49B**

**FIG. 49C**

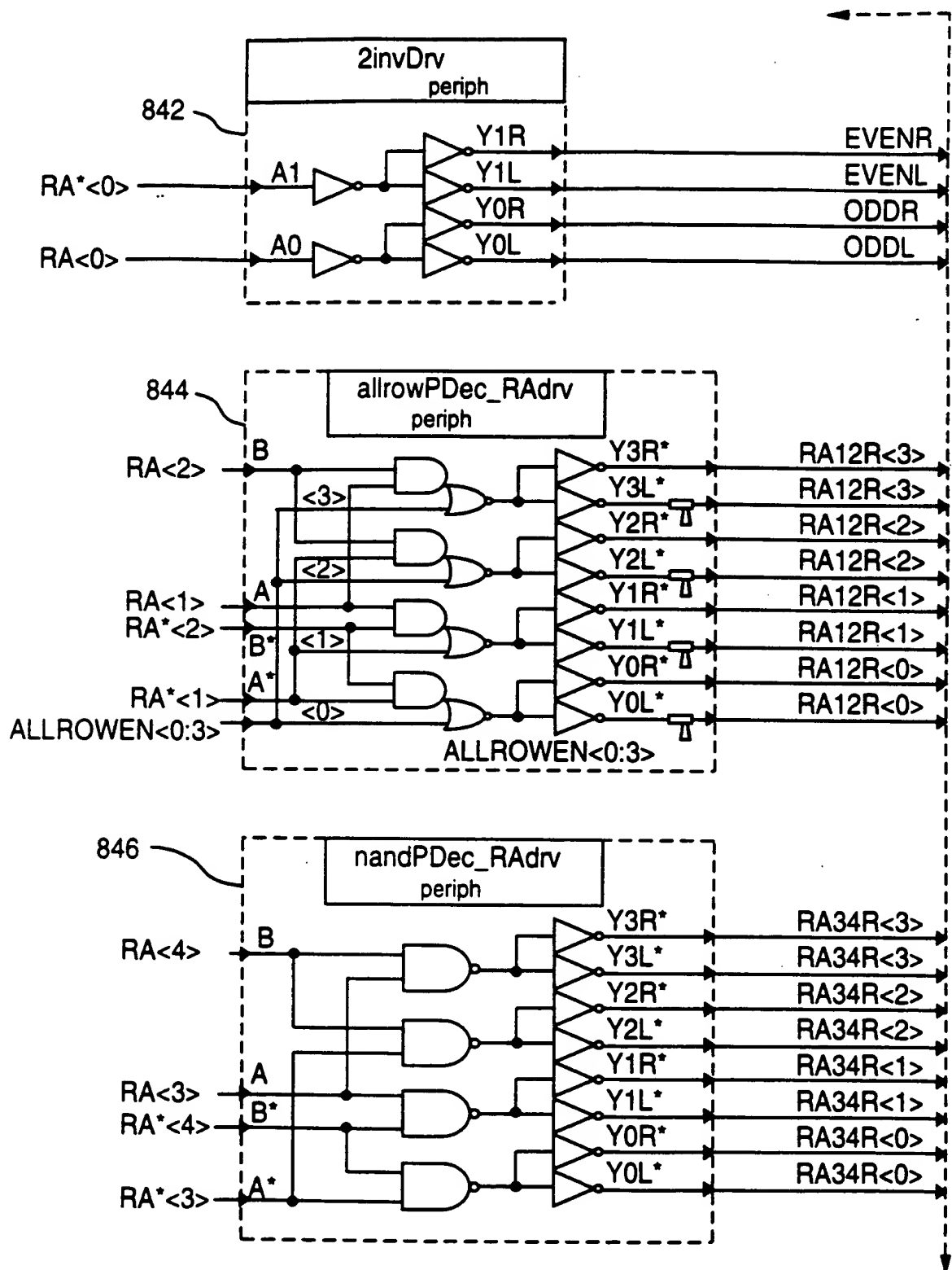
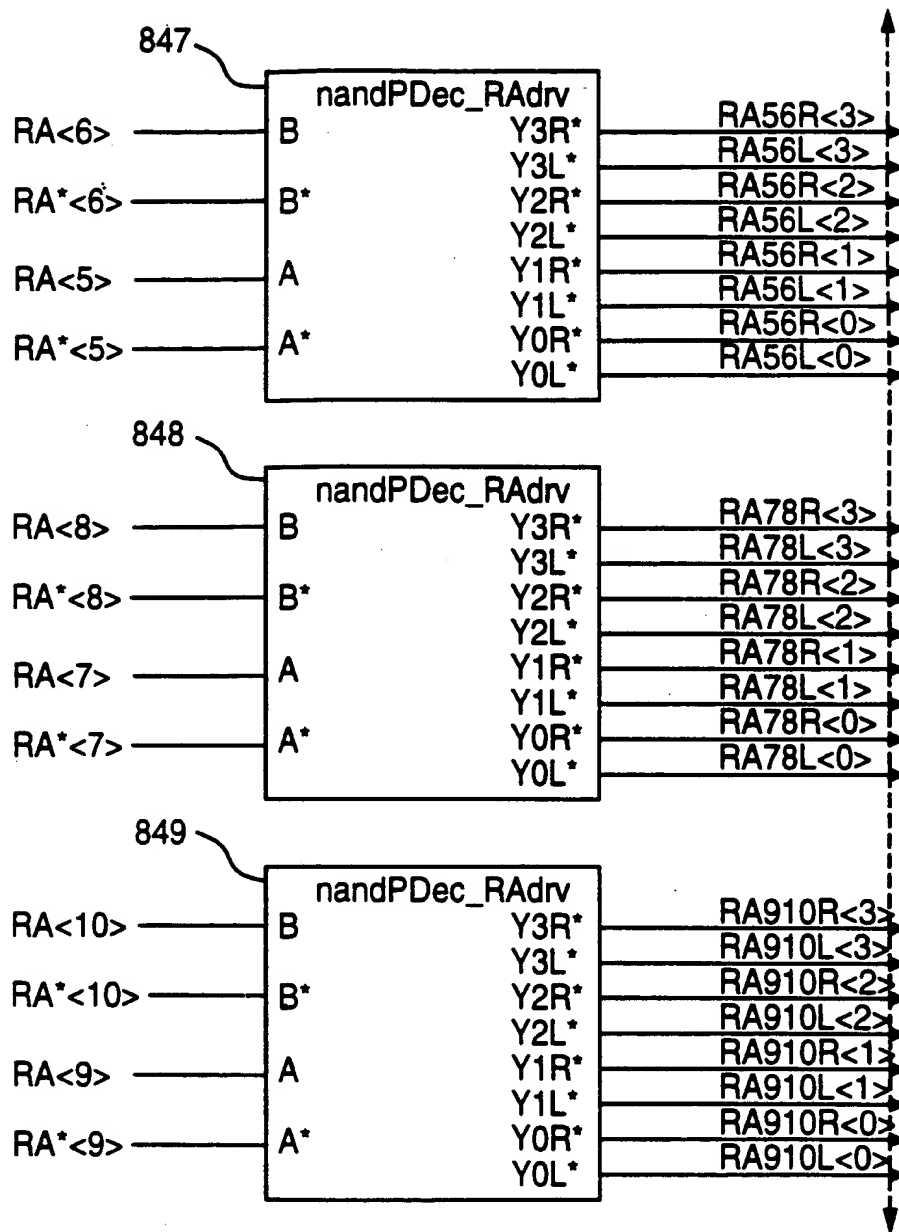


FIG. 50A

**FIG. 50B**

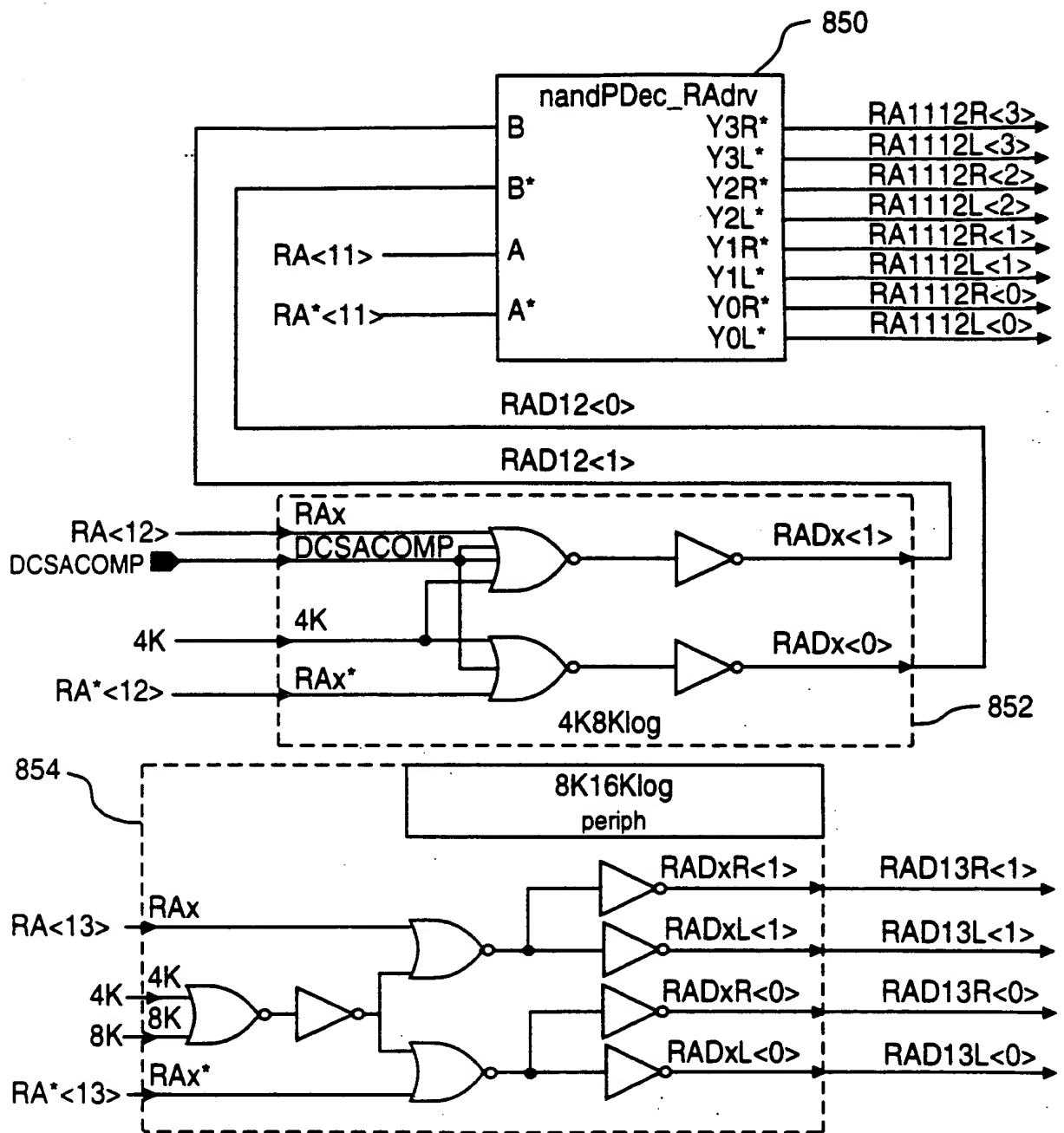


FIG. 50C

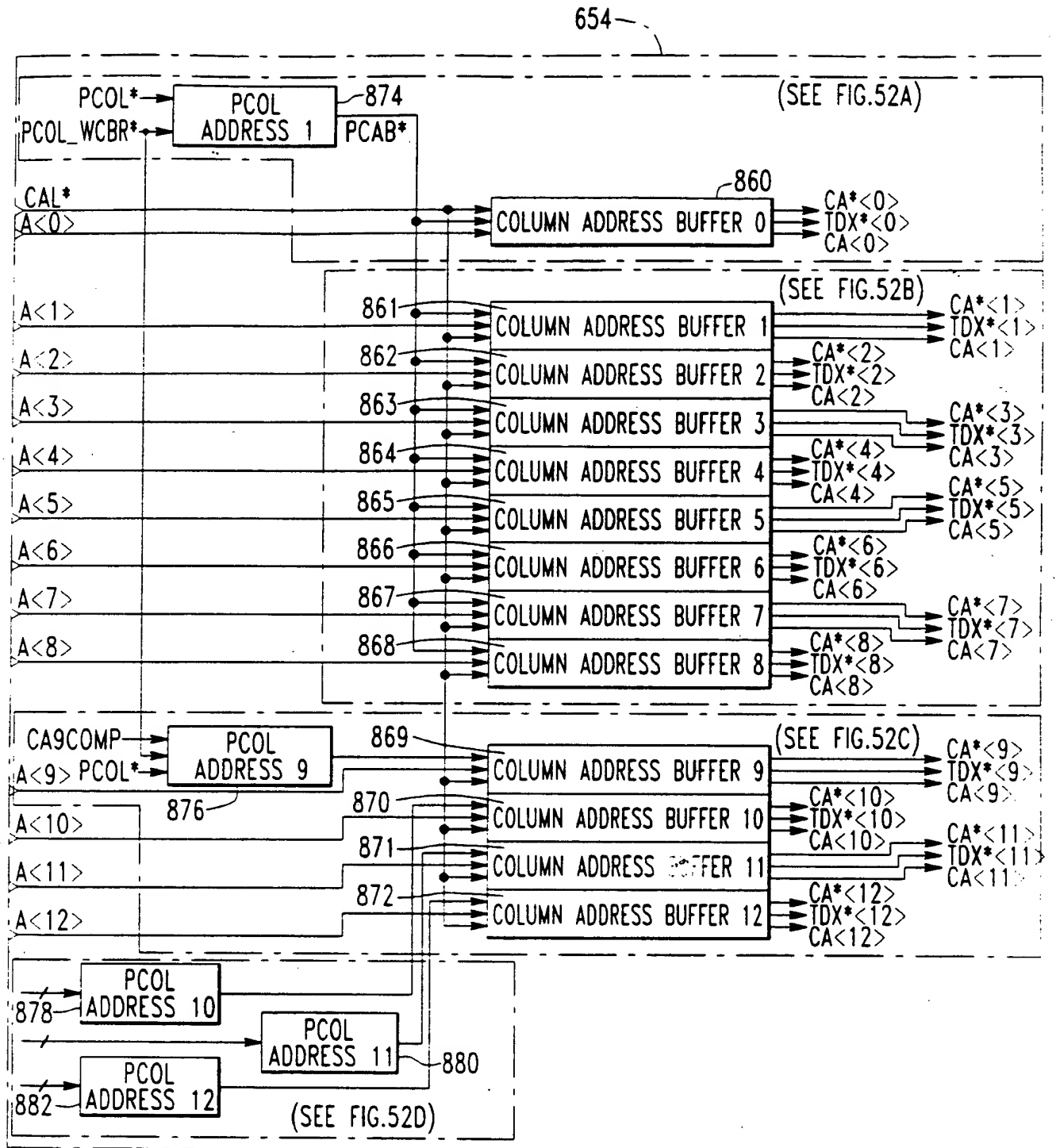


FIG. 51A

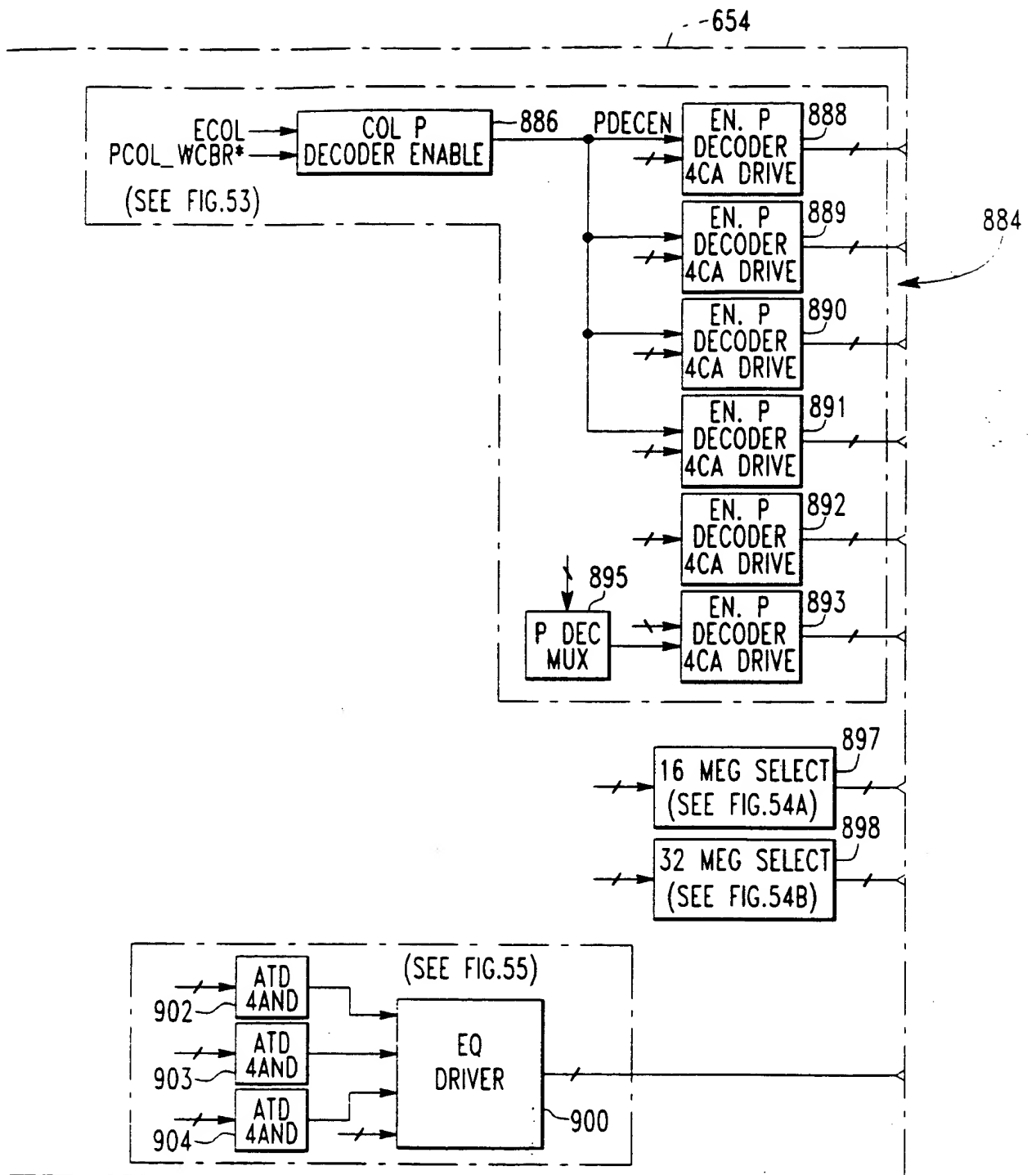


FIG. 51B

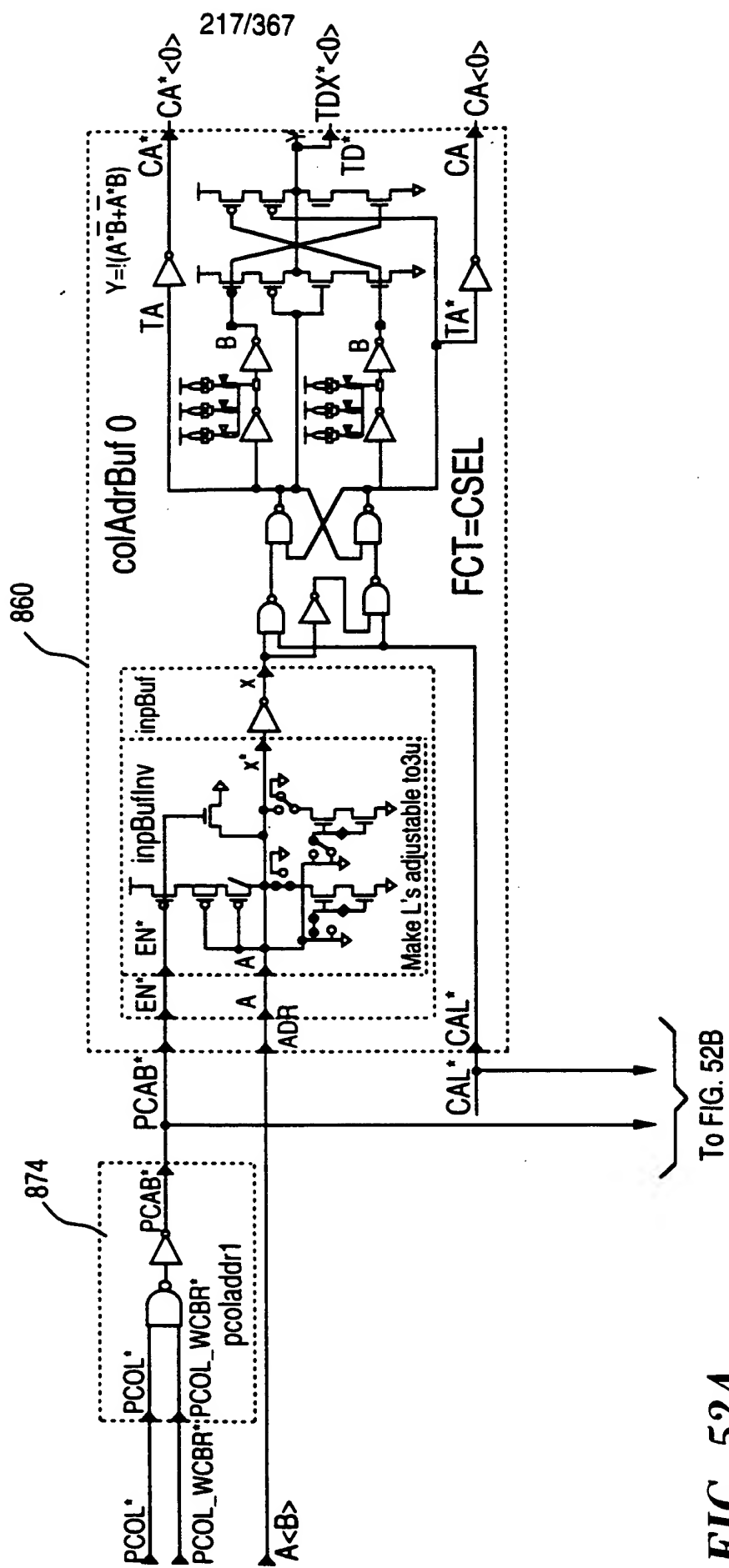
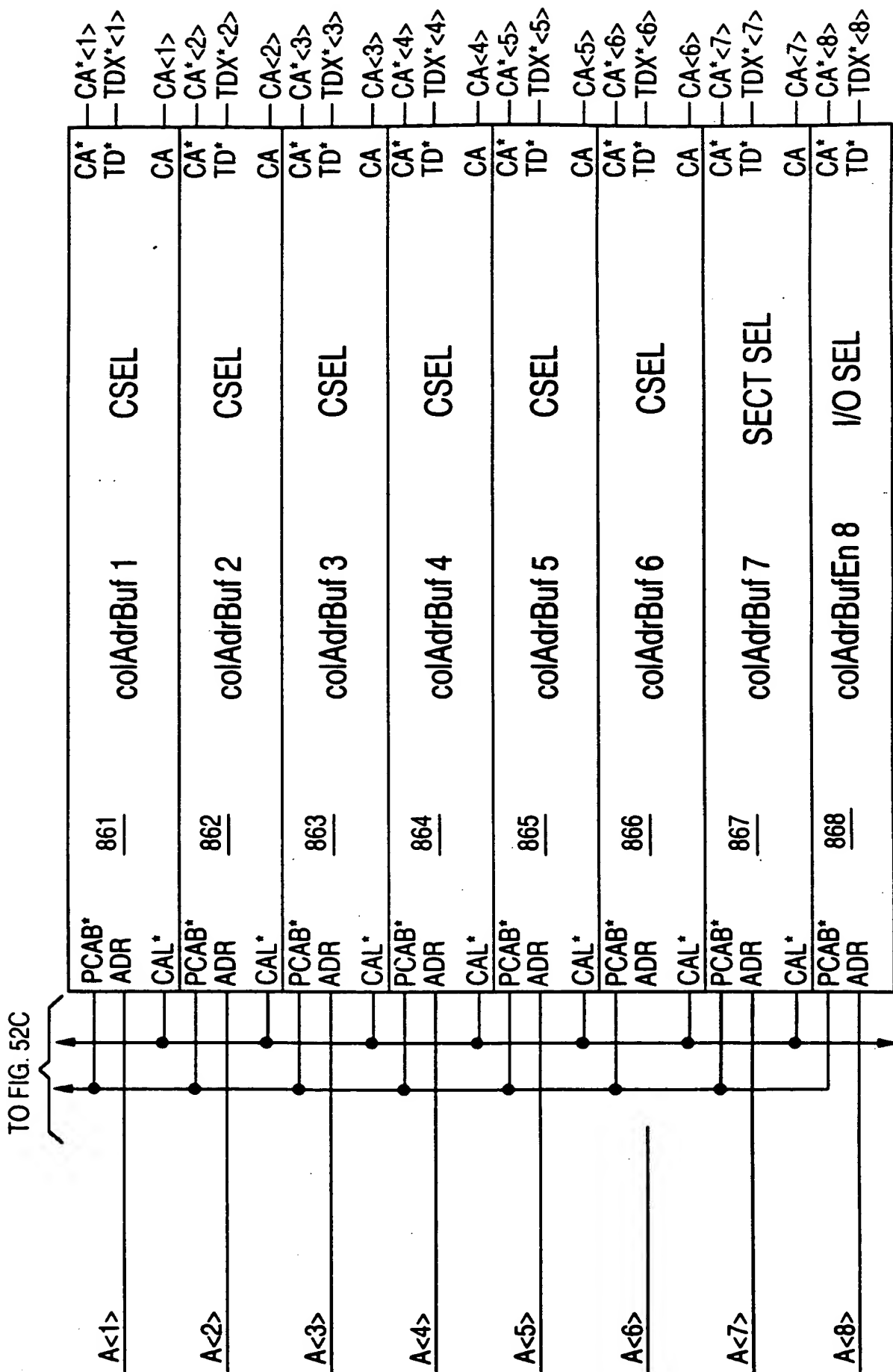
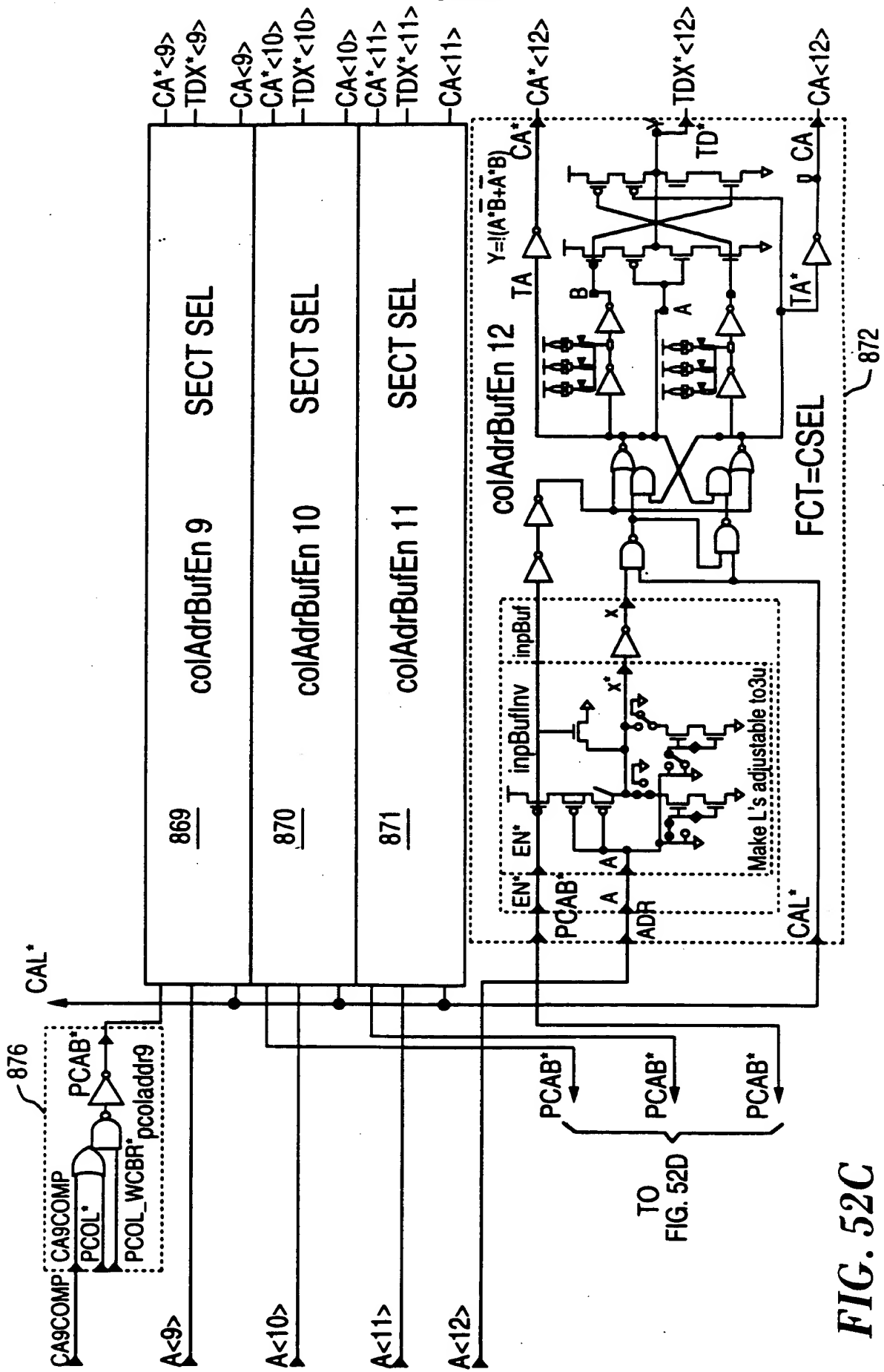


FIG. 52A



TO FIG. 52C

FIG. 52B



TO
FIG. 52D

FIG. 52C

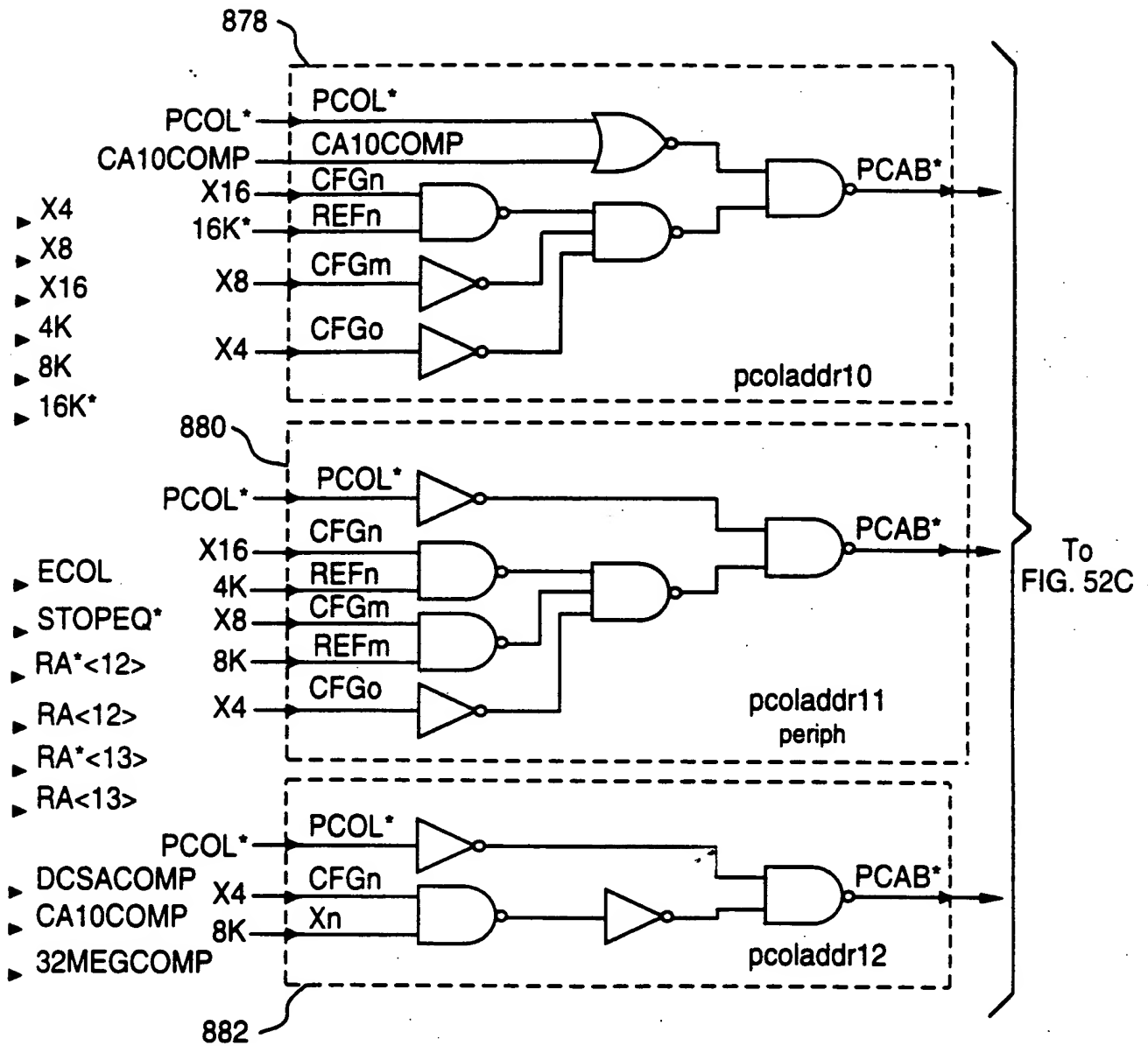
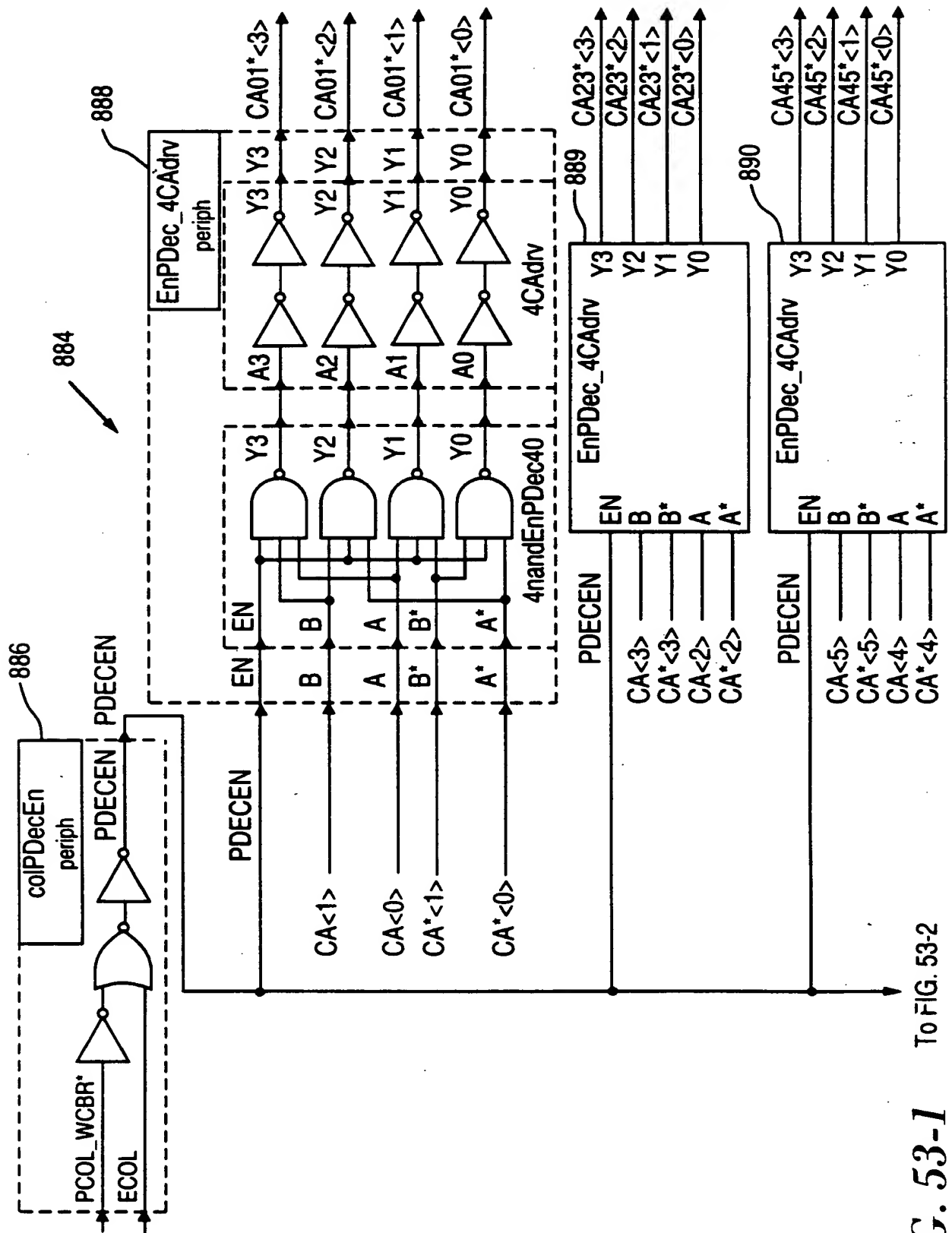


FIG. 52D



To FIG. 53-2

FIG. 53-1

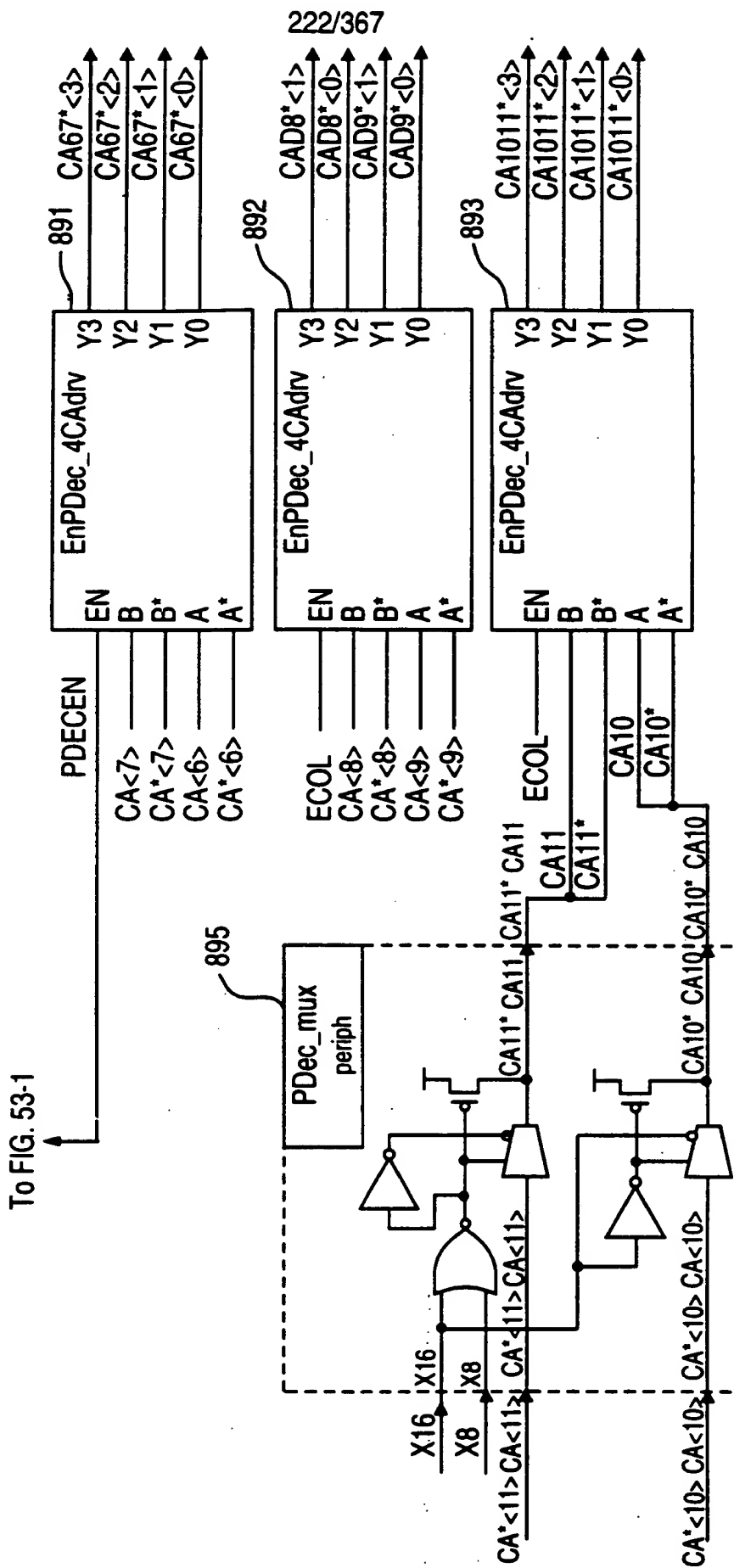


FIG. 53-2

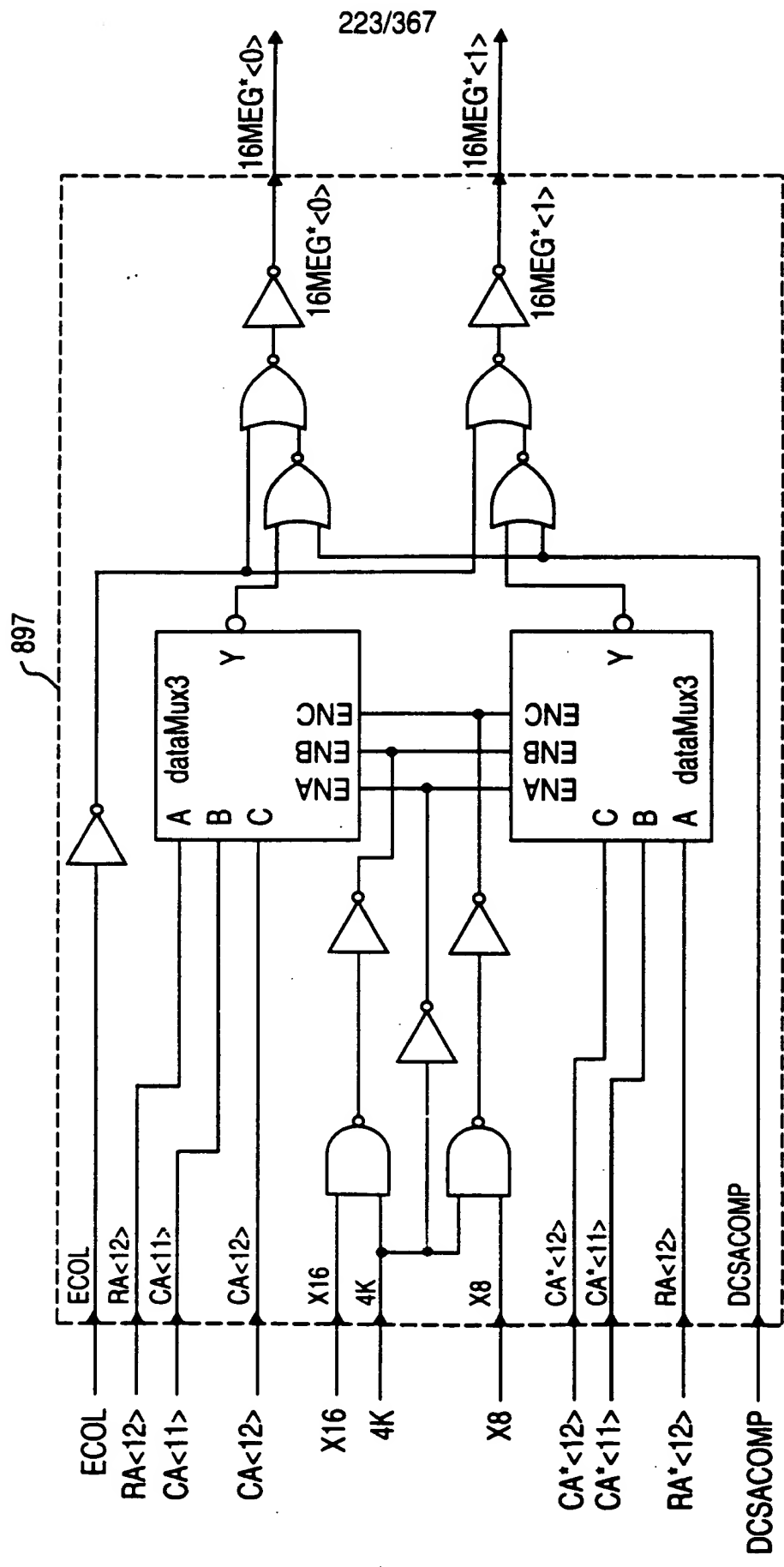


FIG. 54A

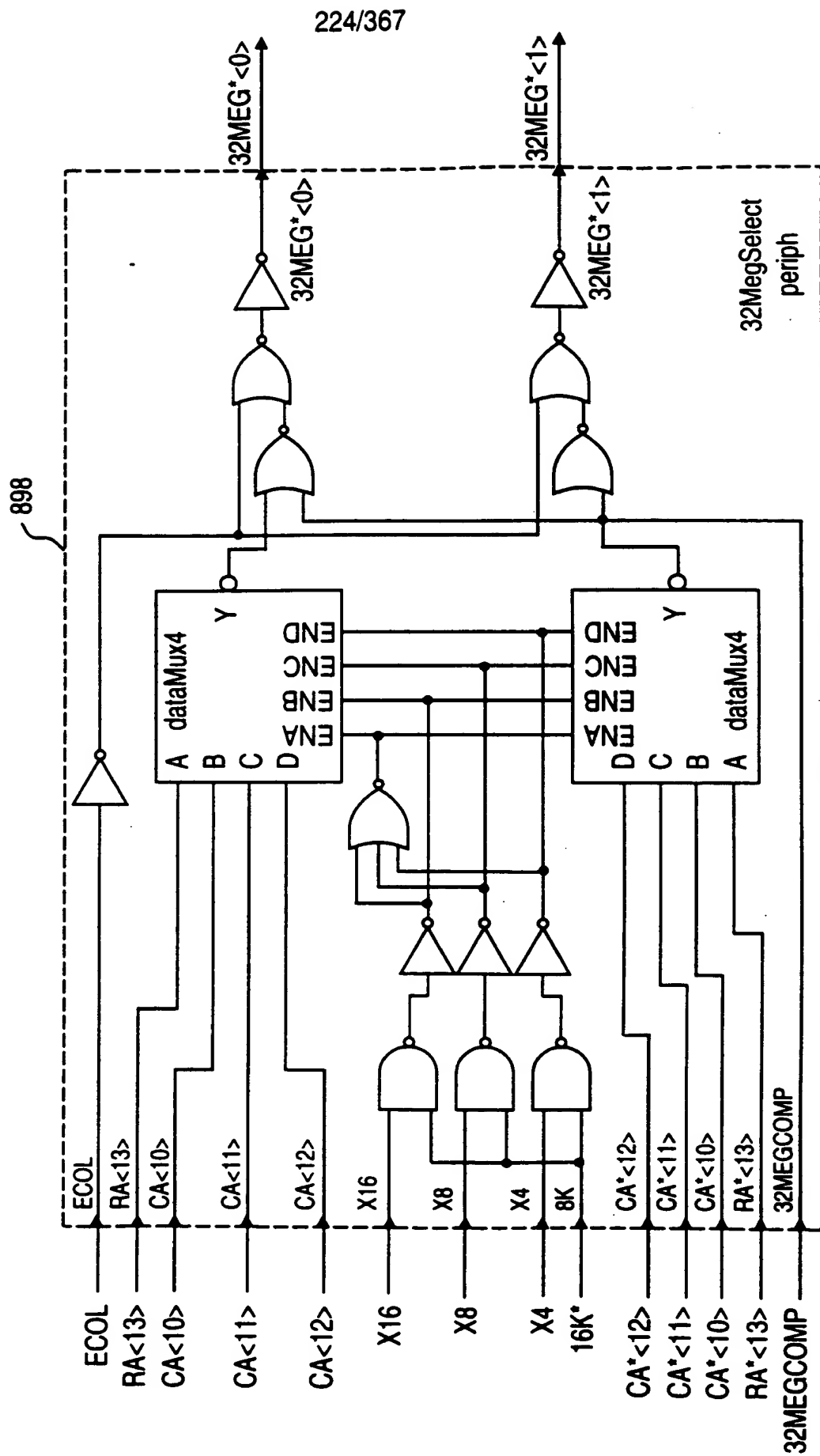


FIG. 54B

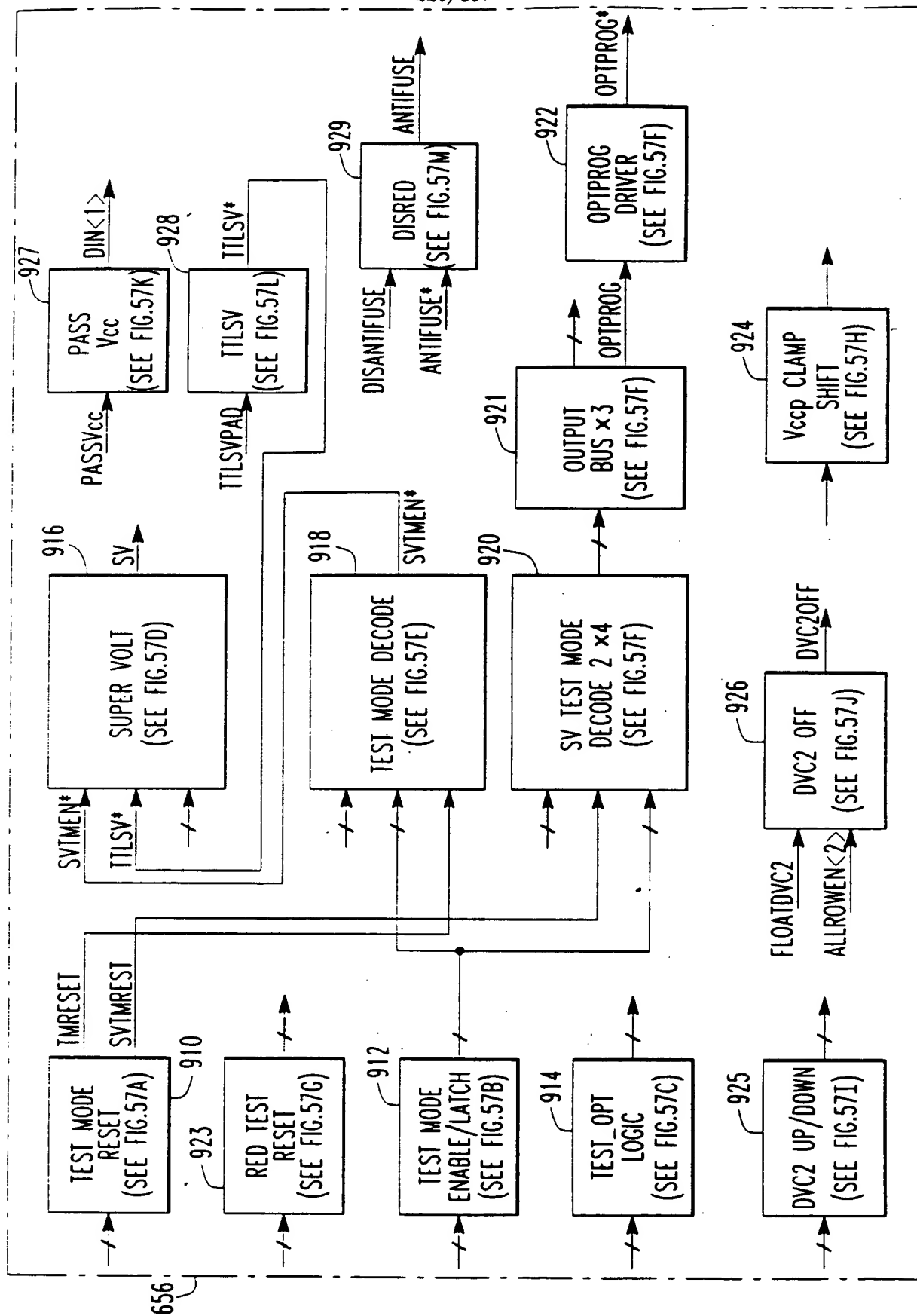


FIG. 56

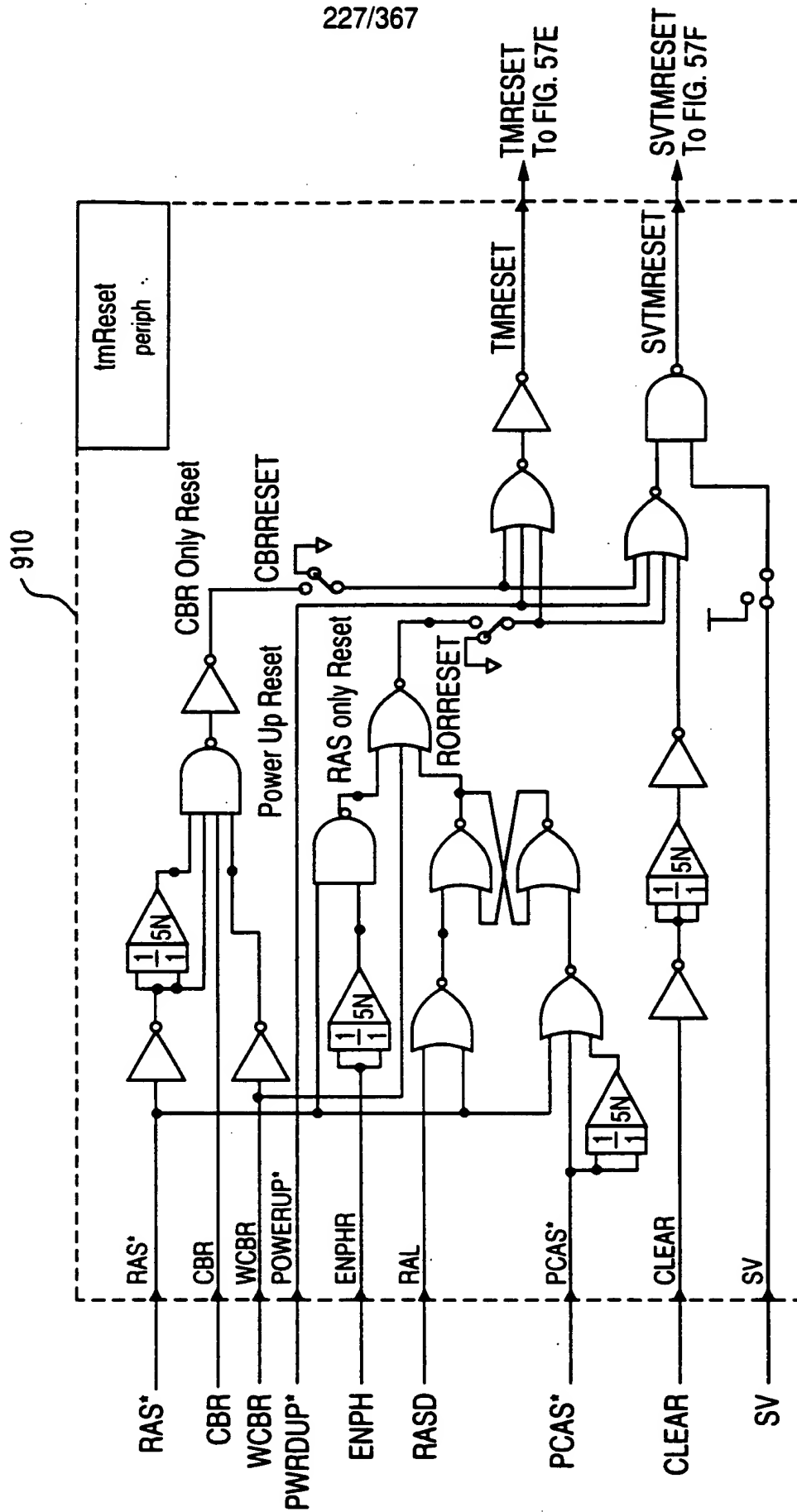


FIG. 57A

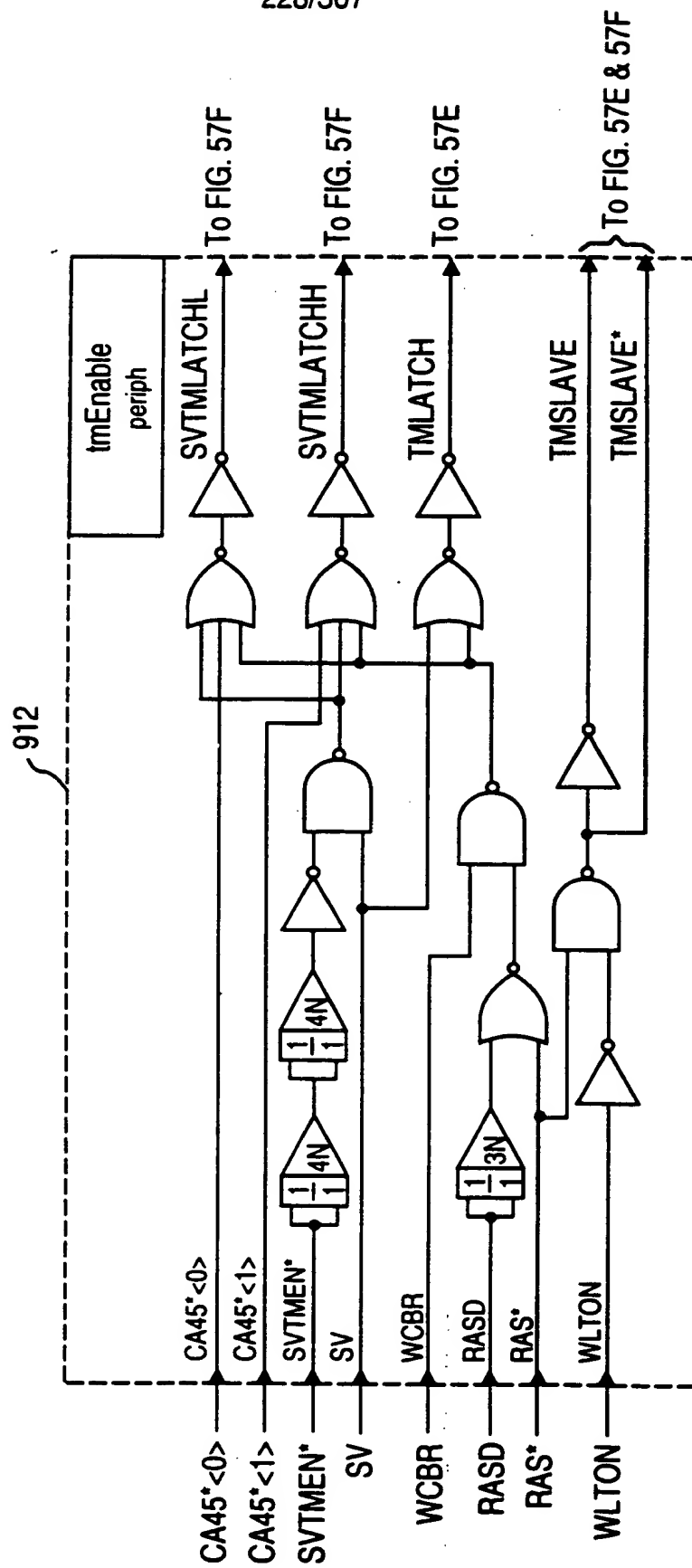


FIG. 57B

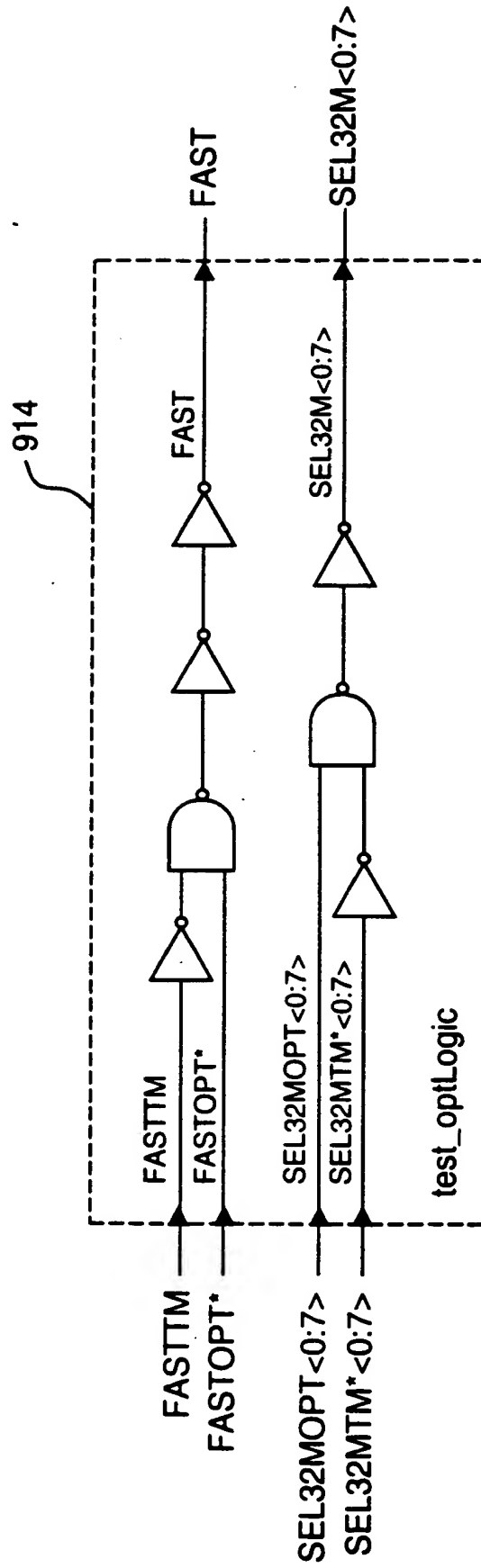
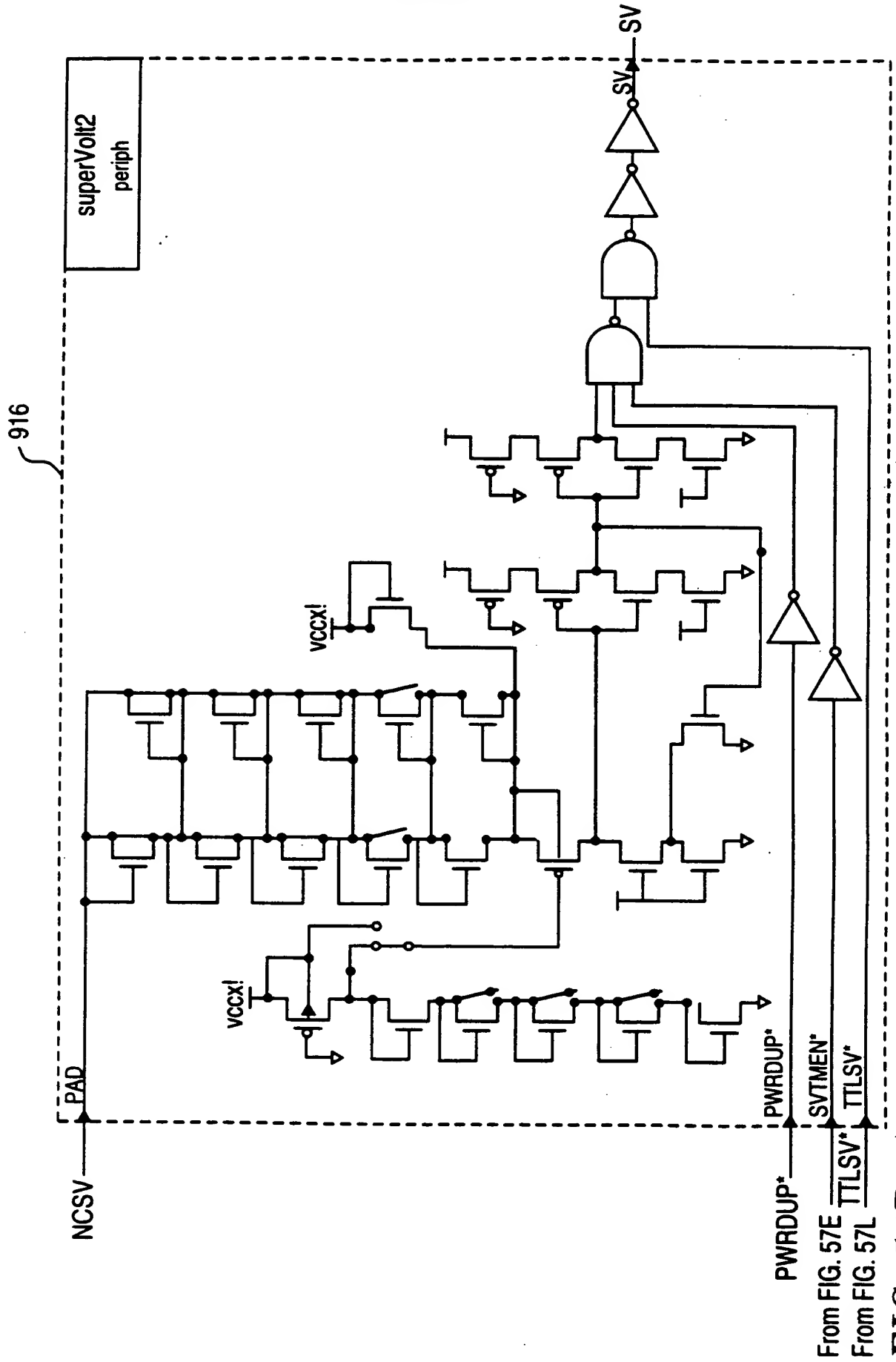


FIG. 57C



From FIG. 57E

From FIG. 57L

FIG. 57D

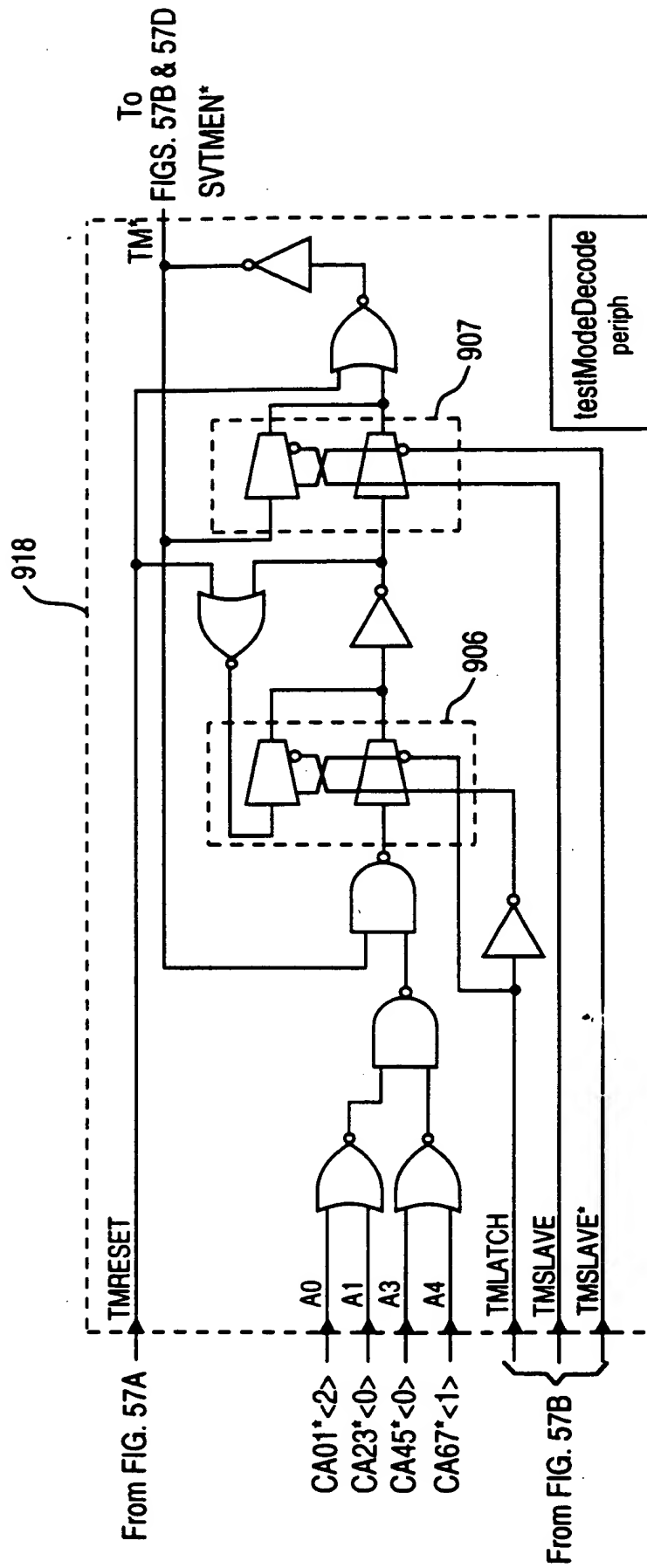


FIG. 57E

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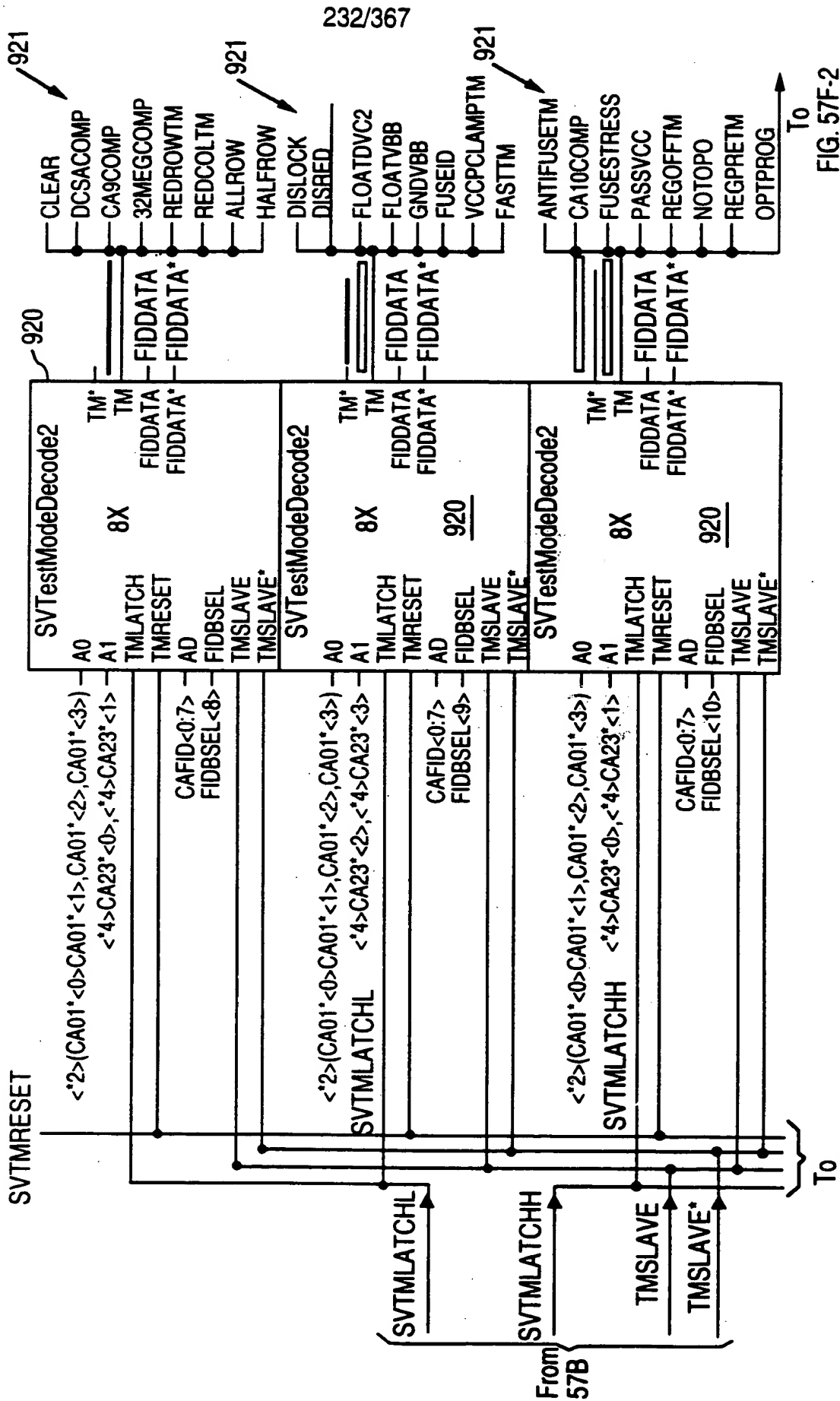


FIG. 57F-3

FIG. 57F-1

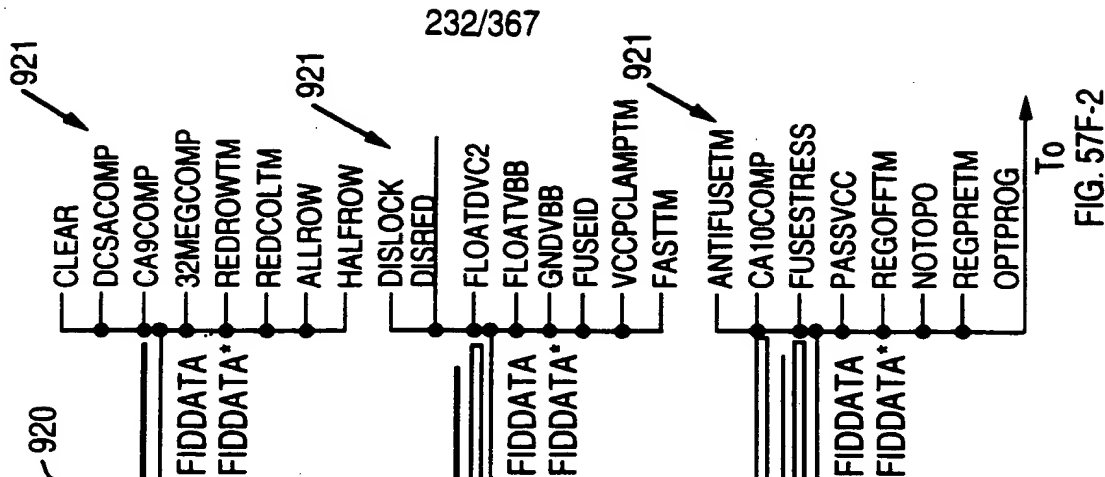


FIG. 57F-2

FIG. 57F-1

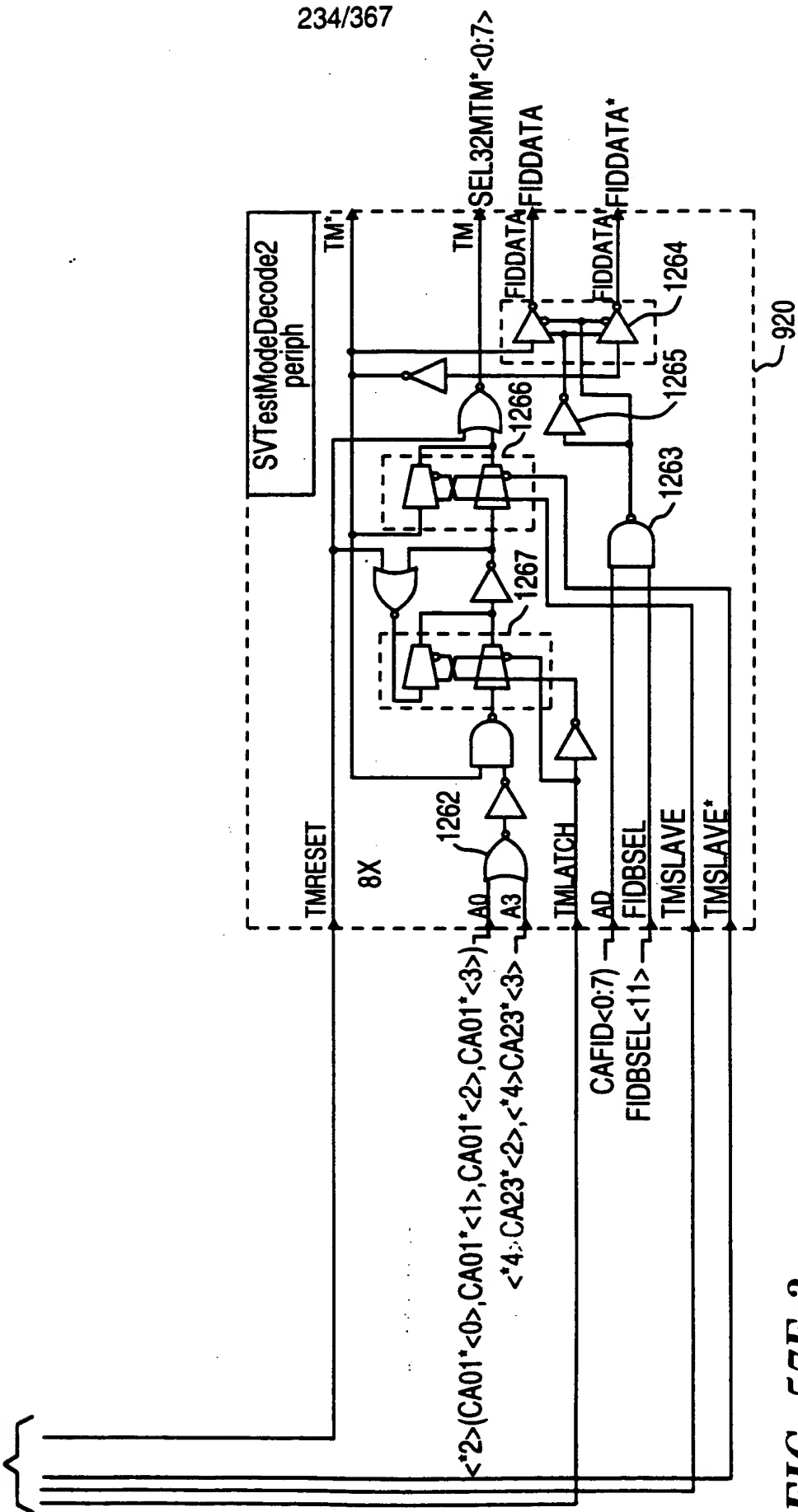


FIG. 57F-3

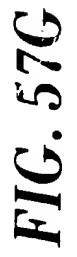
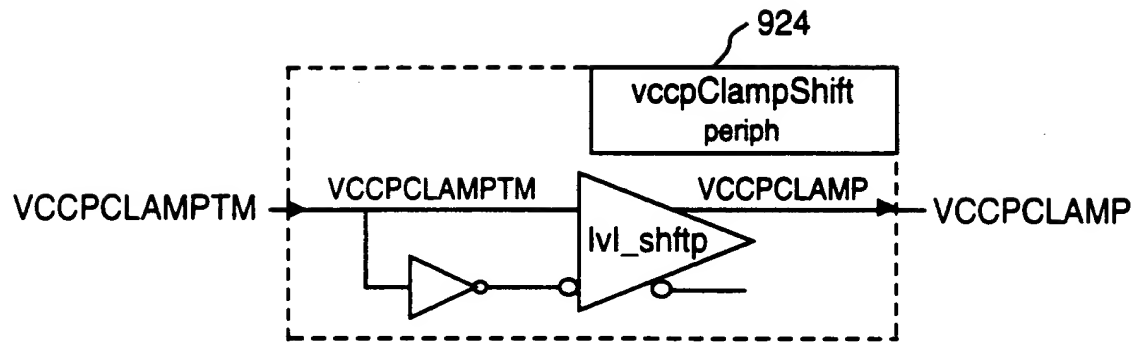
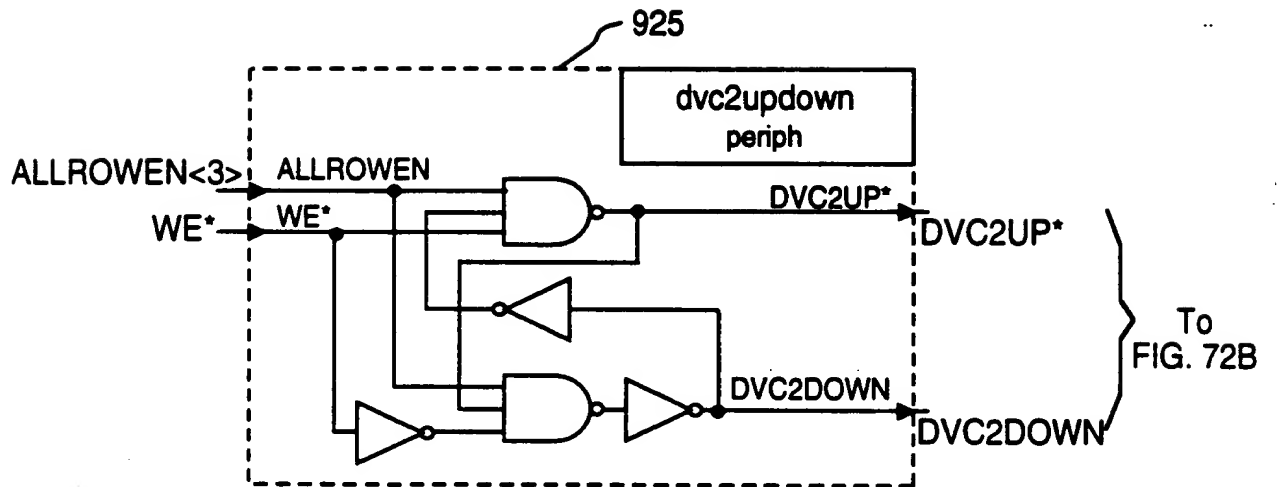
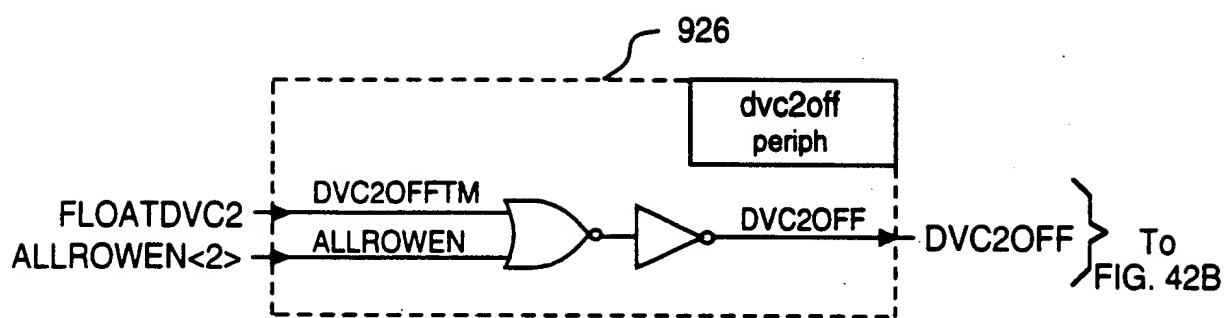
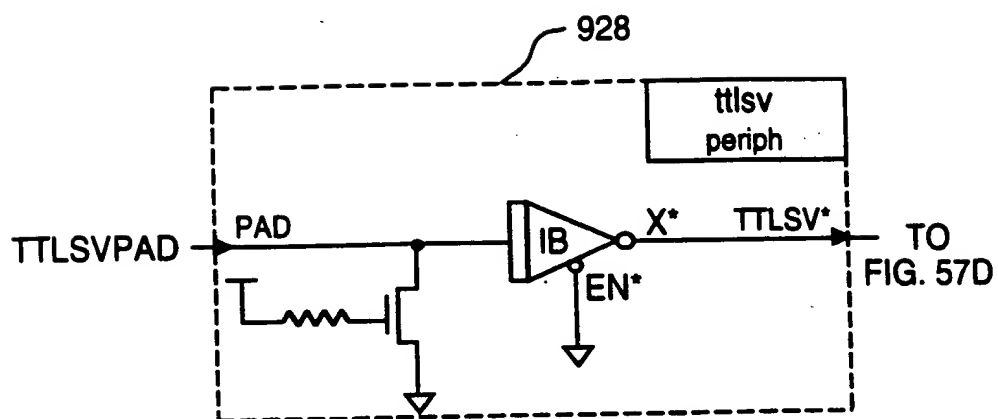


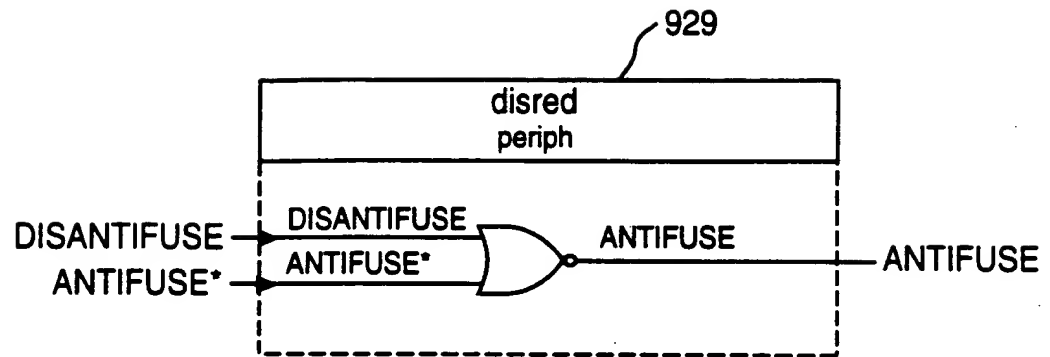
FIG. 57C

**FIG. 57H**

**FIG. 57I**

**FIG. 57J**

**FIG. 57L**

**FIG. 57M**

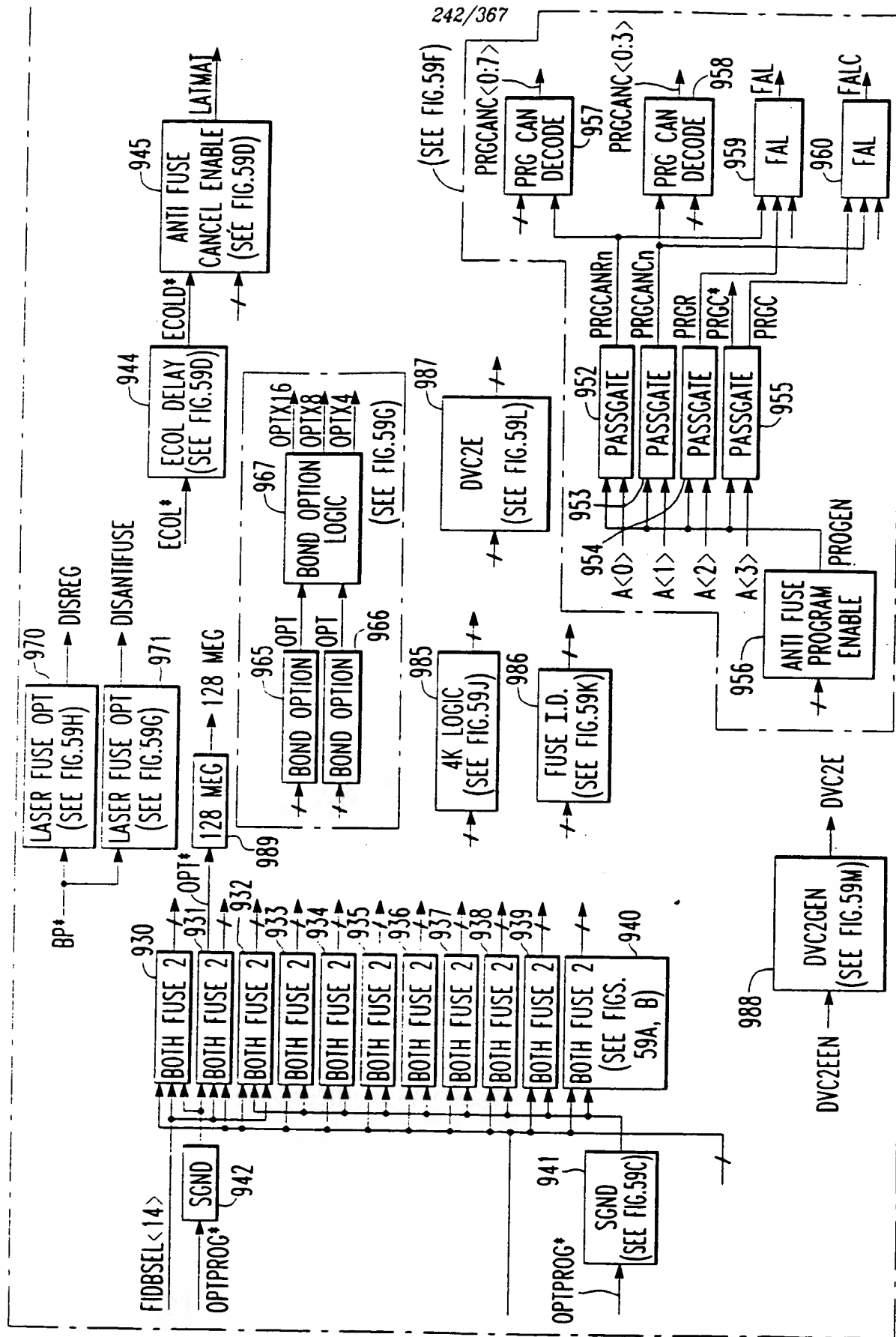


FIG. 58A

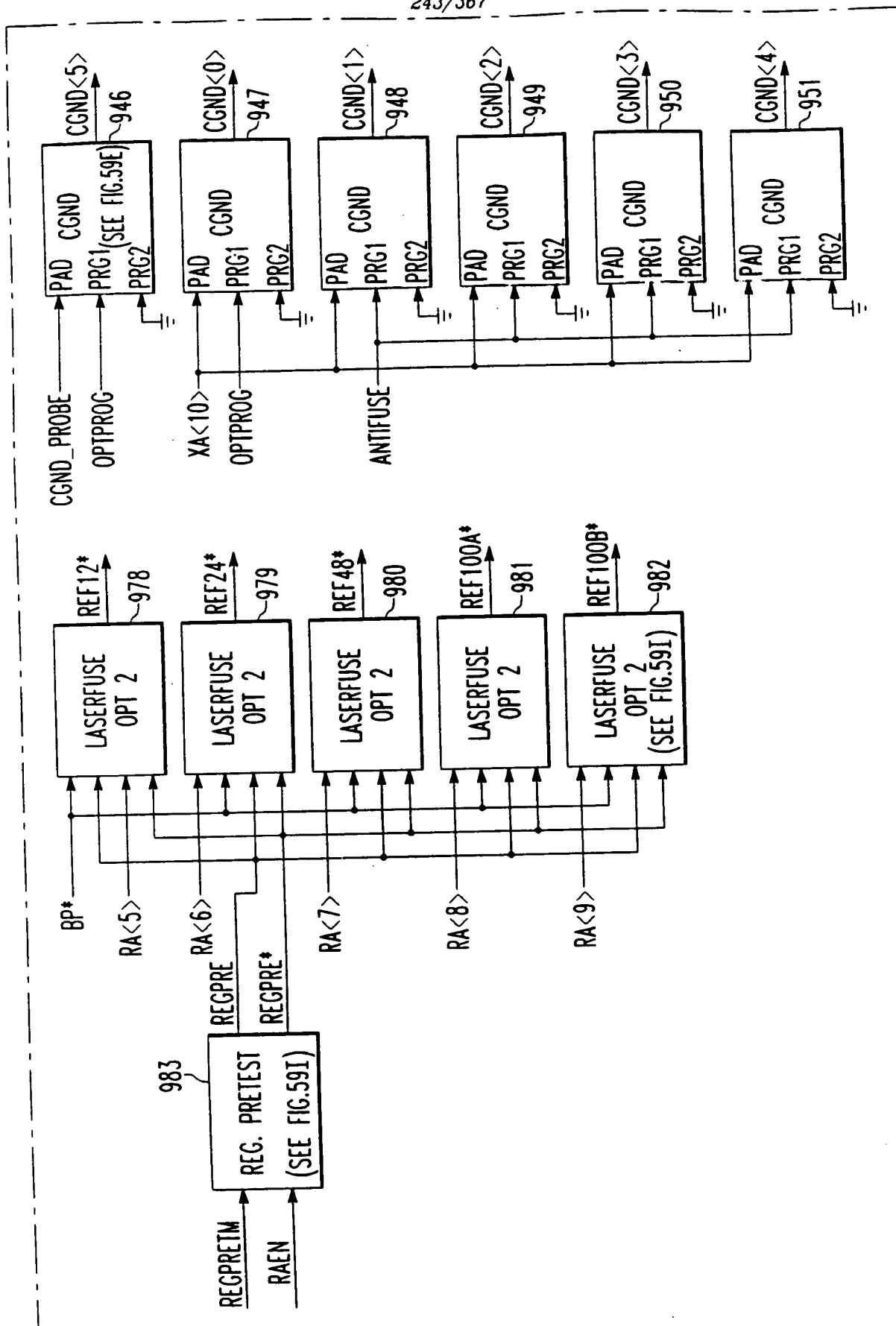


FIG. 58B

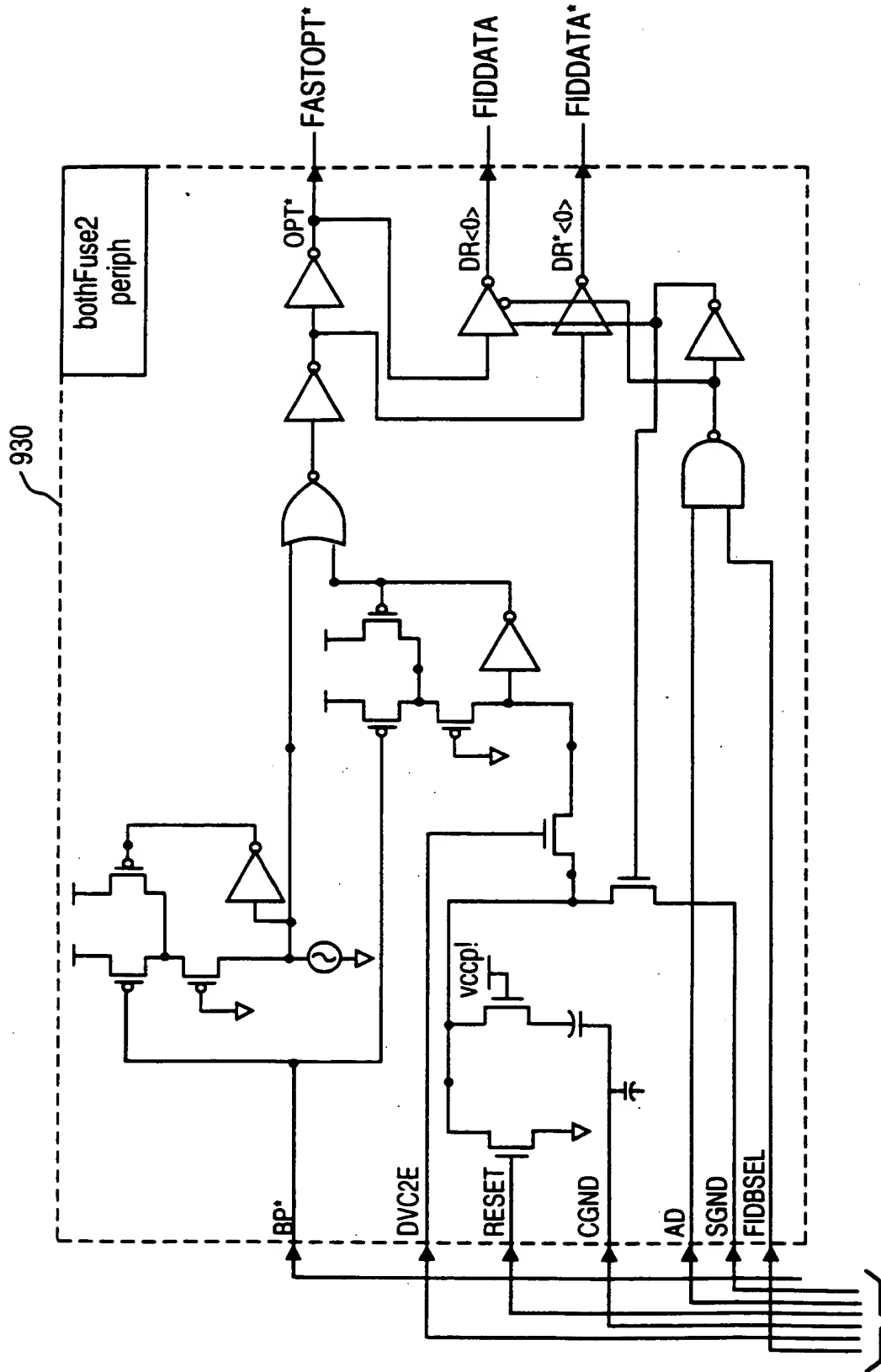


FIG. 59B

FIG. 59A

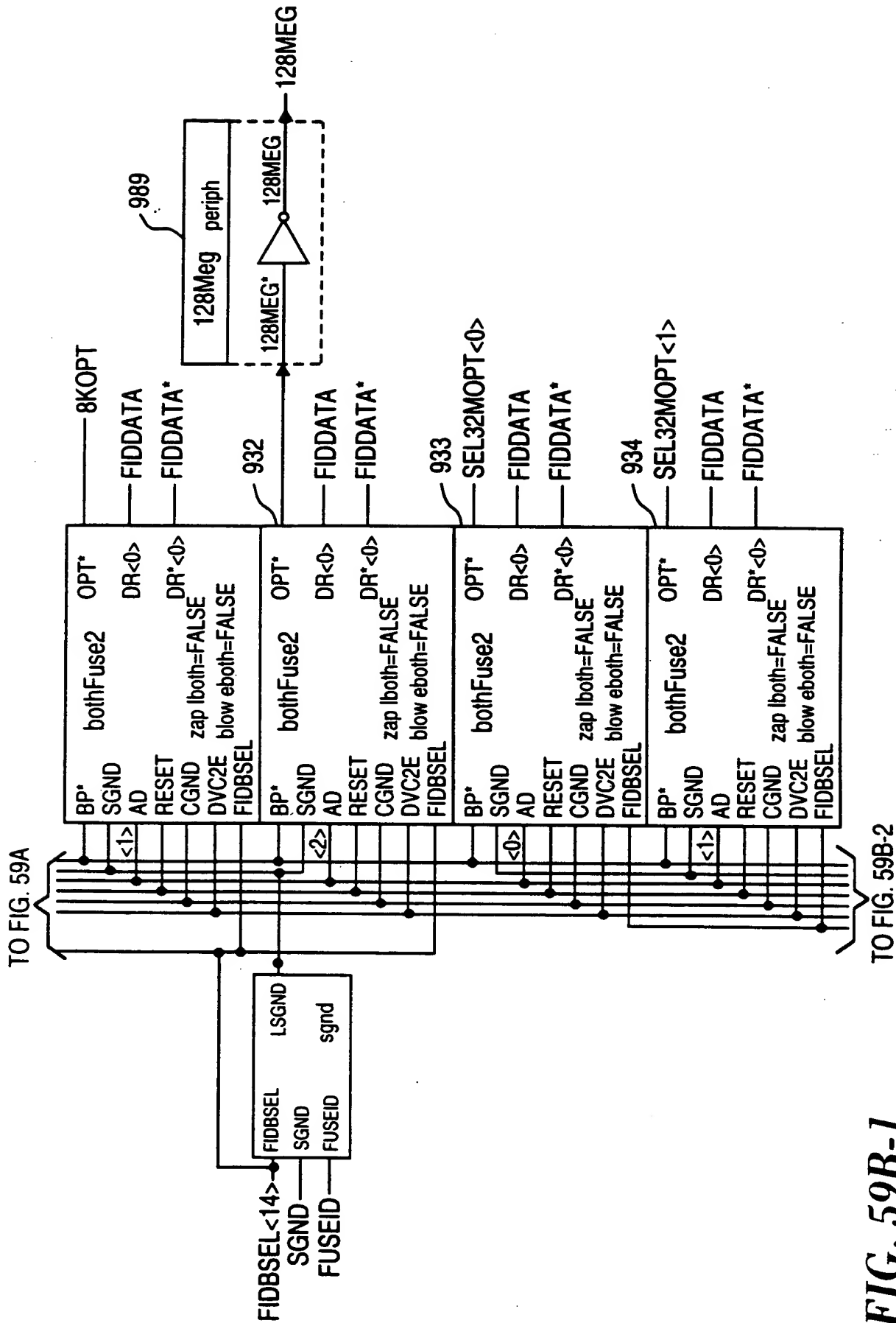


FIG. 59B-1

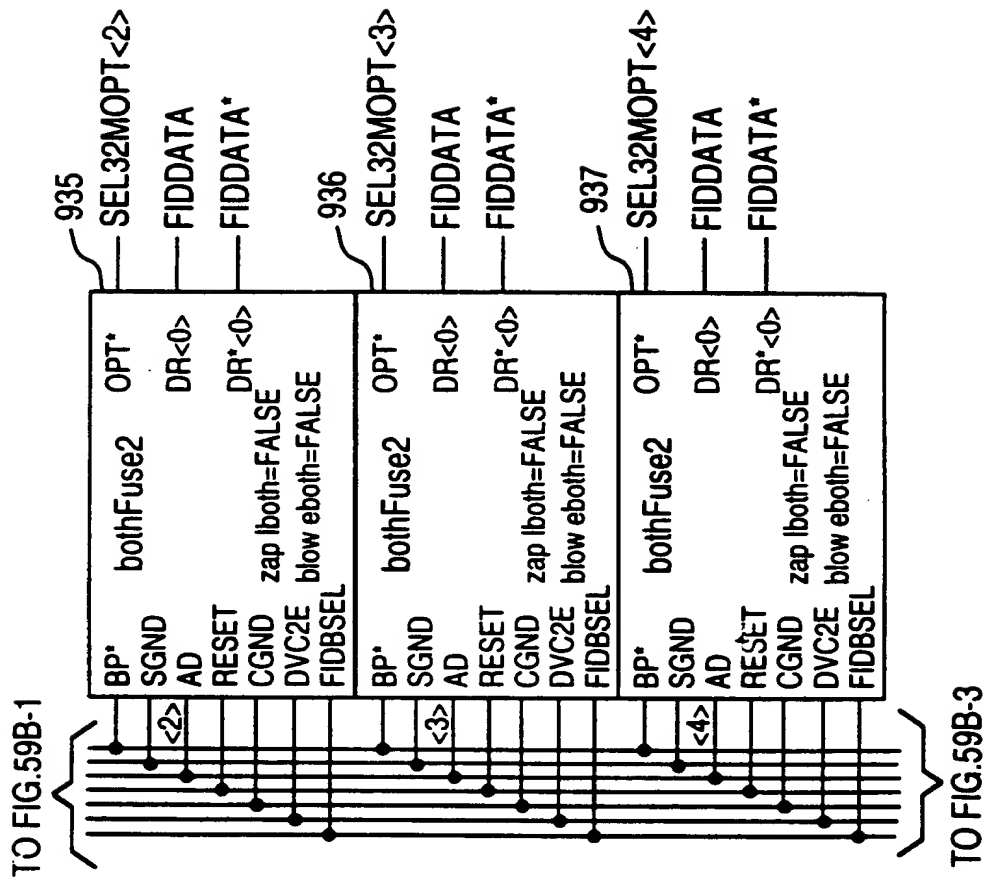


FIG. 59B-2

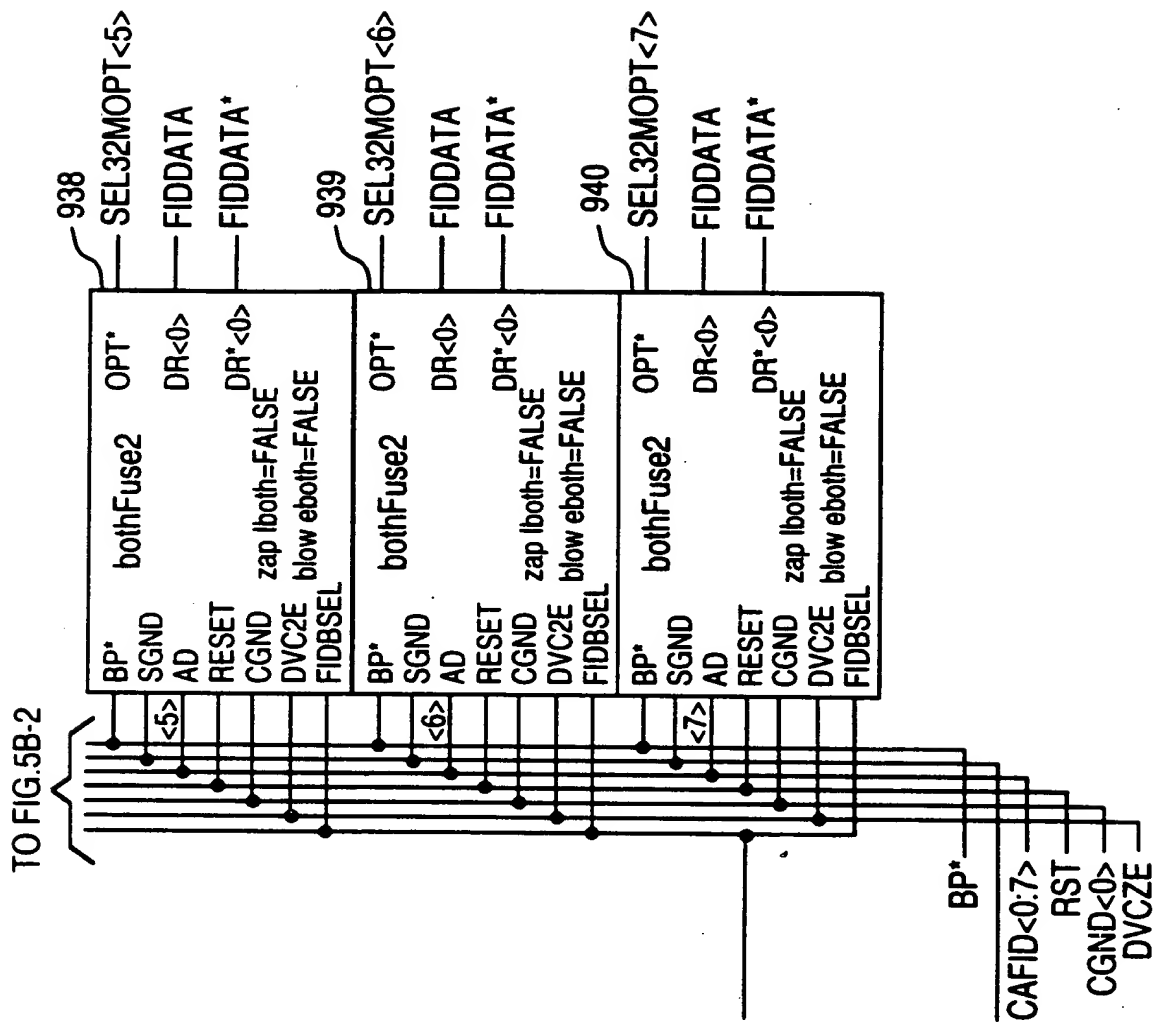
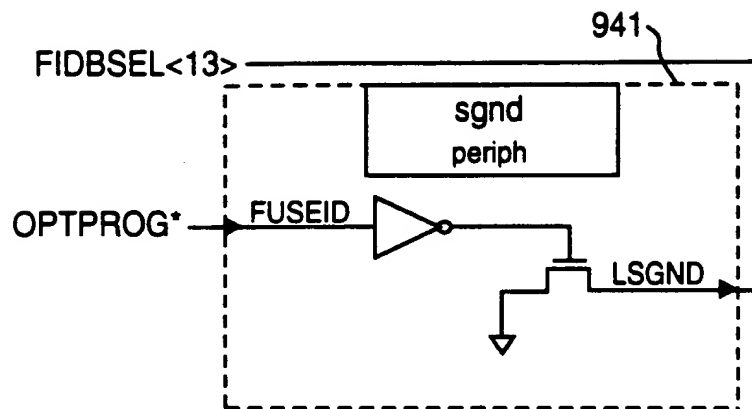


FIG. 59B-3

**FIG. 59C**

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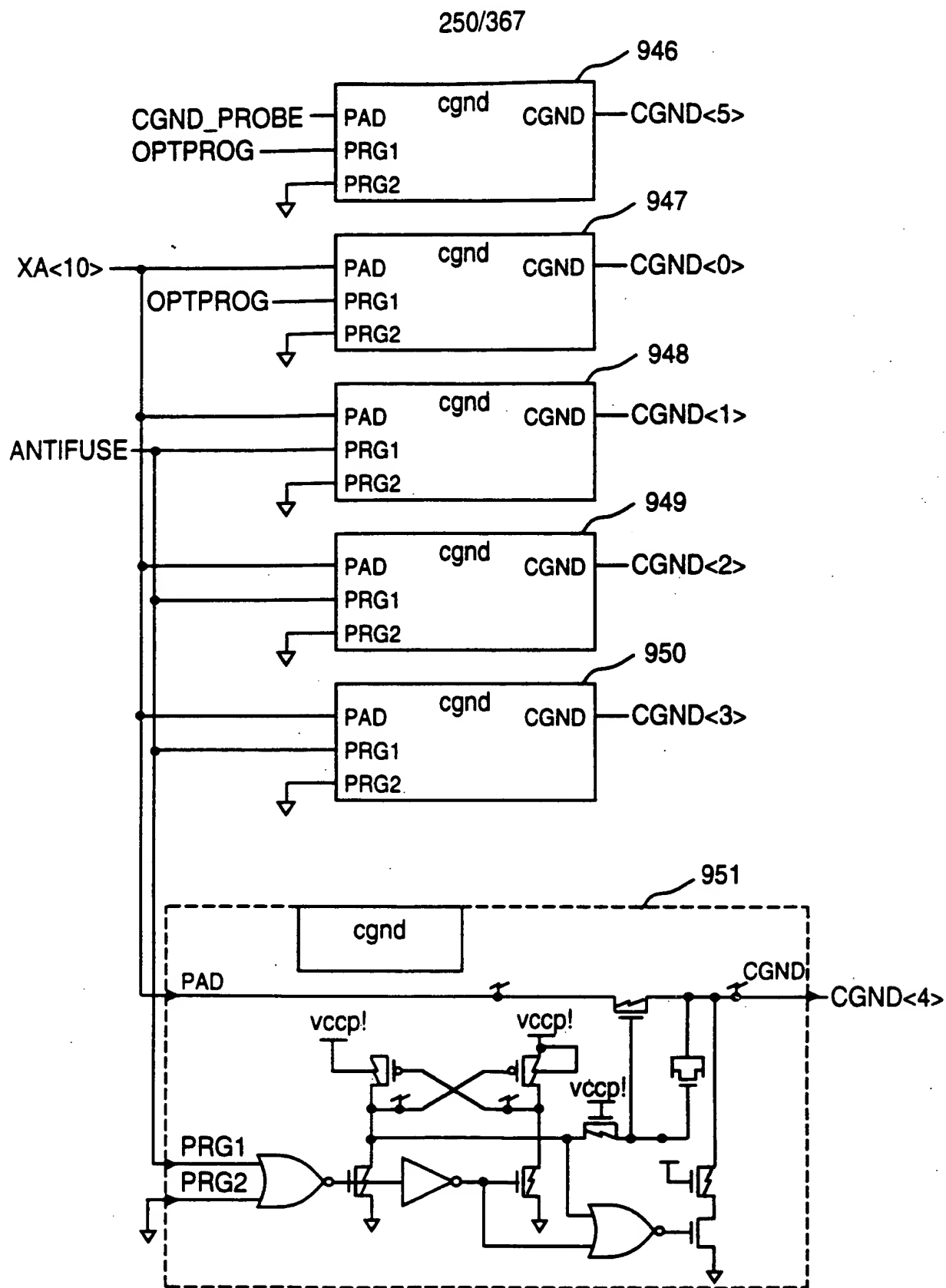


FIG. 59E

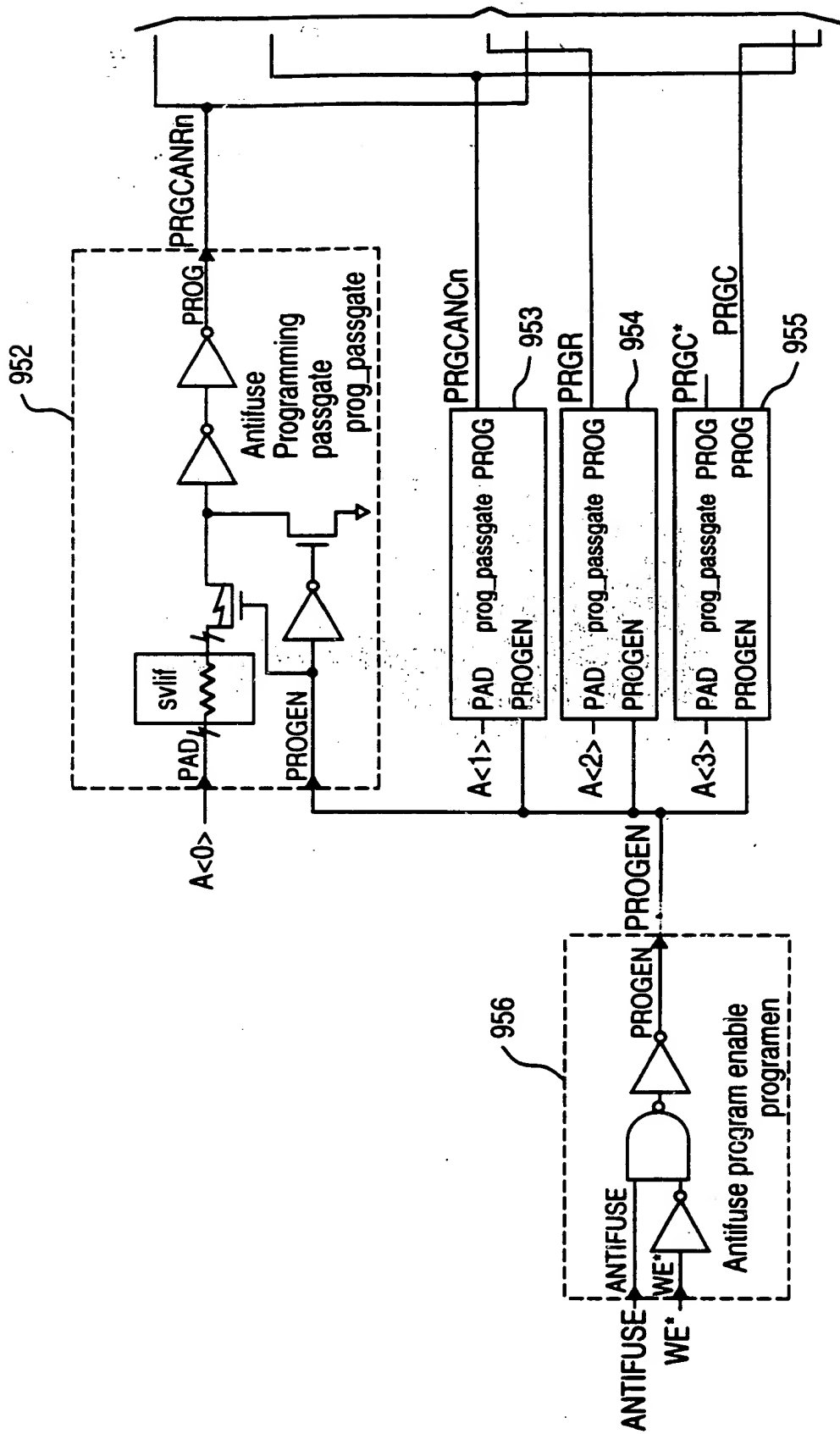


FIG. 59F-1

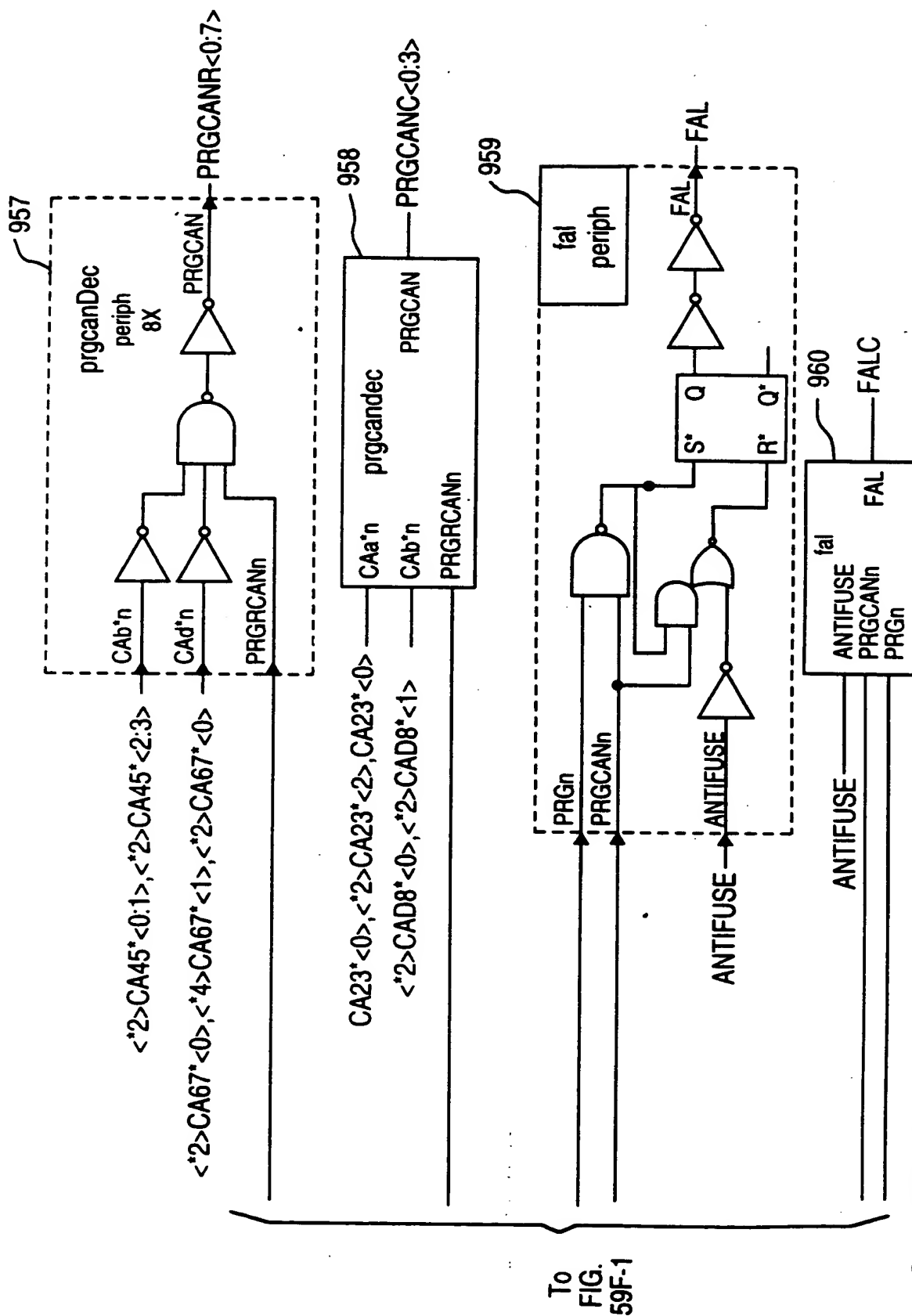


FIG. 59F-2

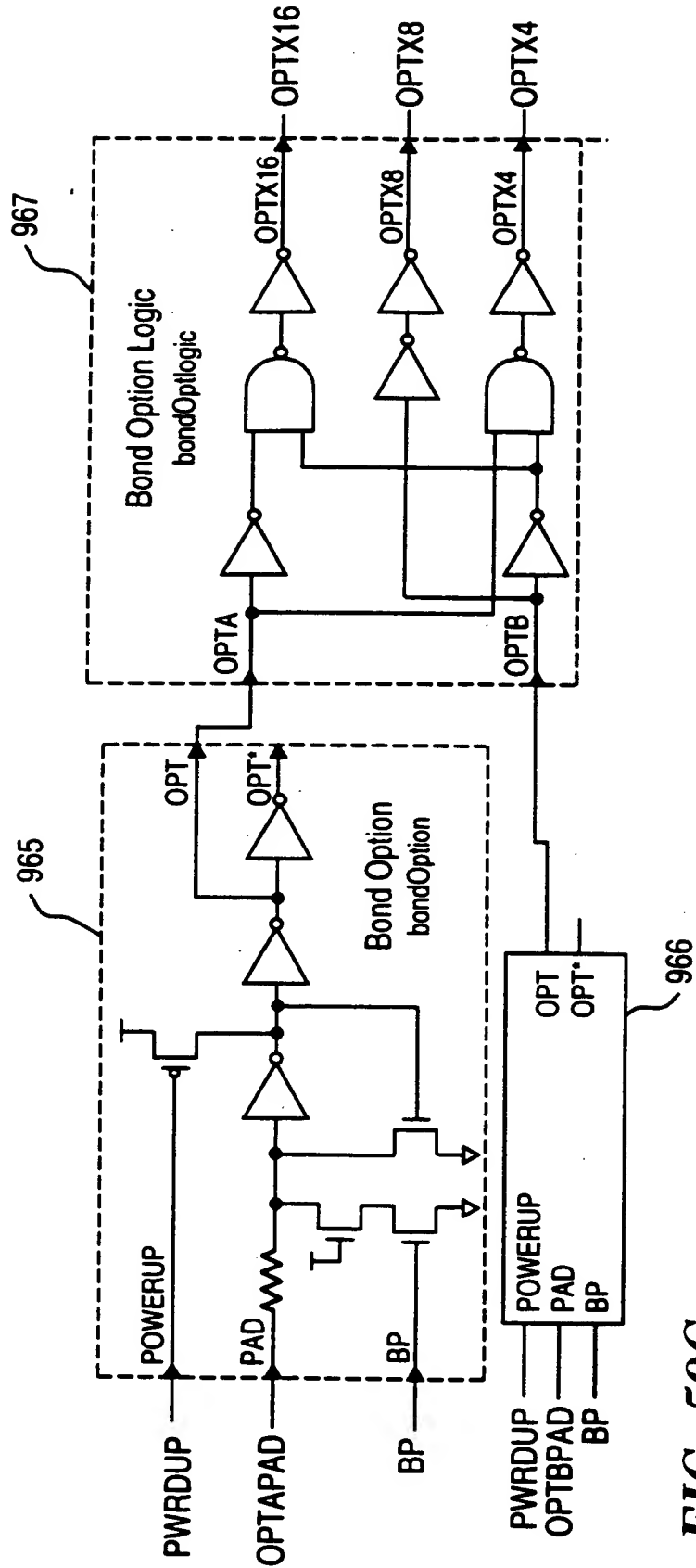


FIG. 59C

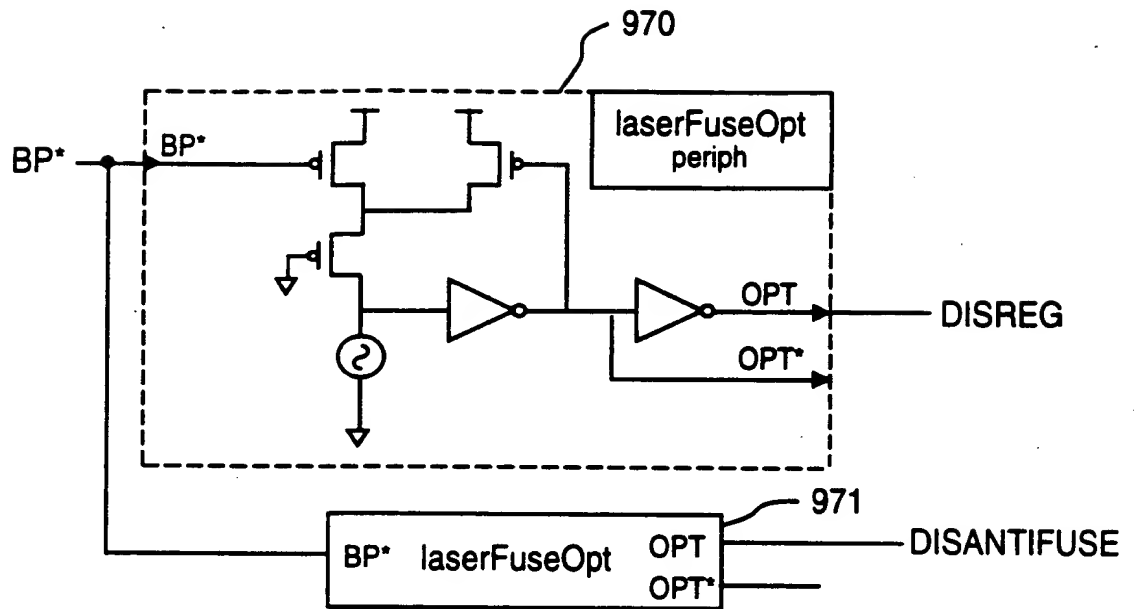


FIG. 59H

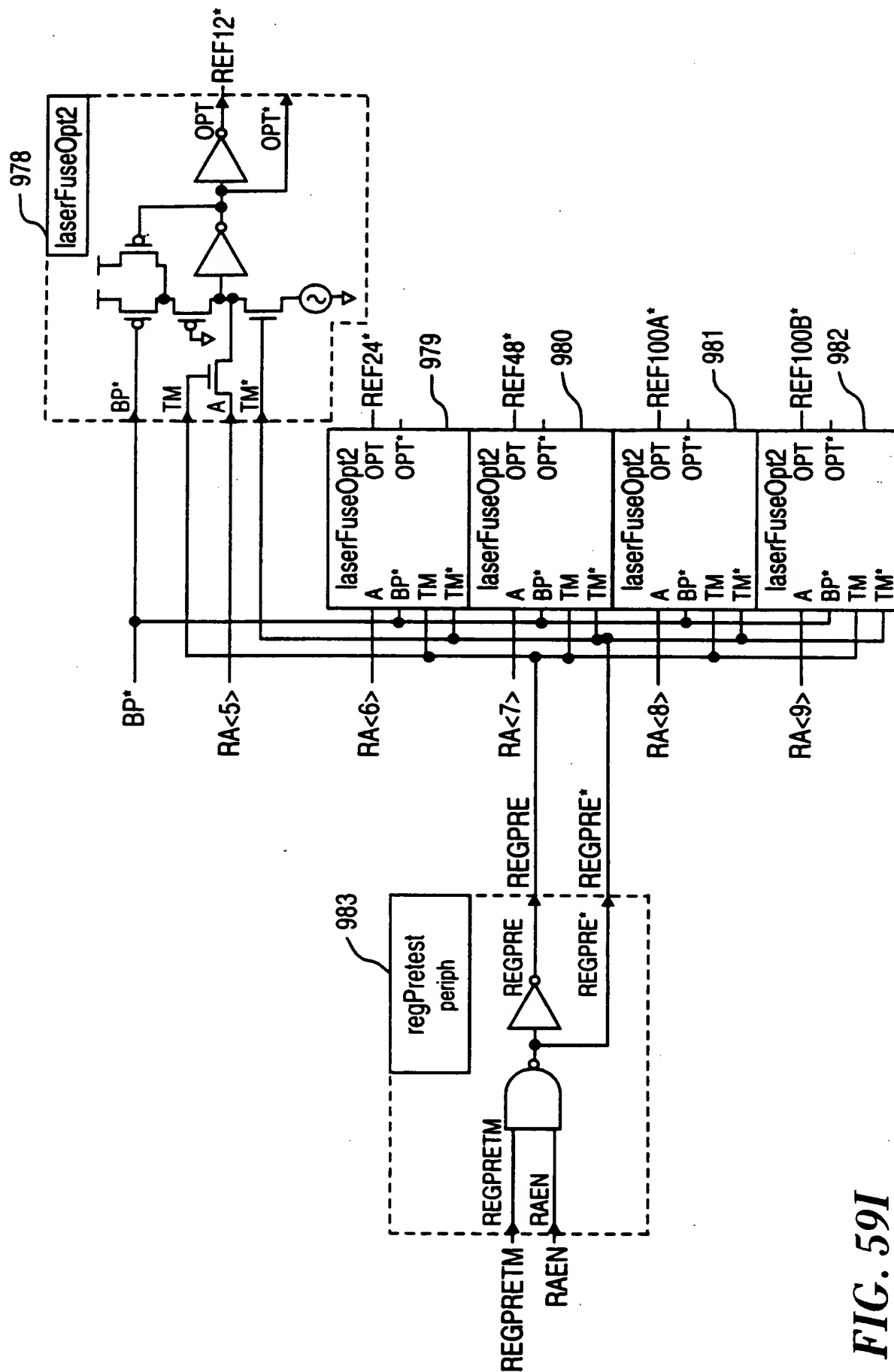
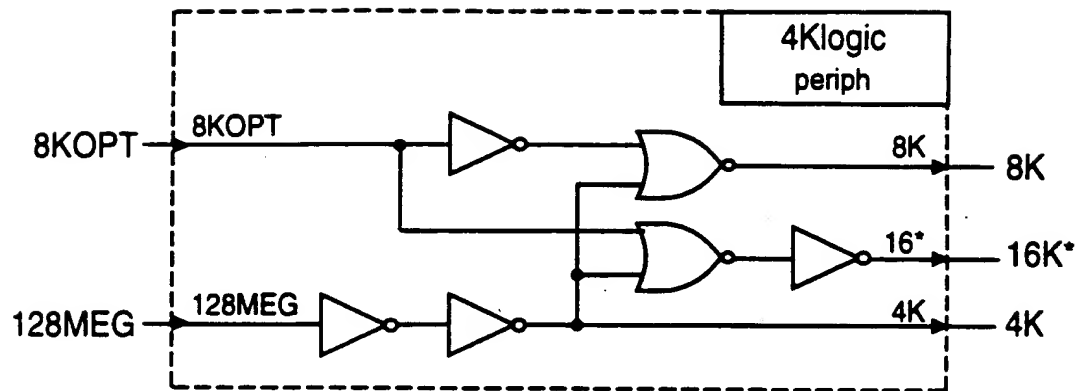
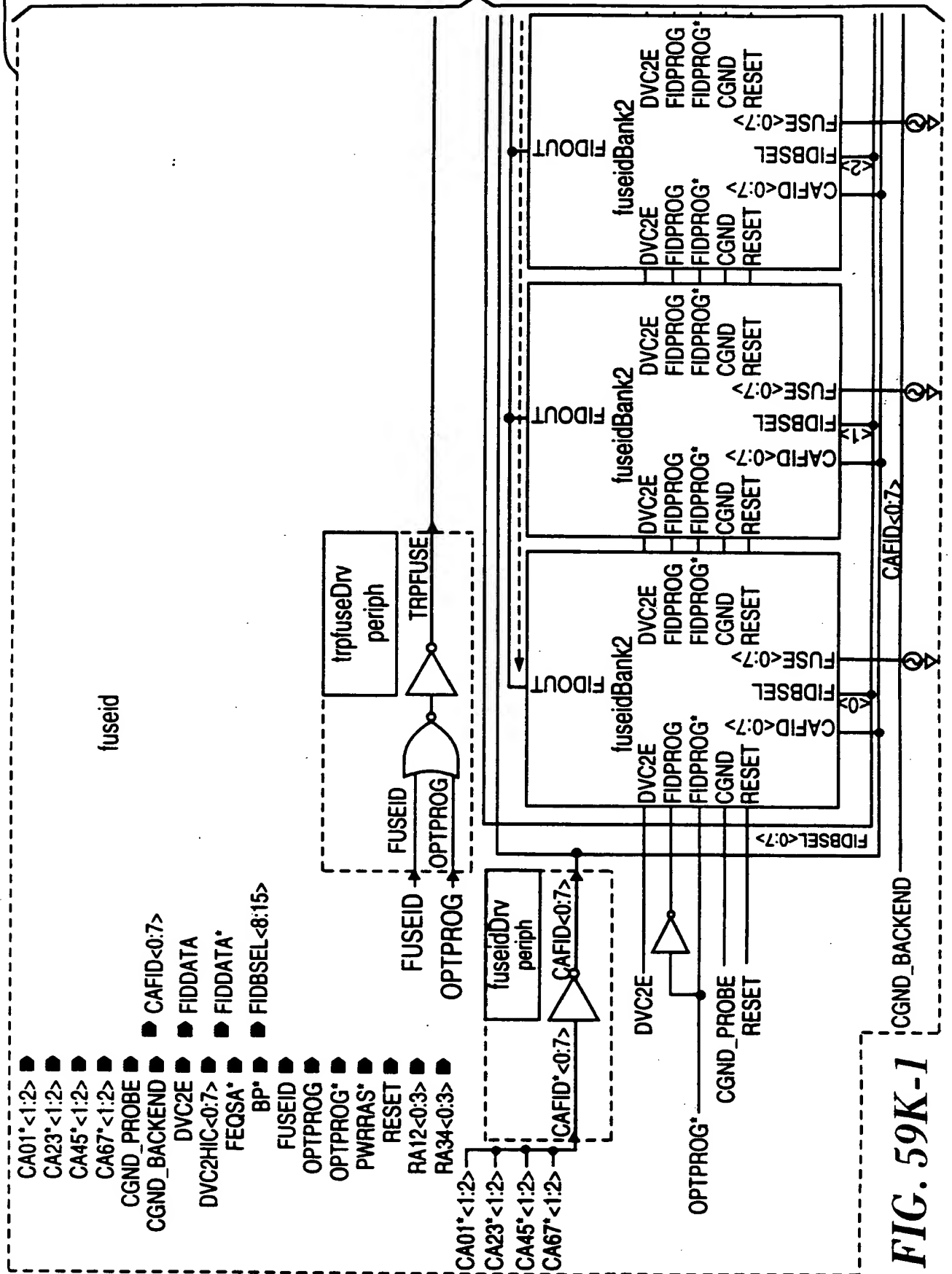


FIG. 591

**FIG. 59J**



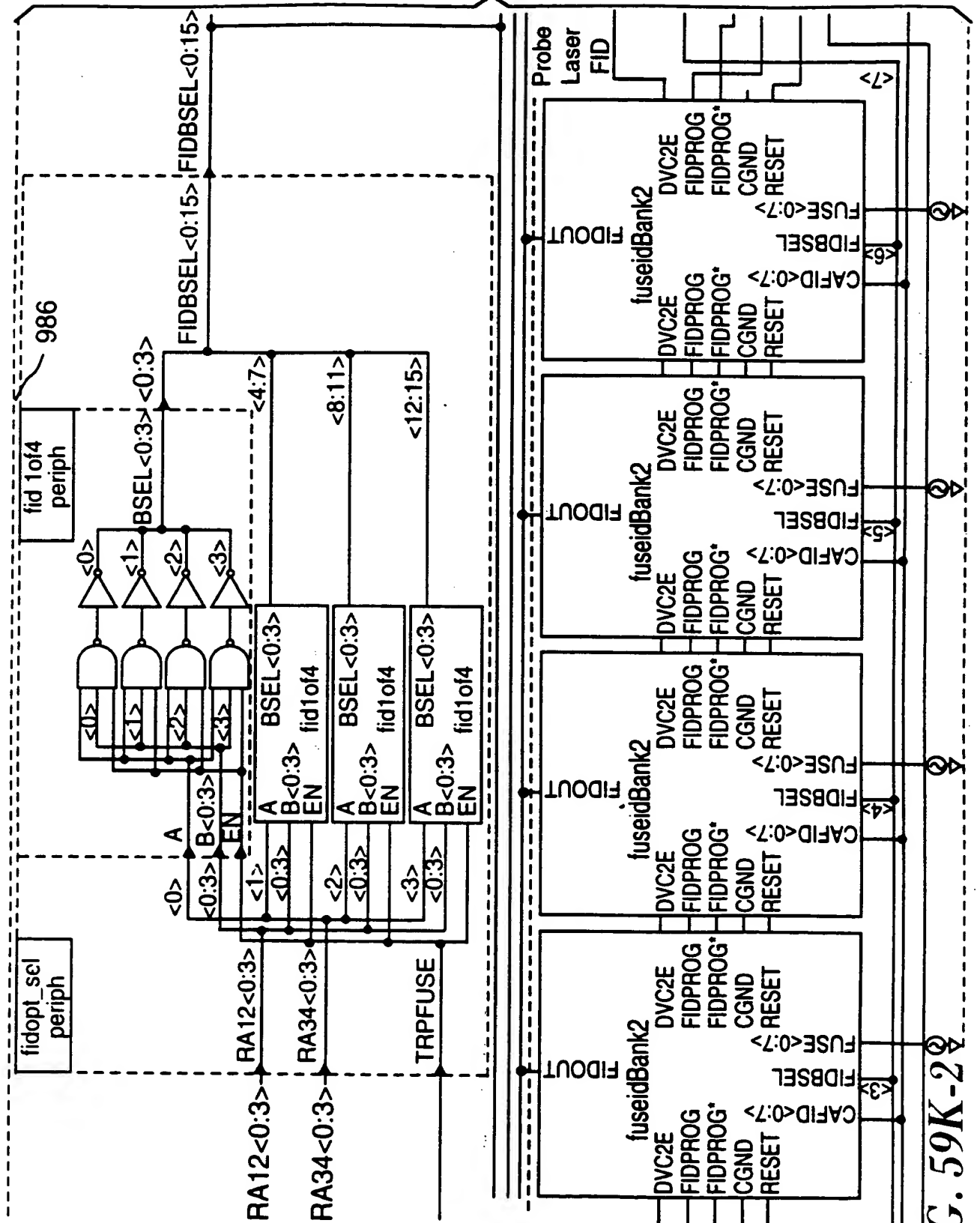


FIG. 59K-2

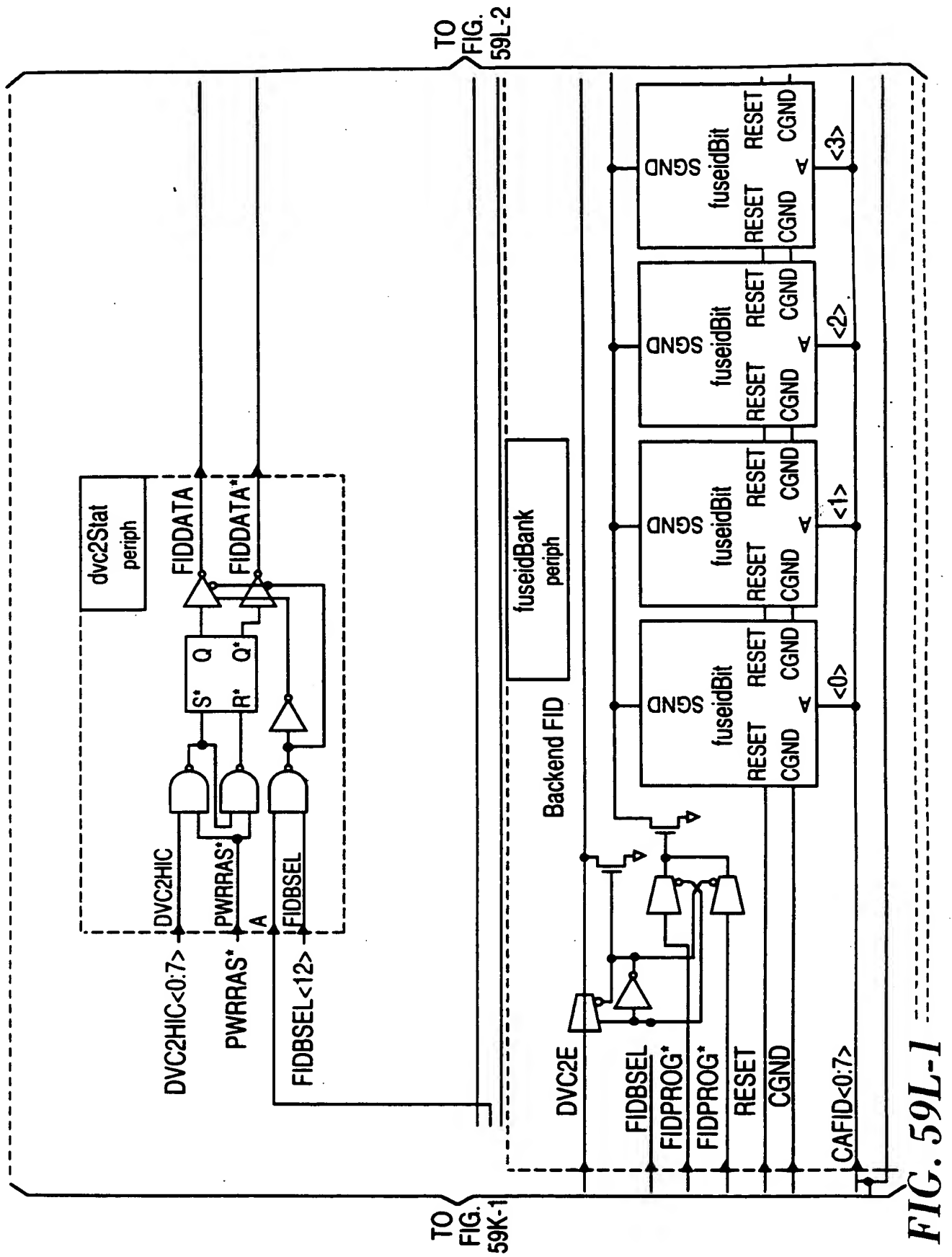
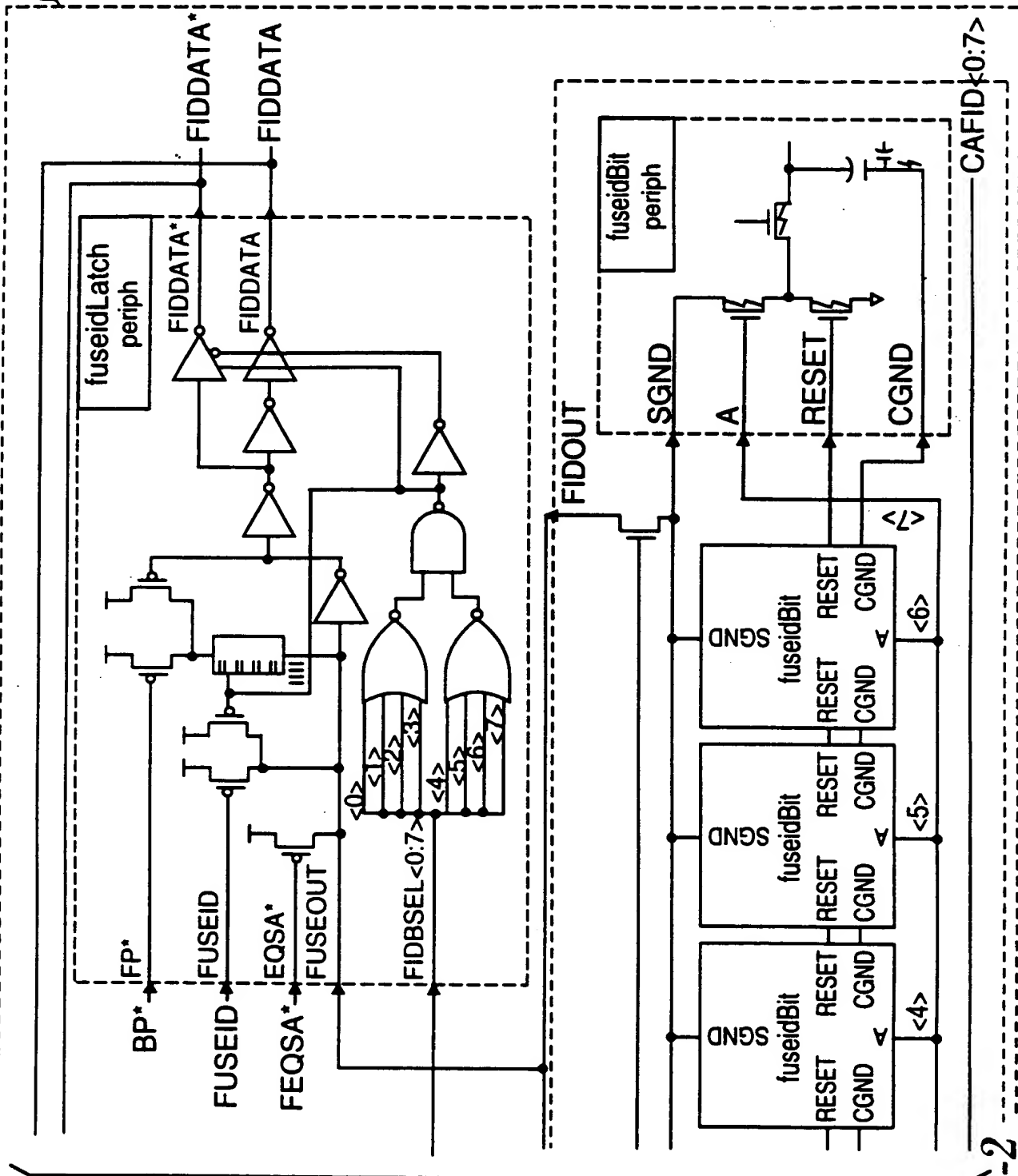


FIG. 59L-1

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TO
FIG.
59L-1

FIG. 59L-2

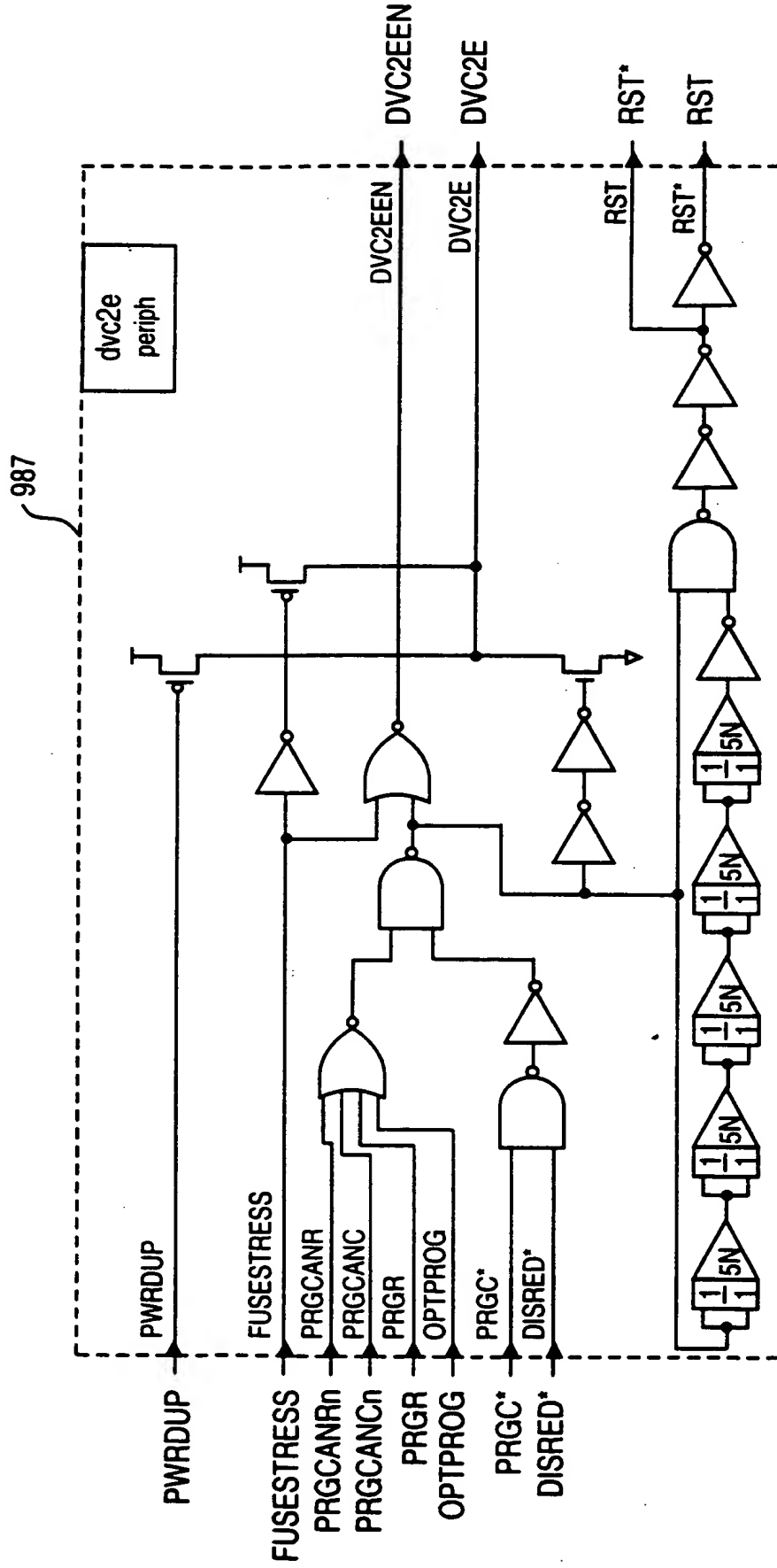
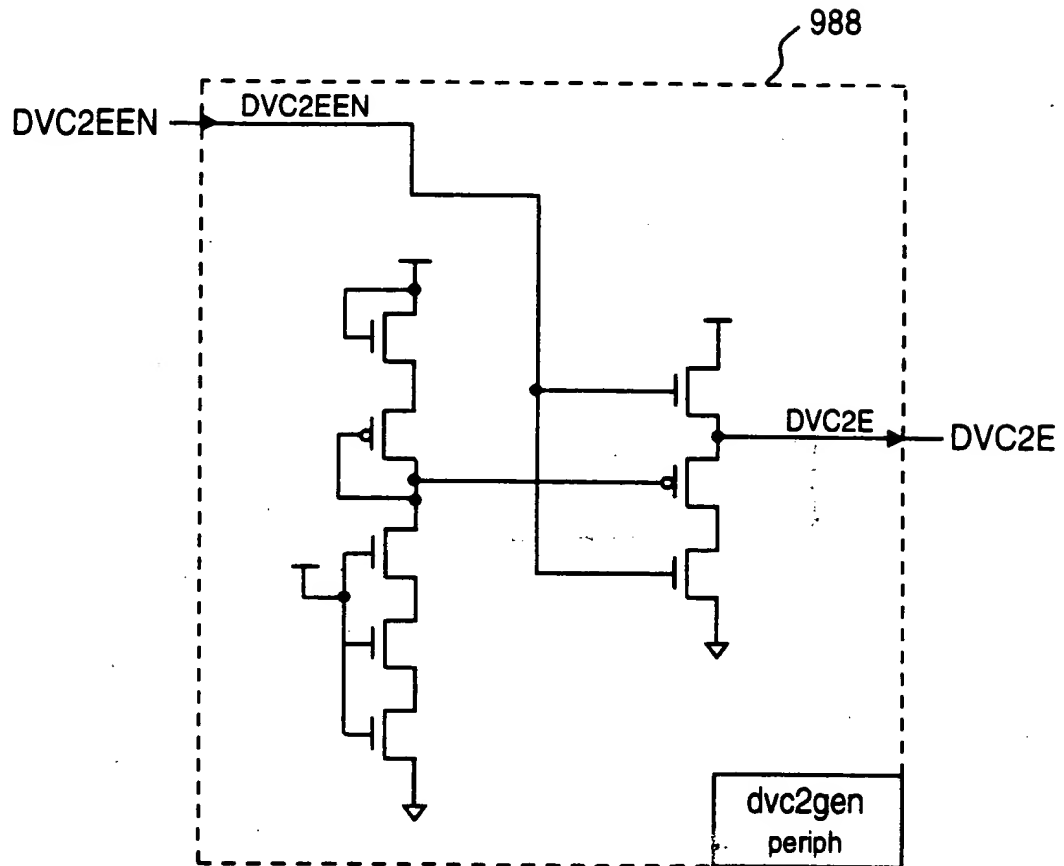


FIG. 59M

**FIG. 59N**

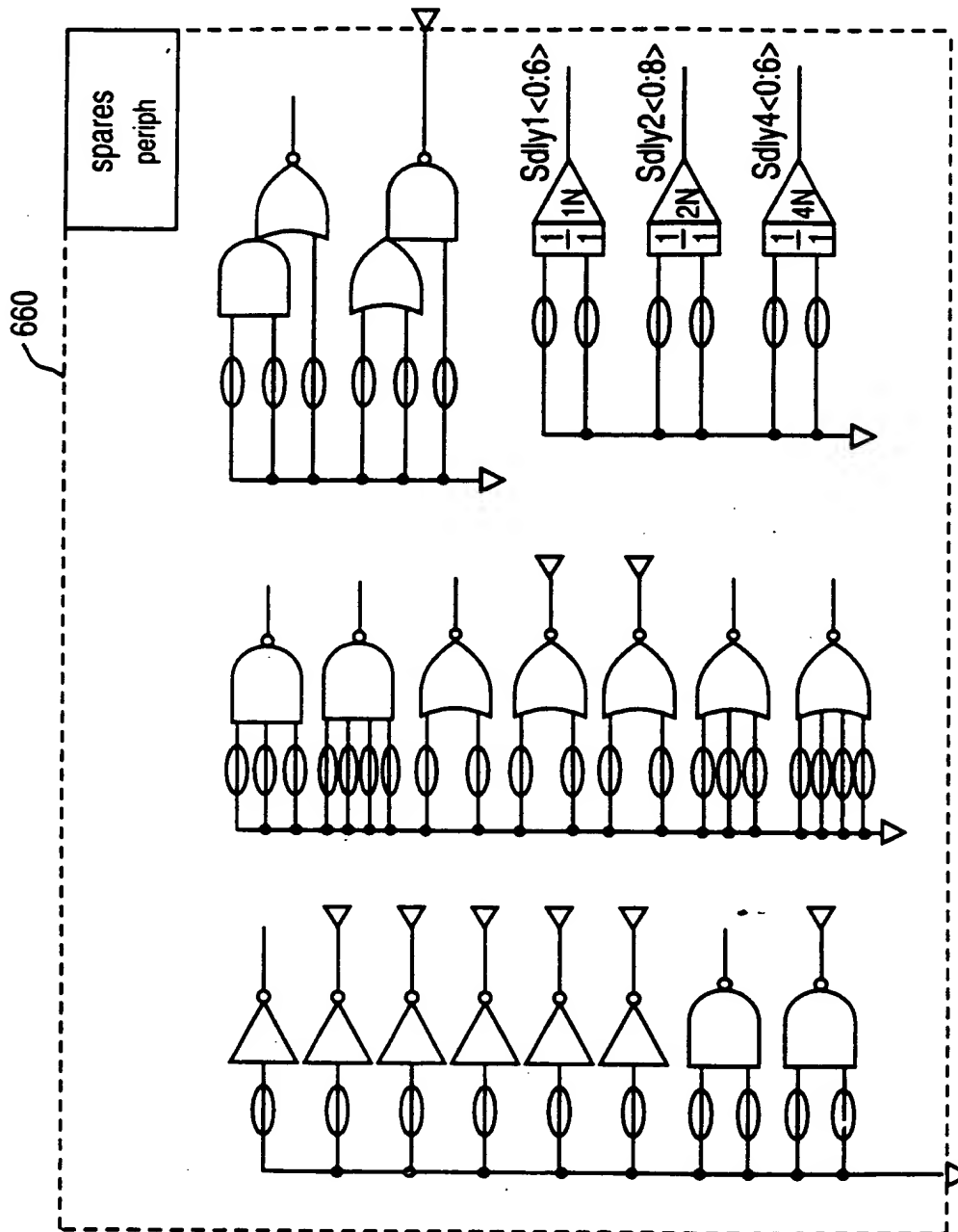
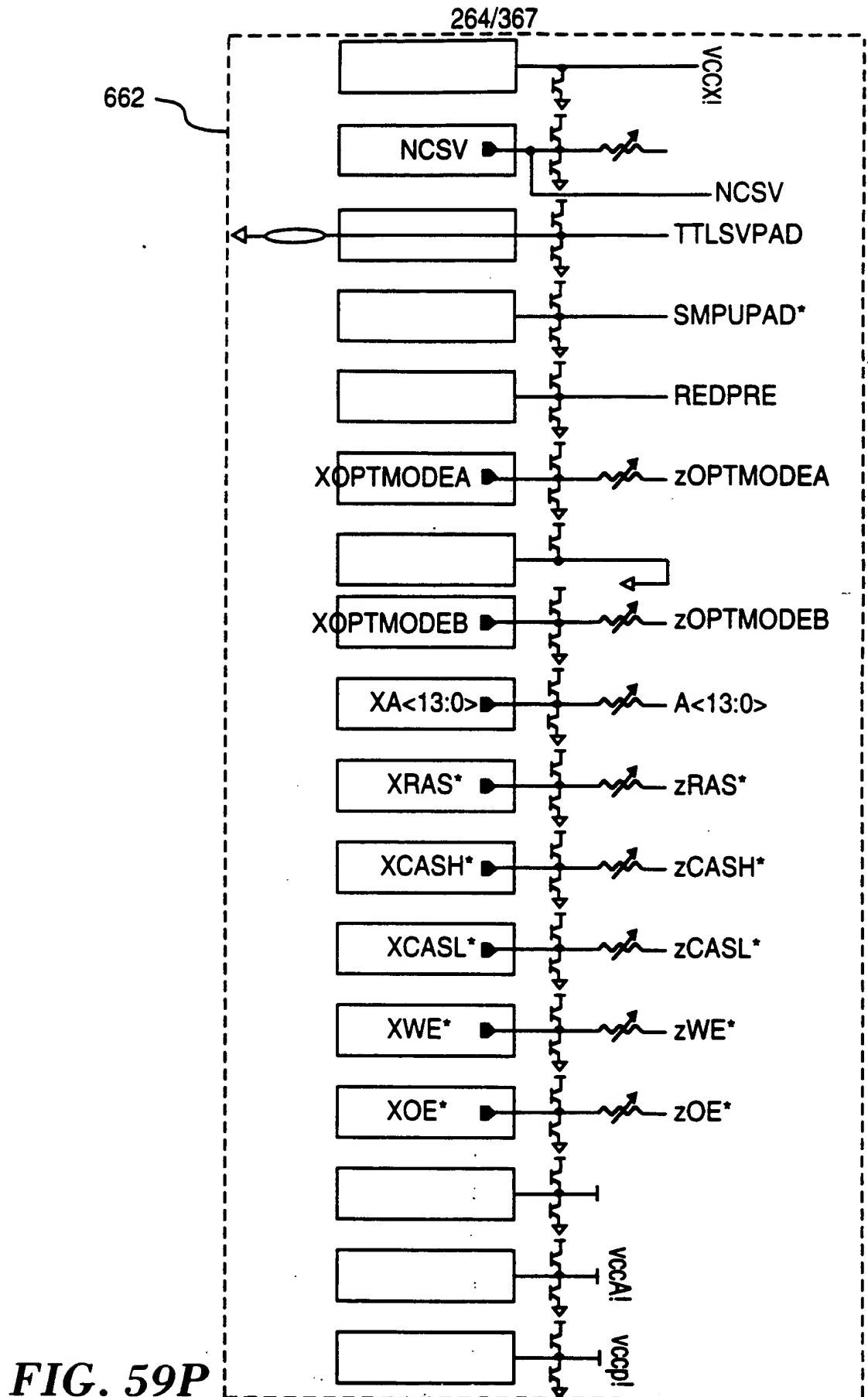


FIG. 590



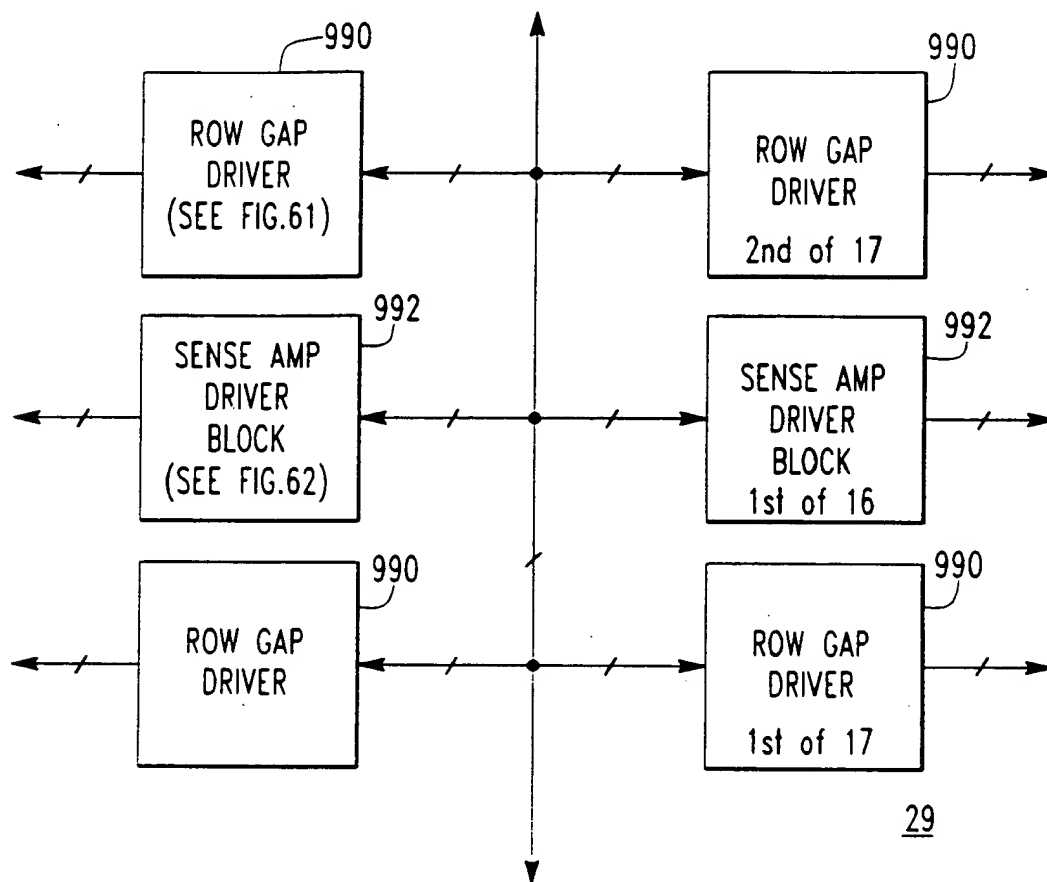


FIG. 60

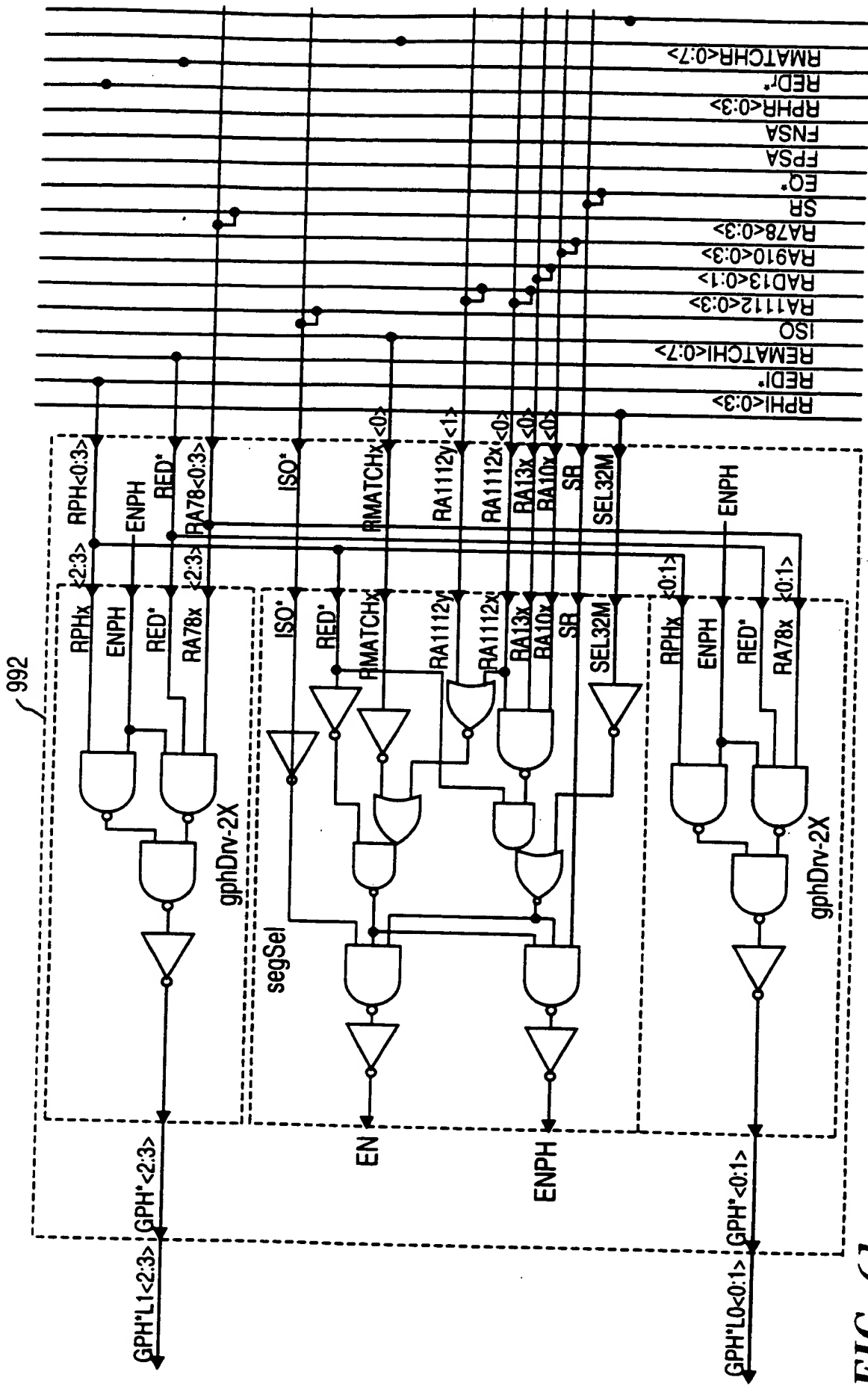


FIG. 61

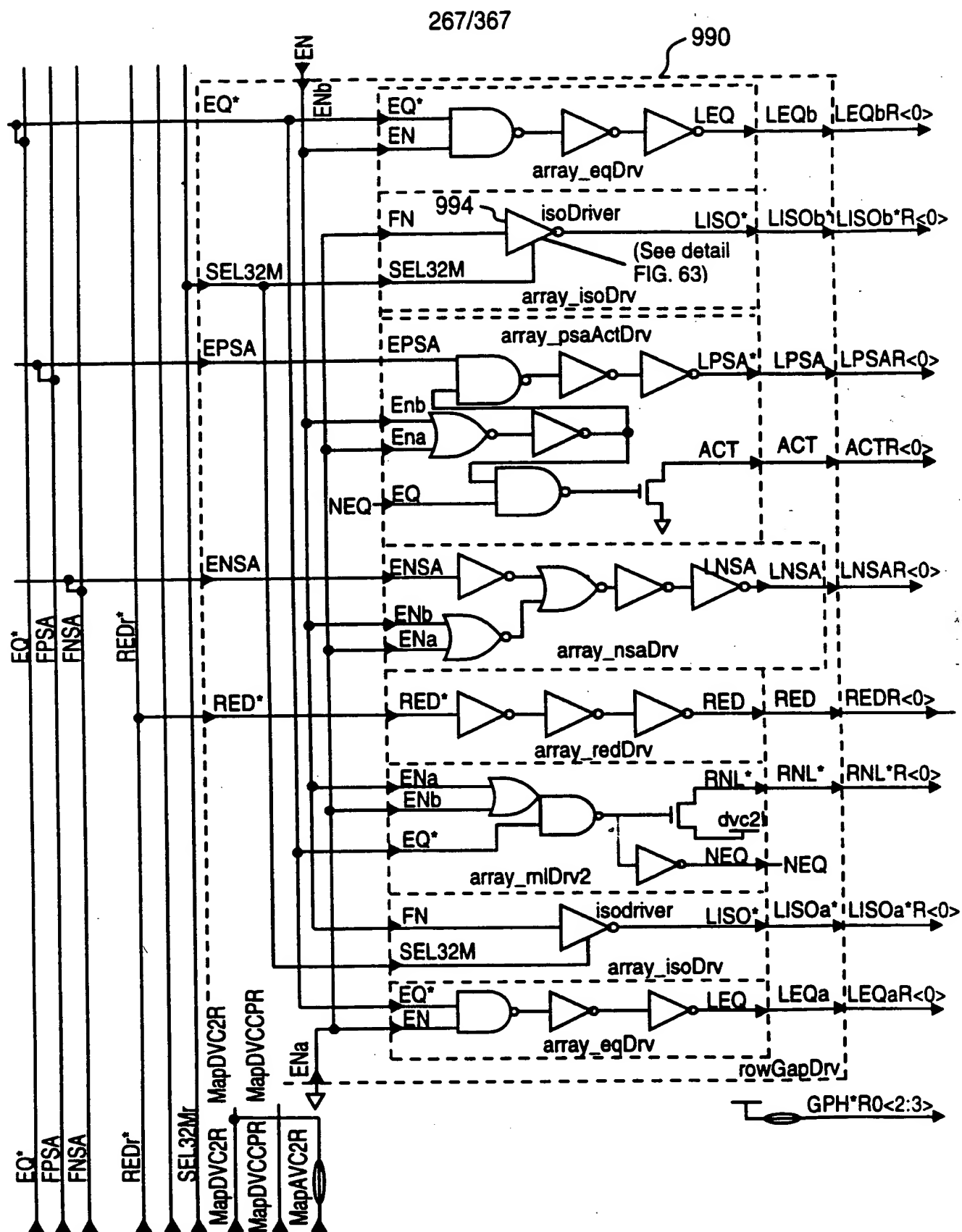


FIG. 62

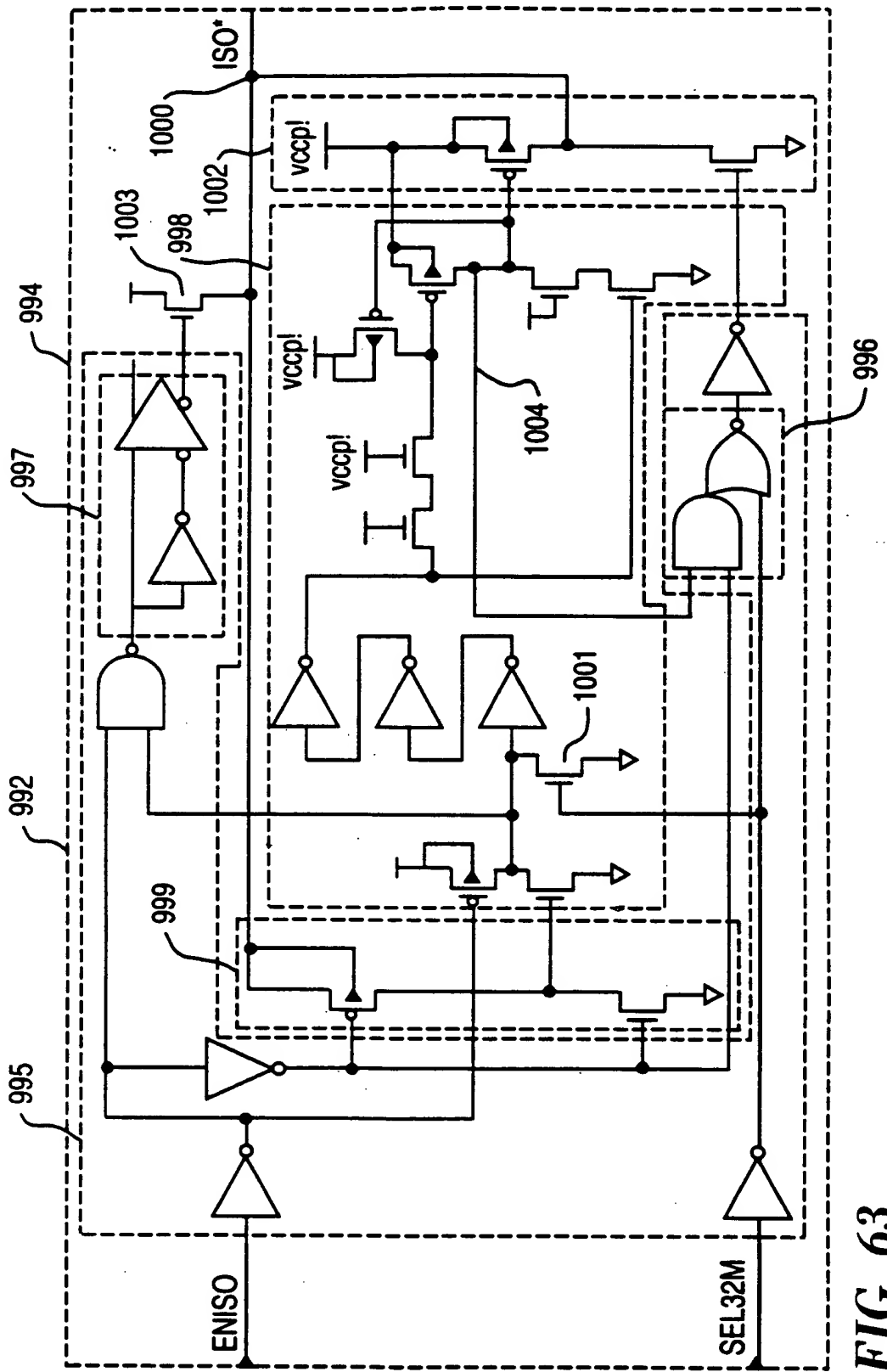


FIG. 63

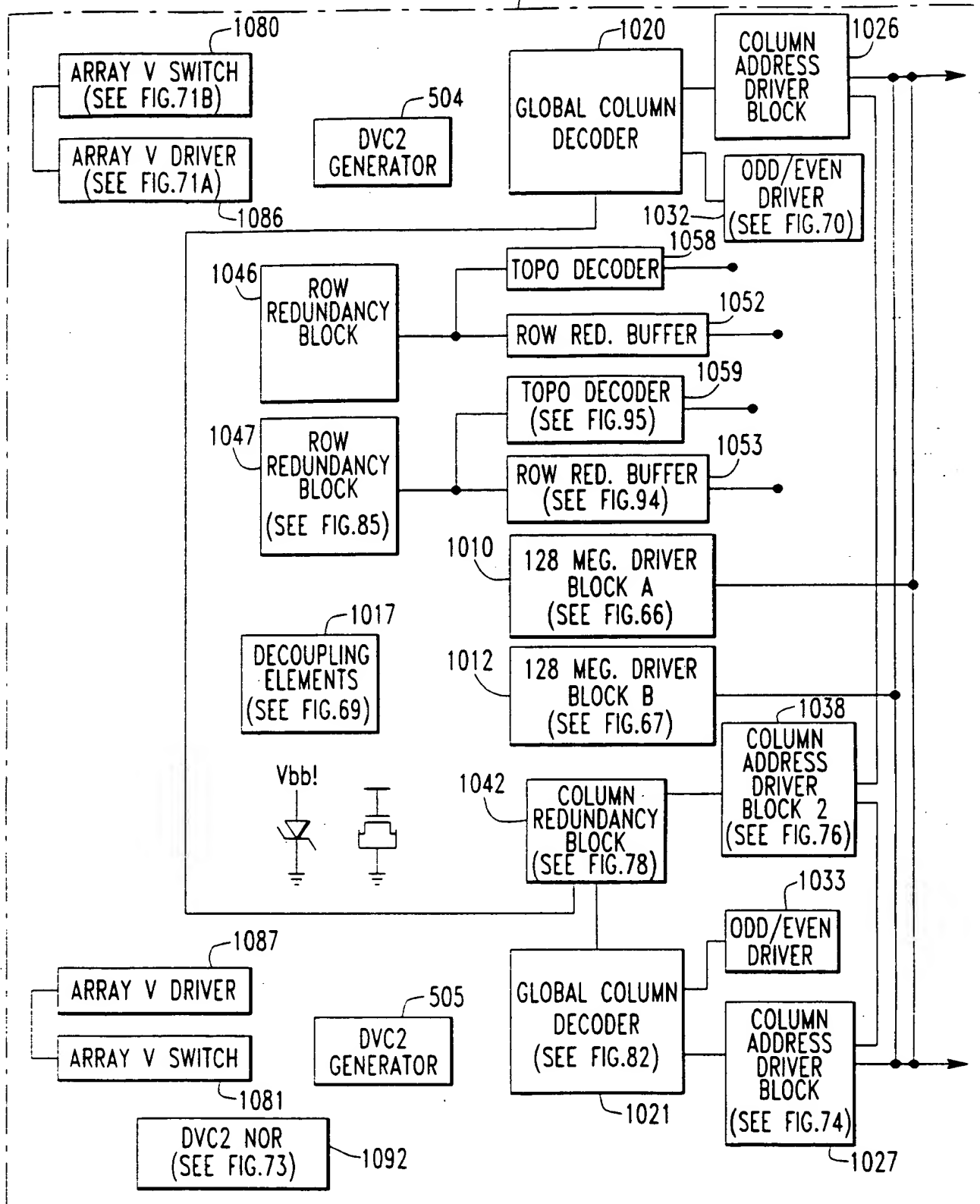


FIG. 64A

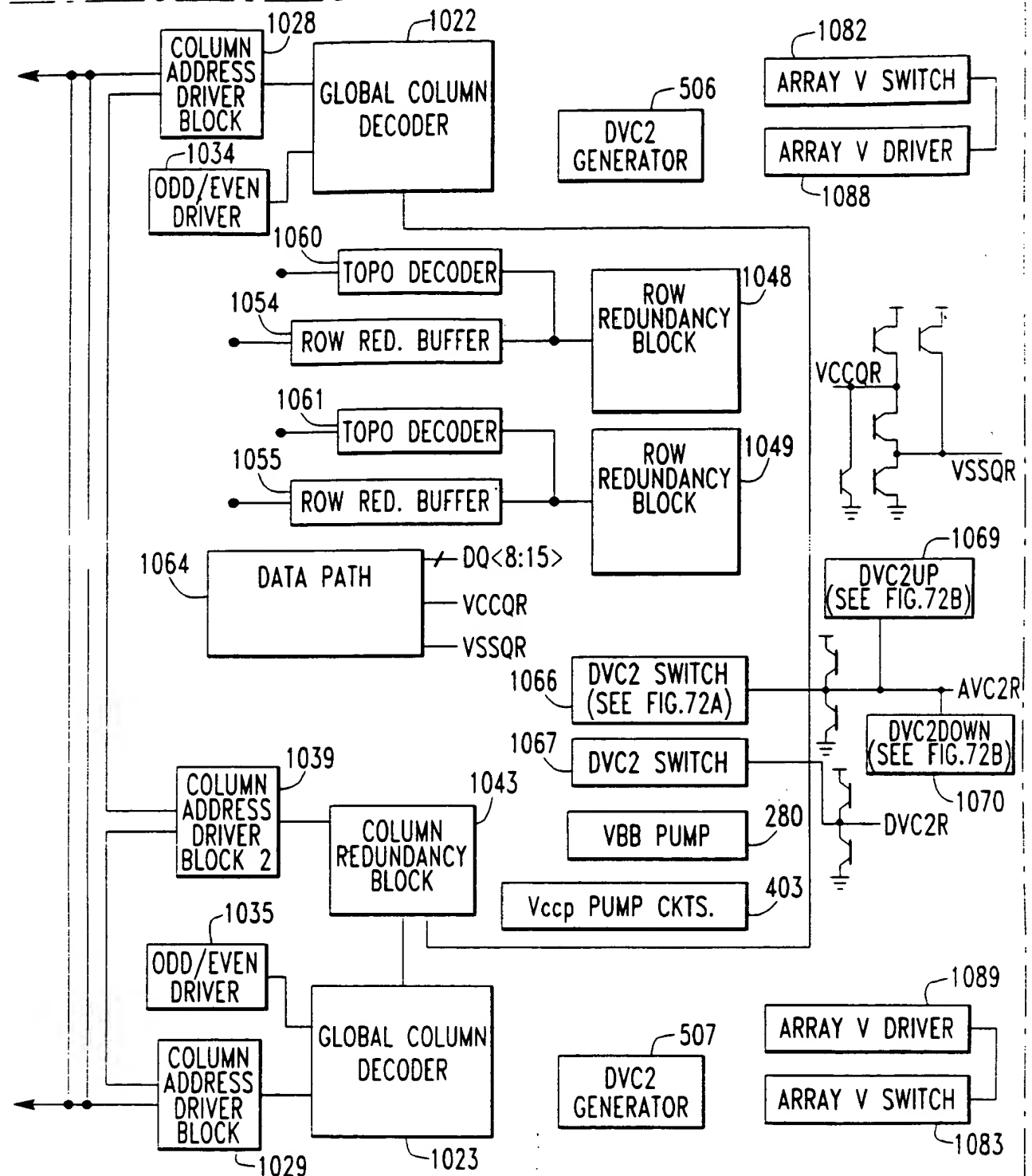


FIG. 64B

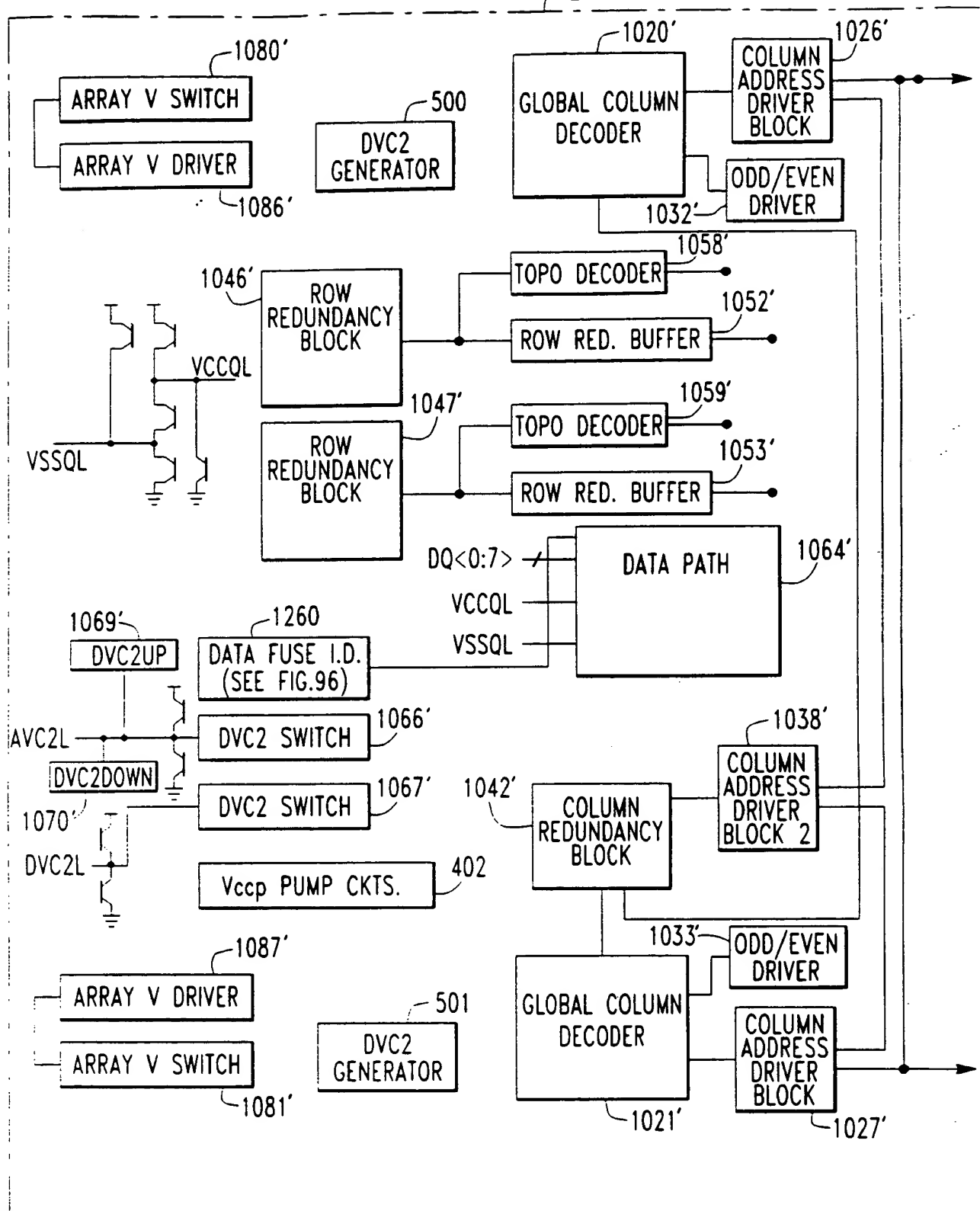


FIG. 65A

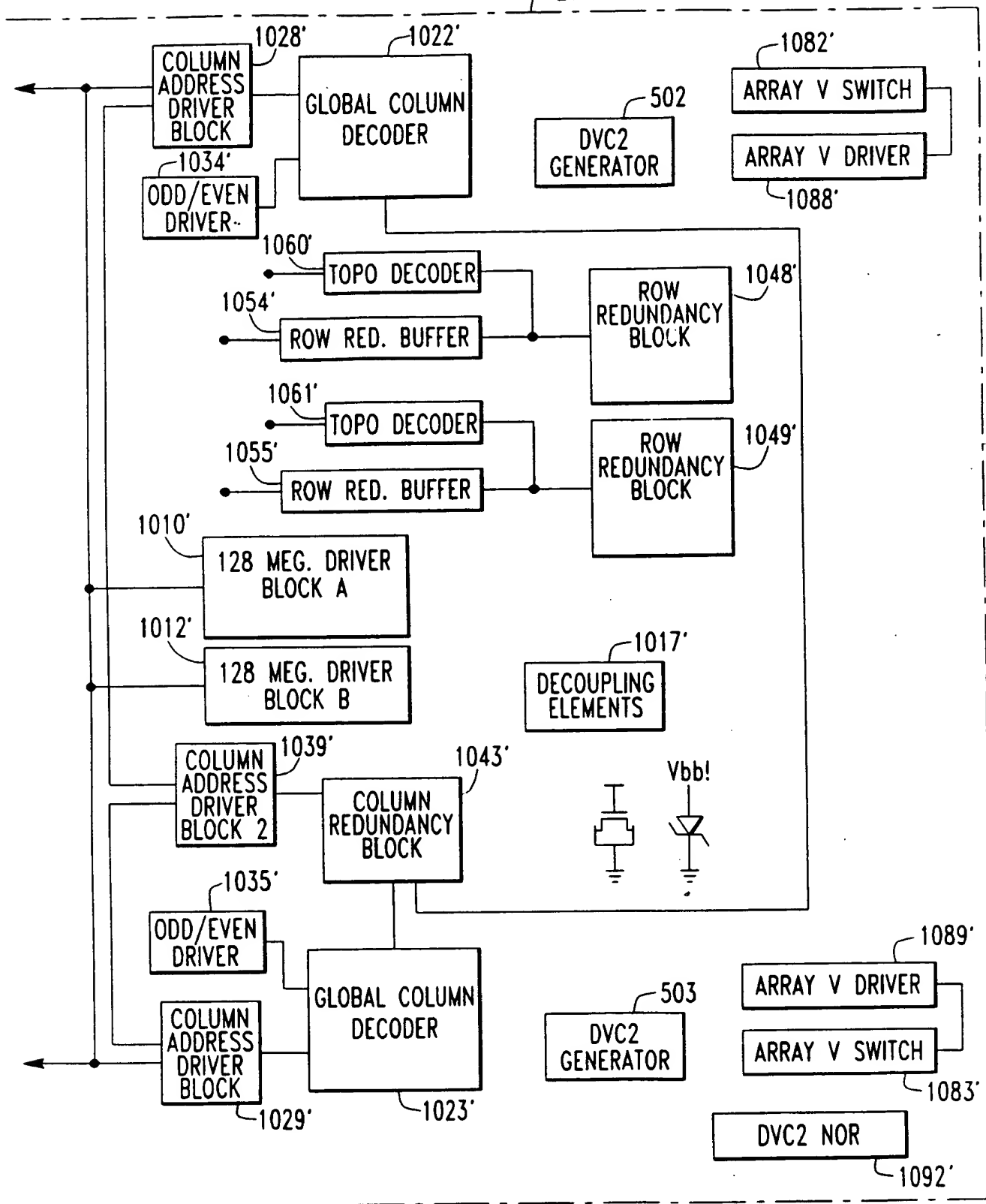


FIG. 65B

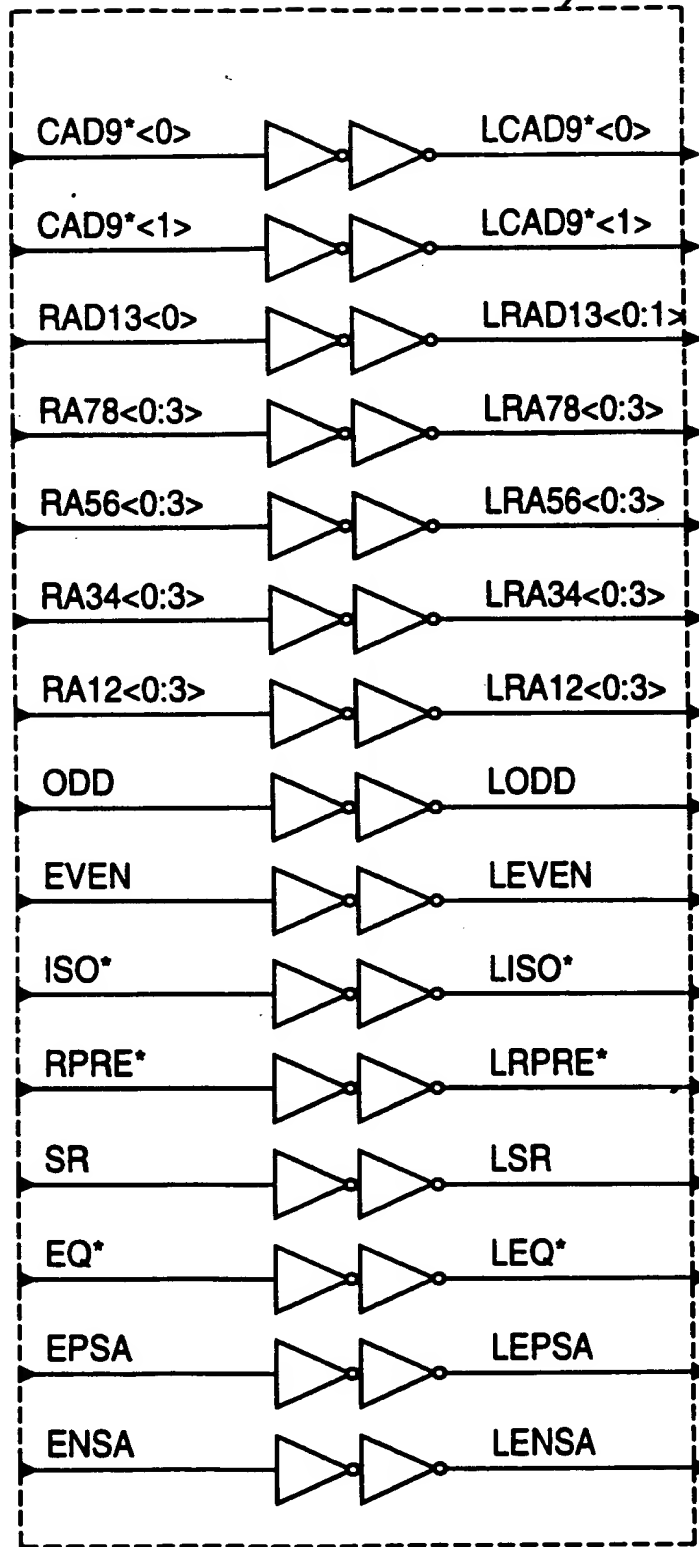
**FIG. 66**

TABLE 90-5826360

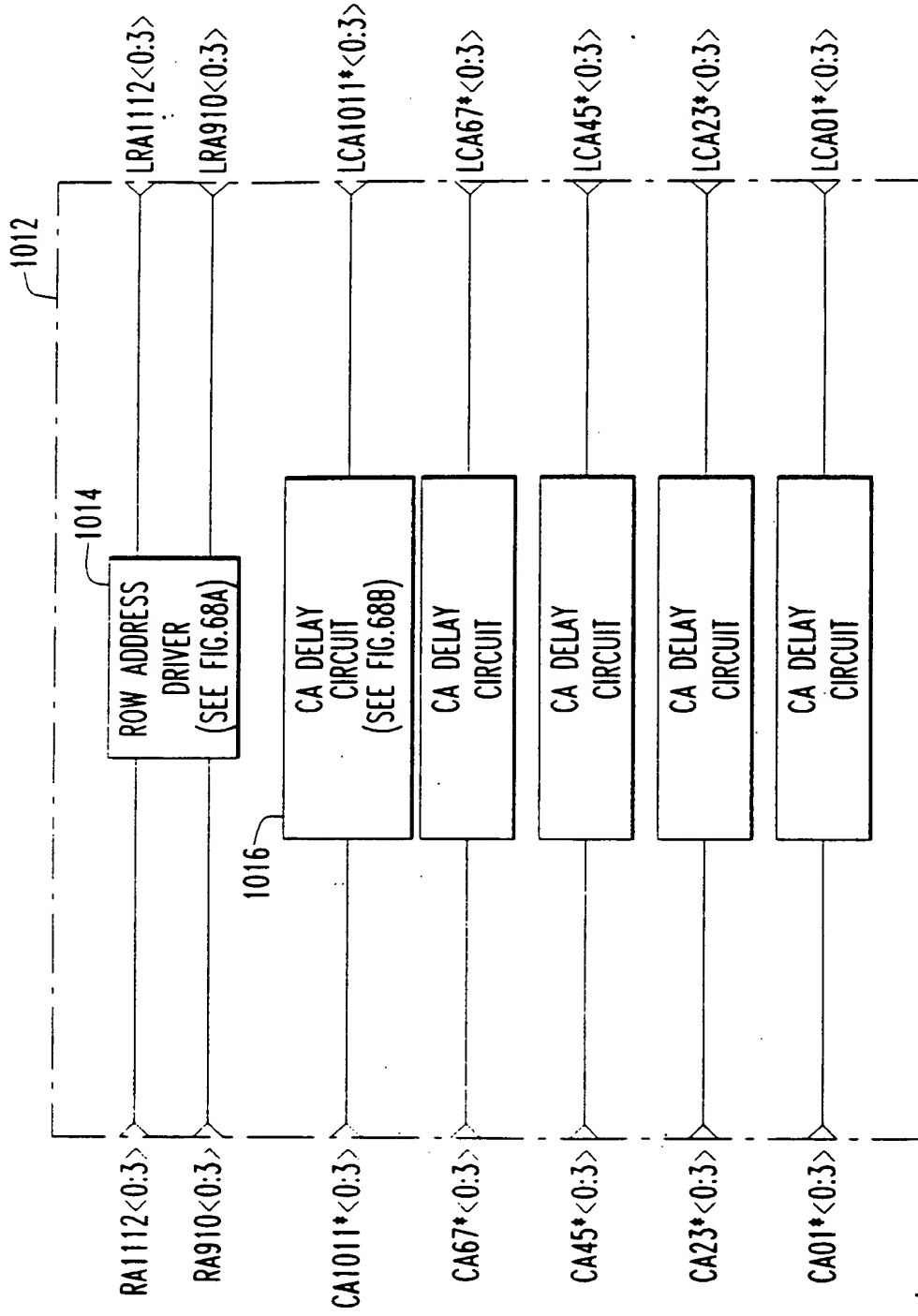
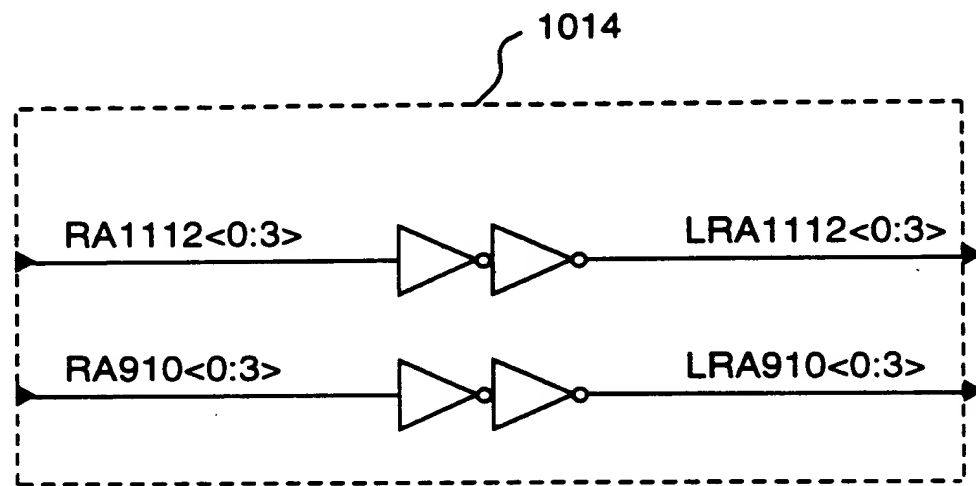
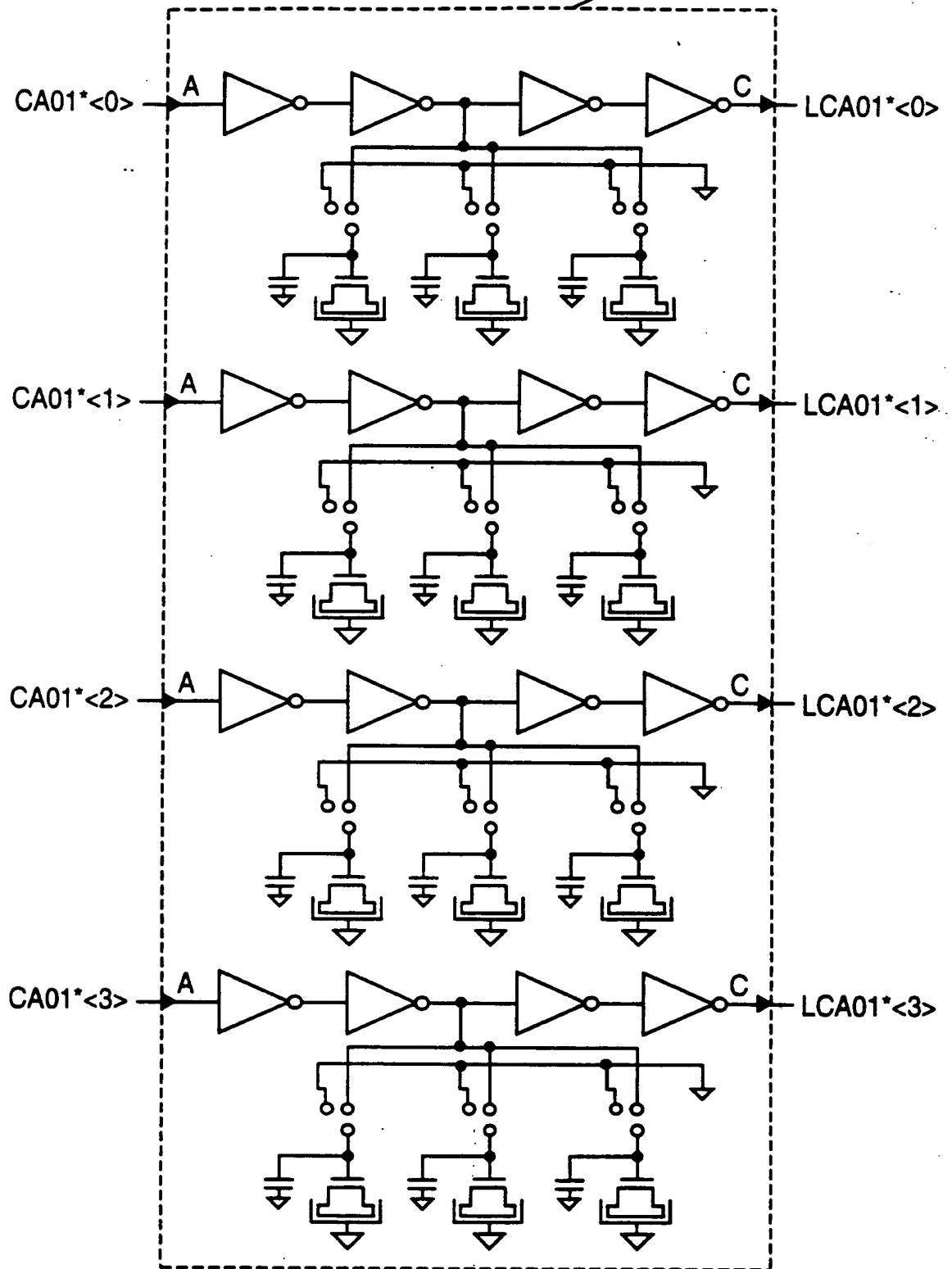
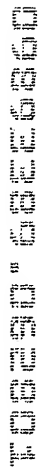
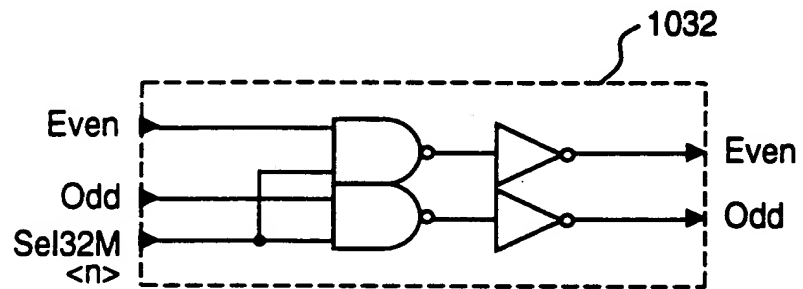


FIG. 67

**FIG. 68A**

**FIG. 68B**

[illegible]

**FIG. 70**

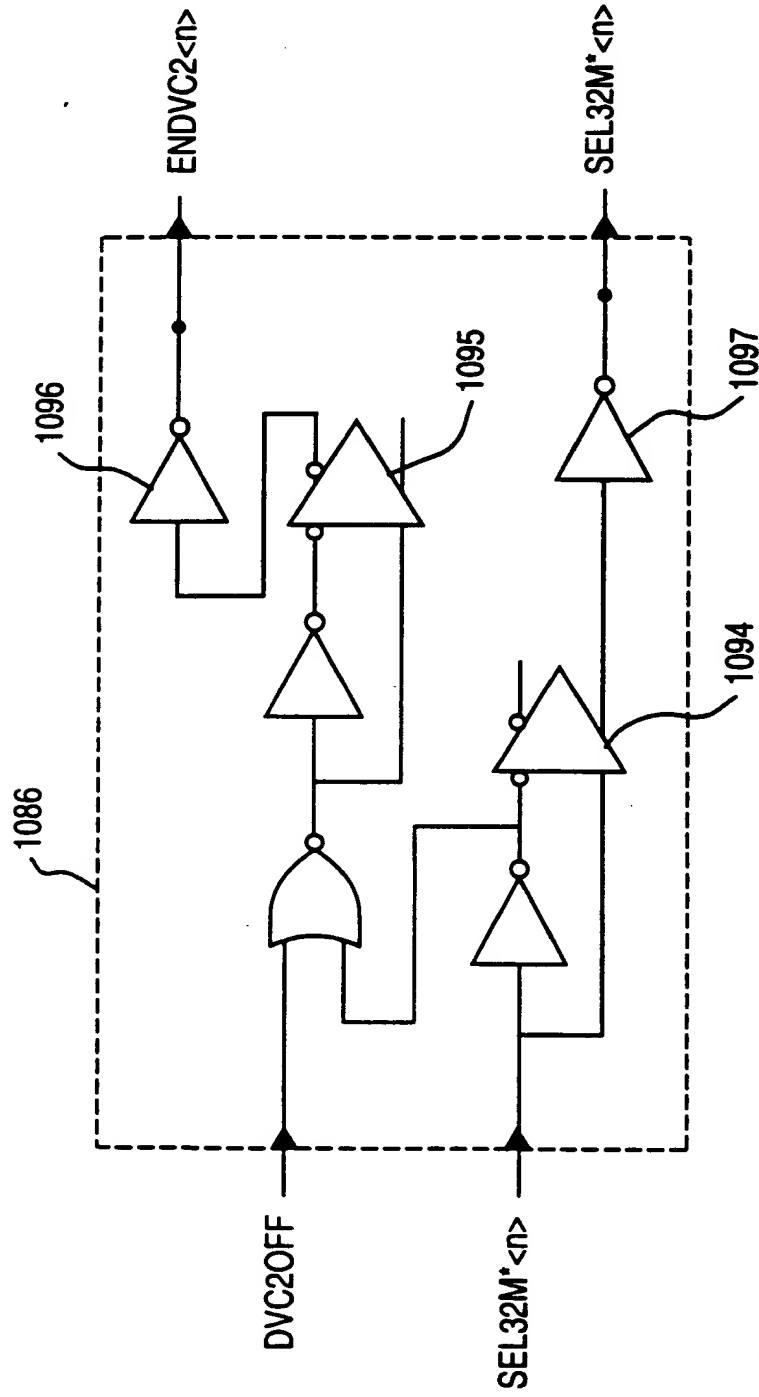
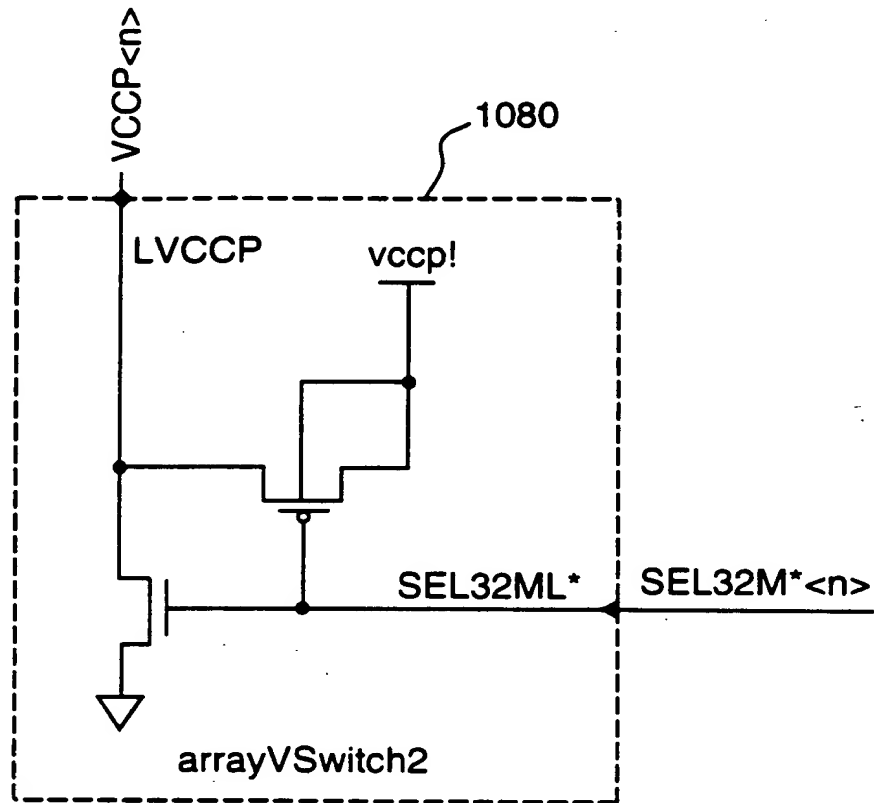


FIG. 71A

**FIG. 71B**

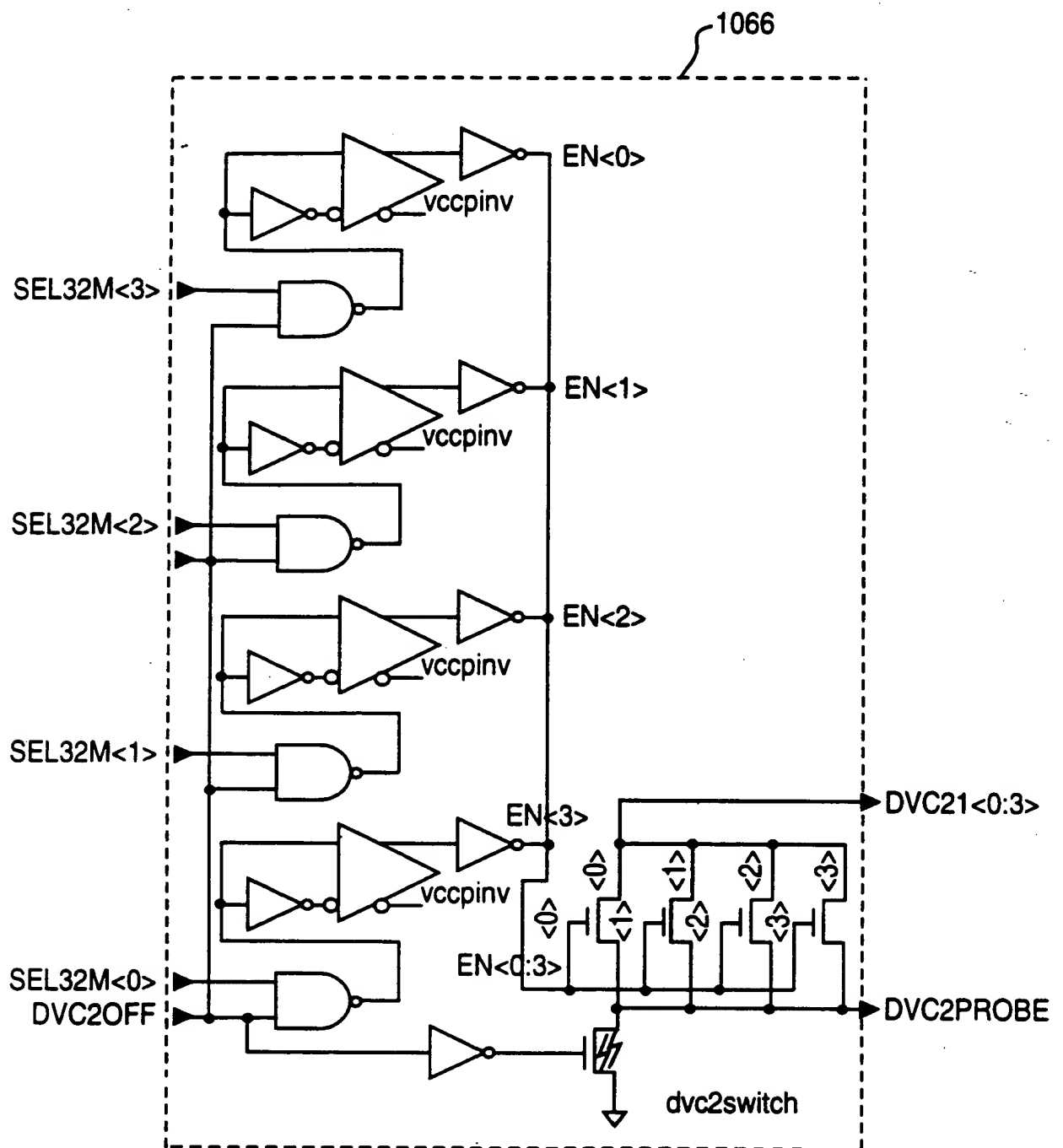
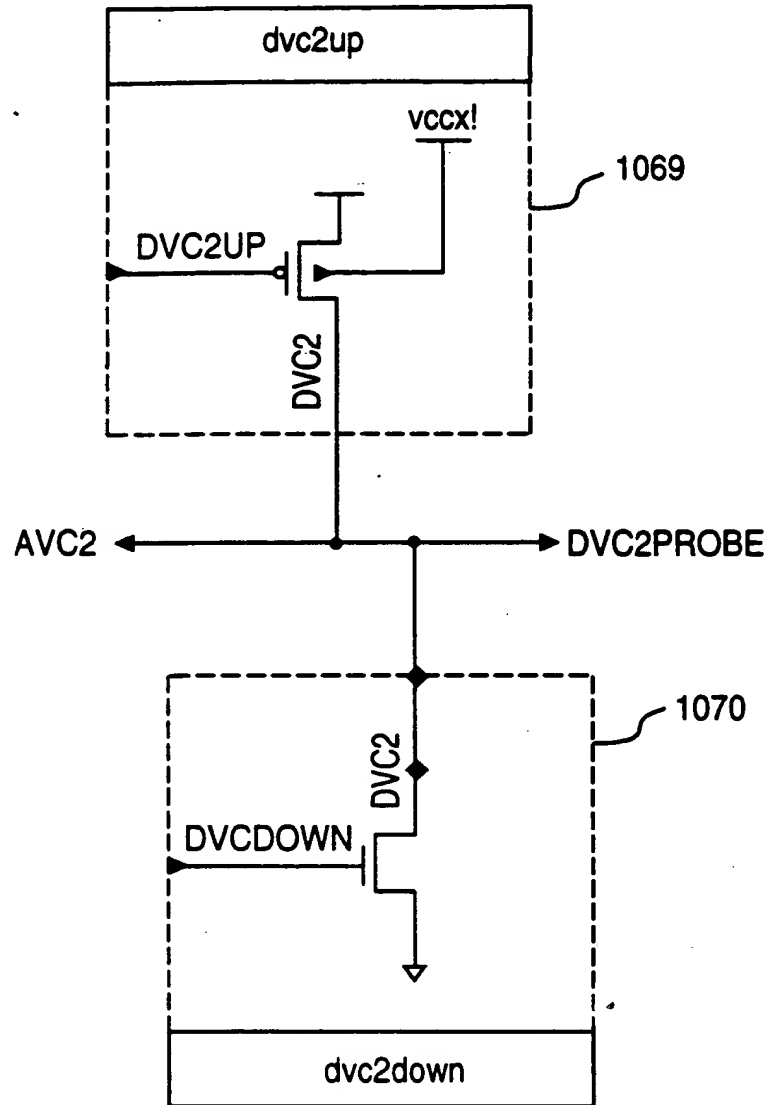
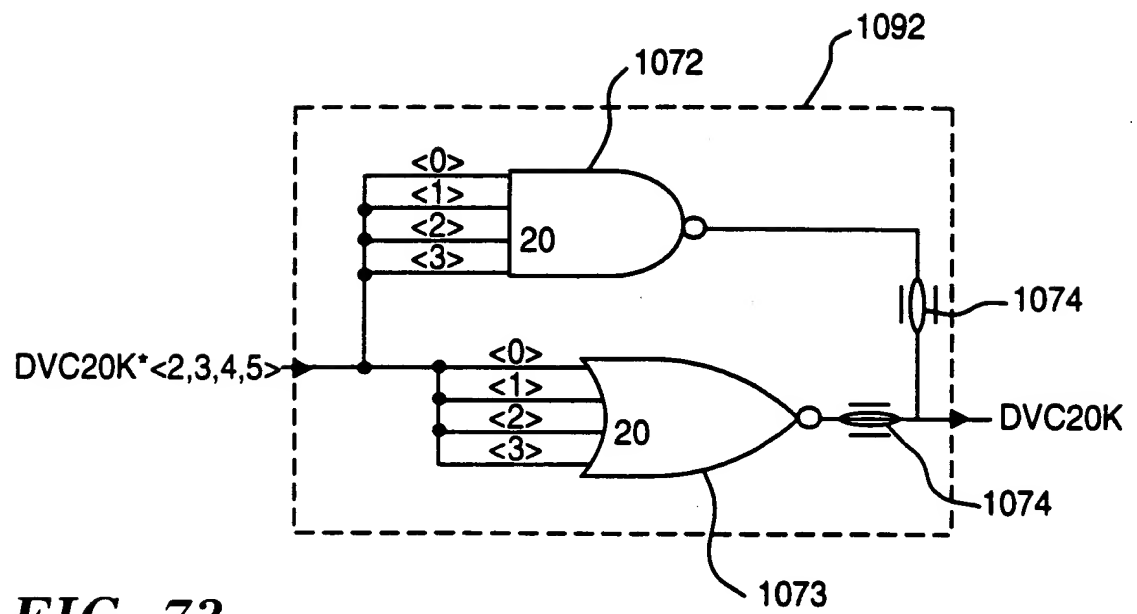


FIG. 72A

**FIG. 72B**

**FIG. 73**

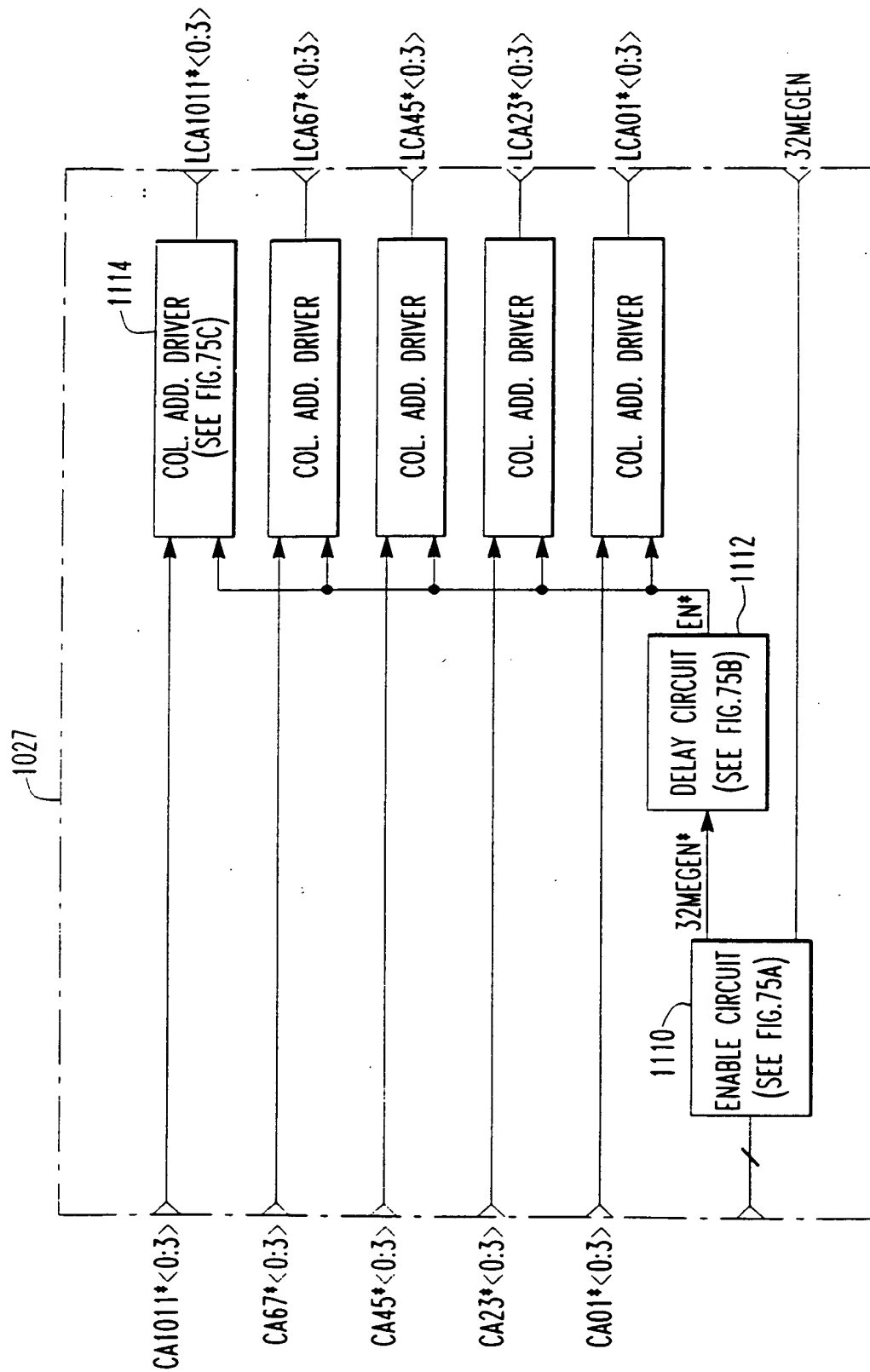


FIG. 74

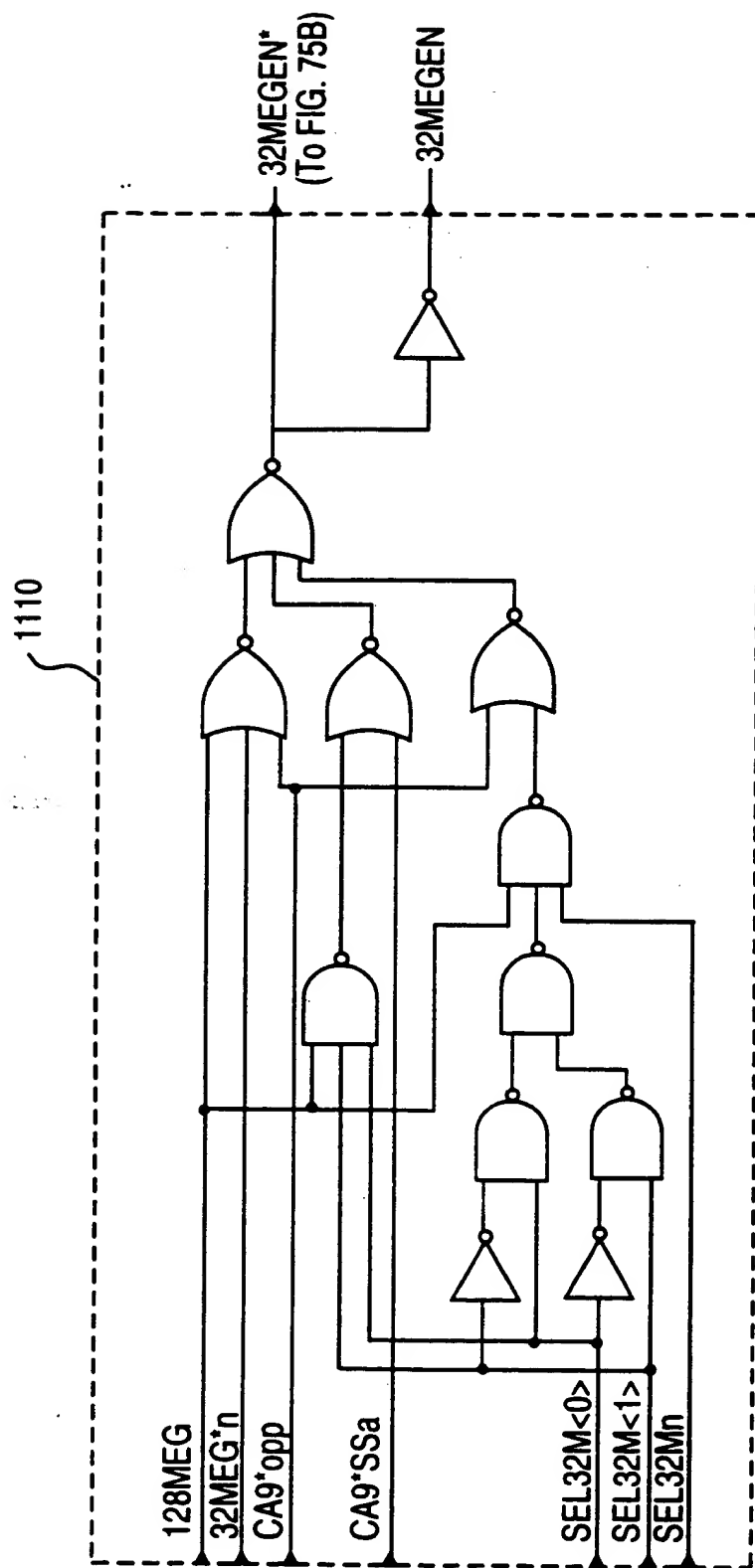
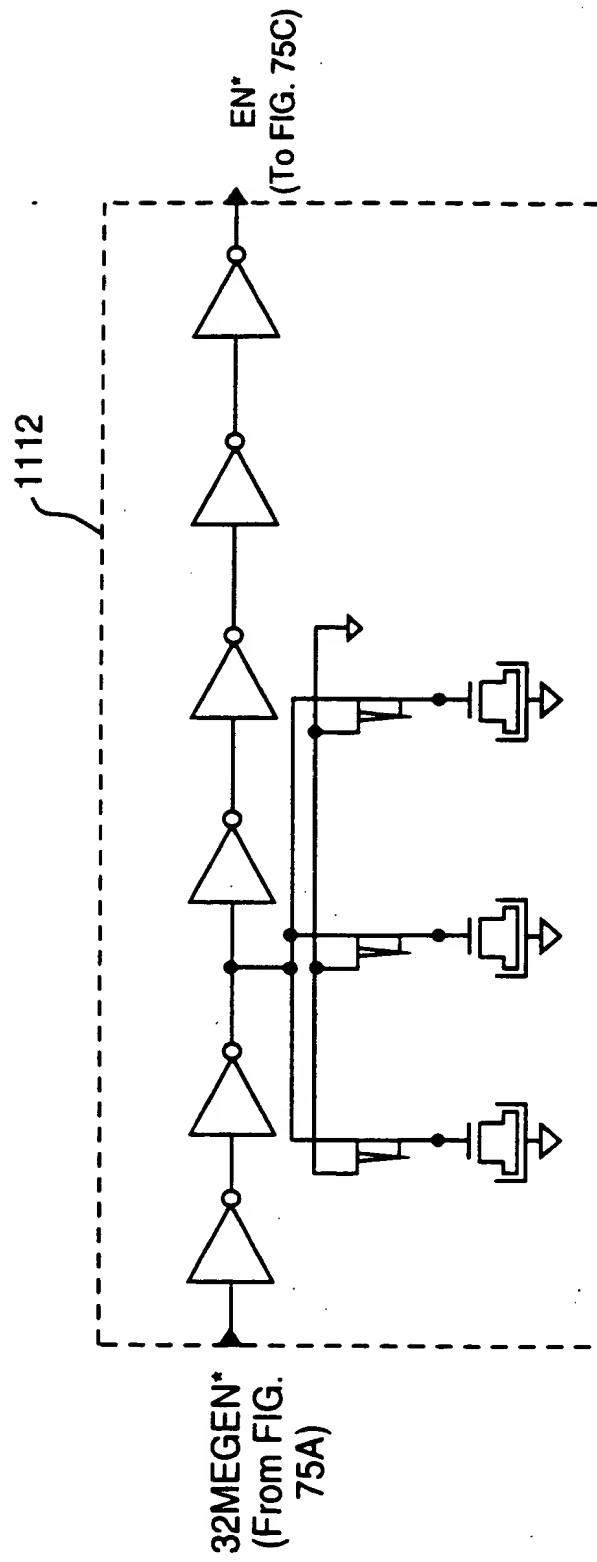


FIG. 75A

**FIG. 75B**

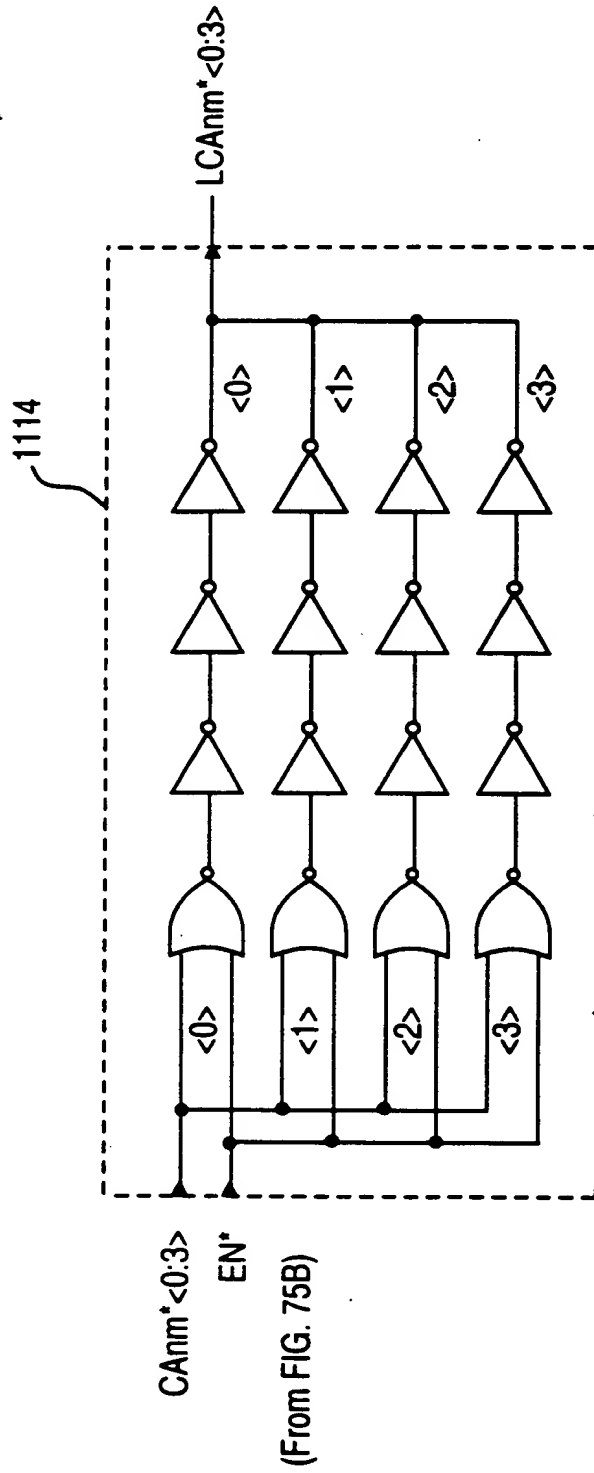


FIG. 75C

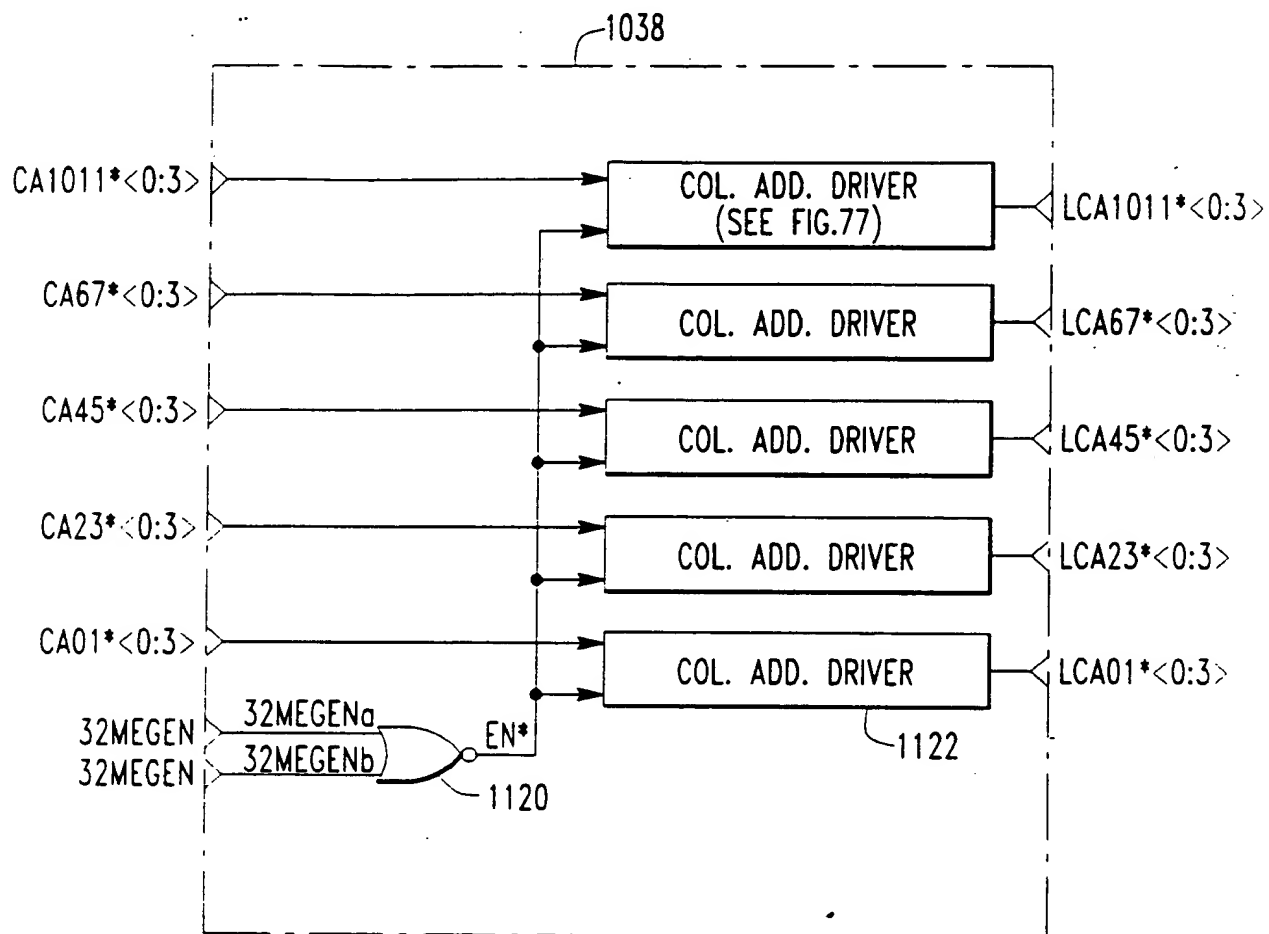


FIG. 76

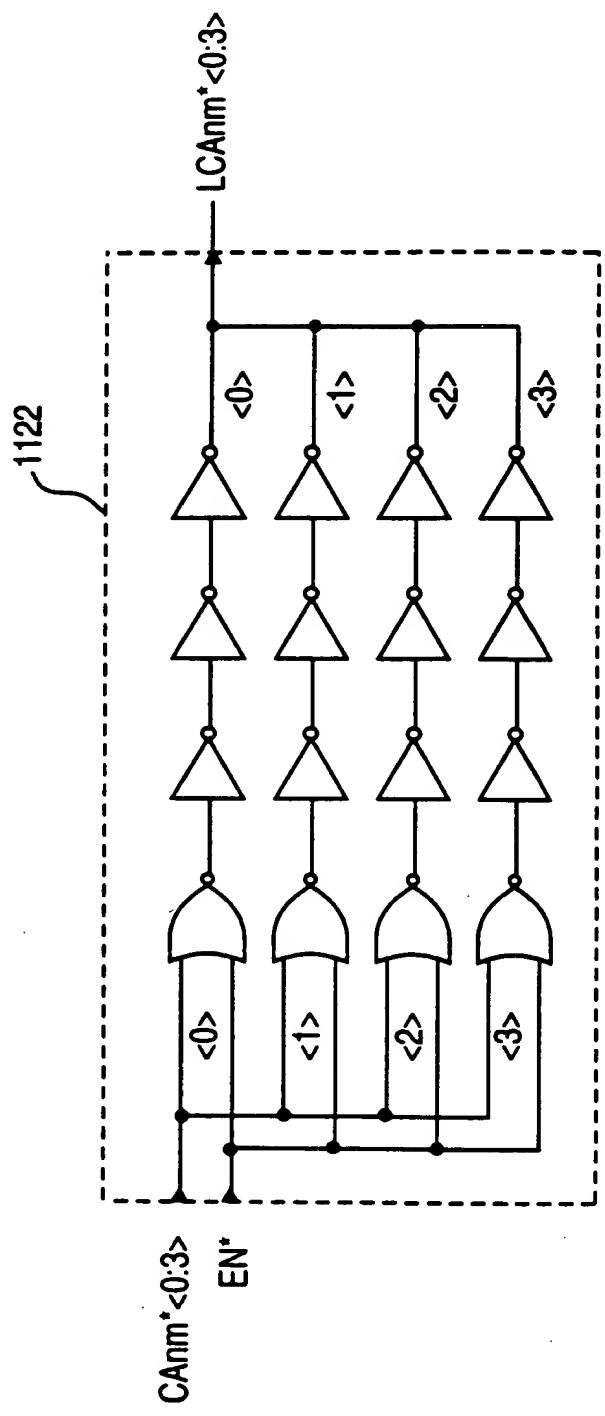


FIG. 77

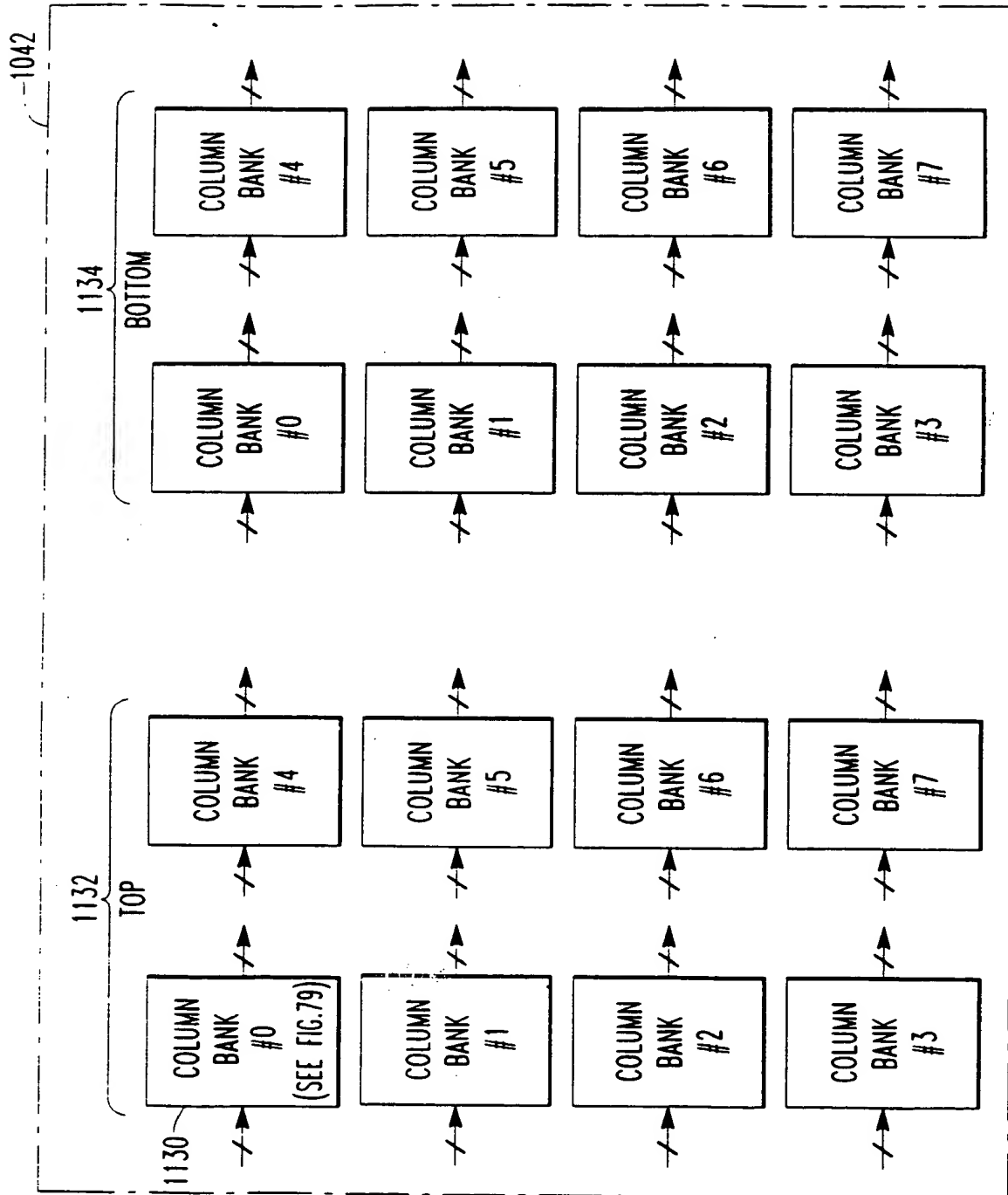
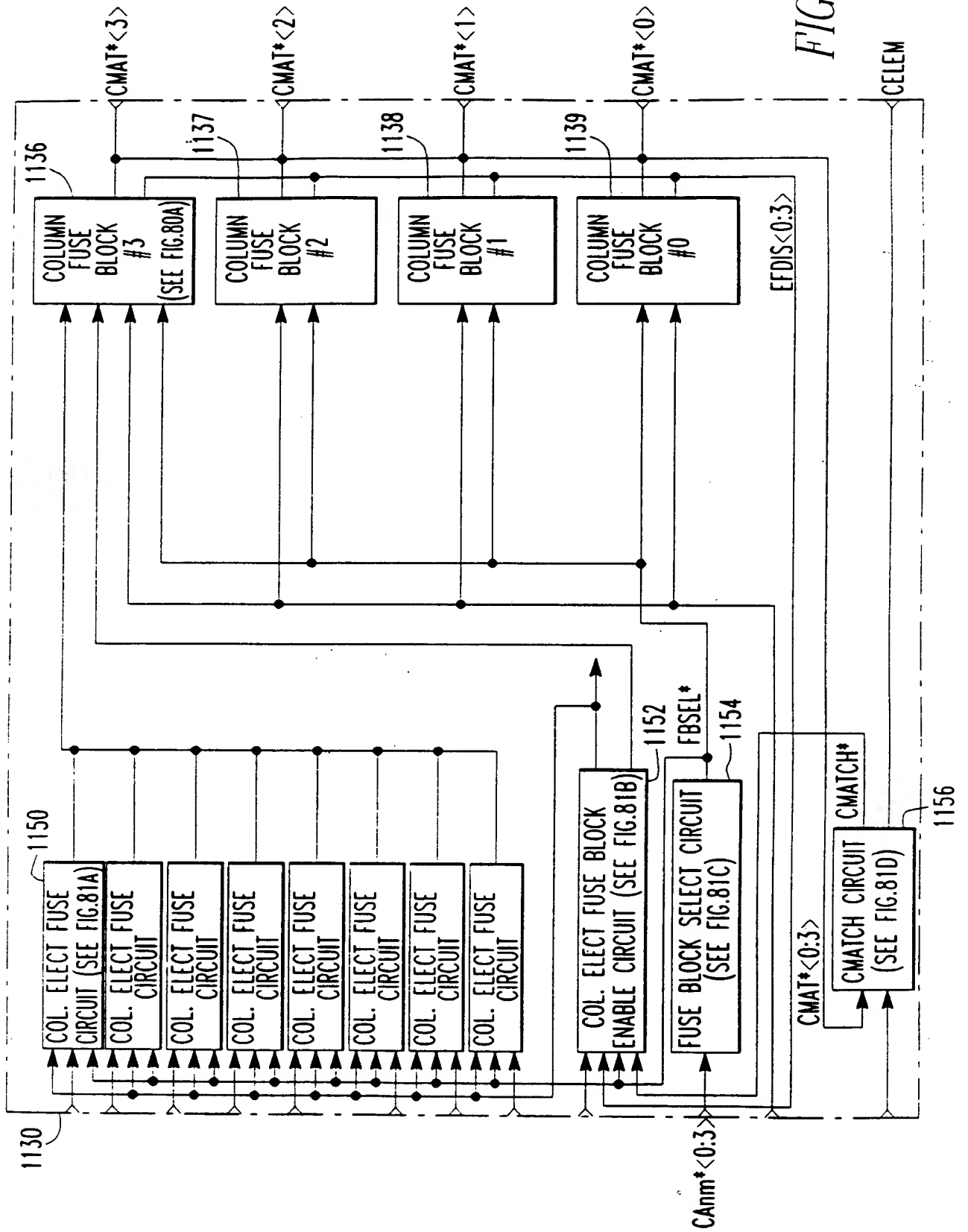


FIG. 78



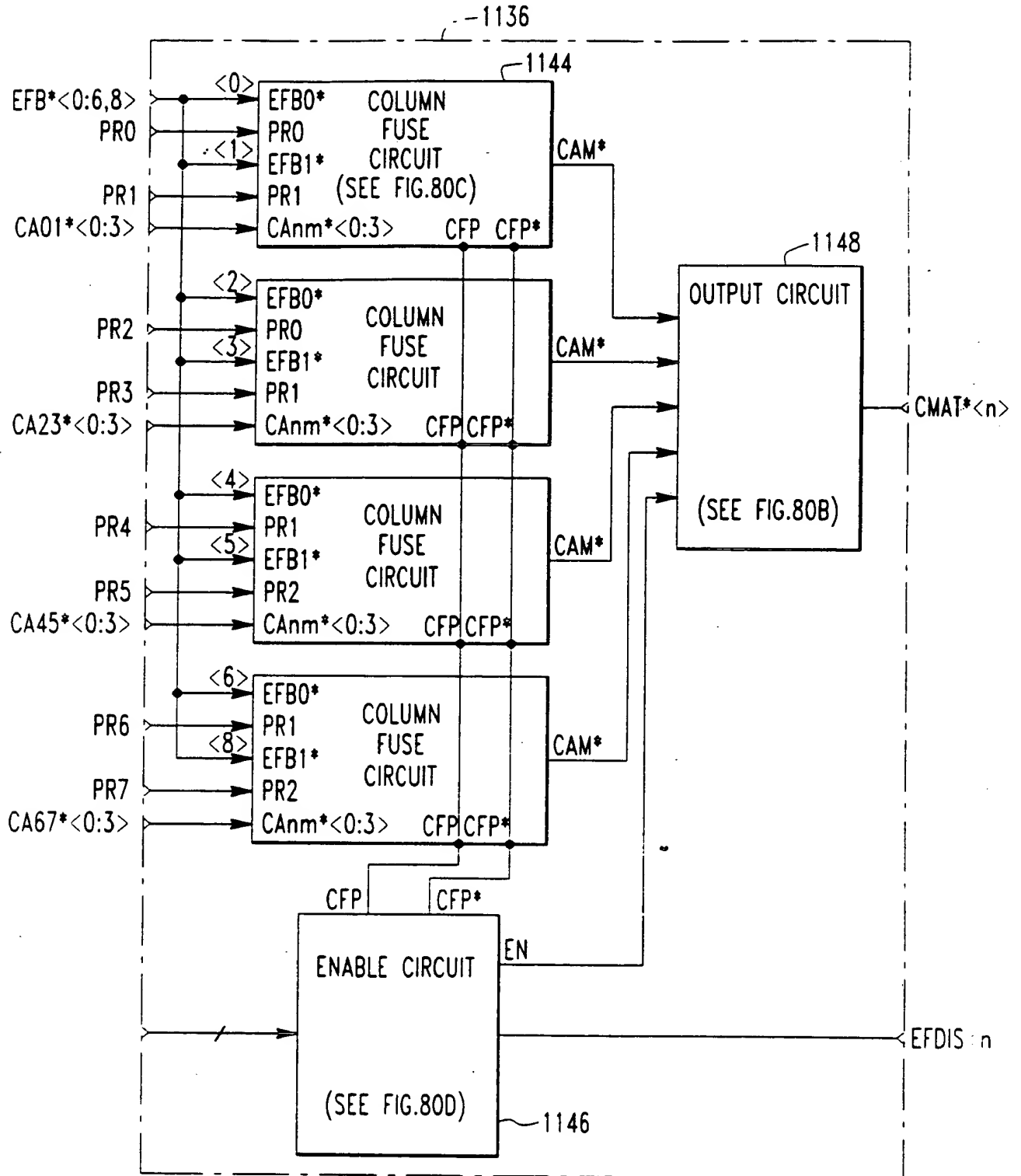
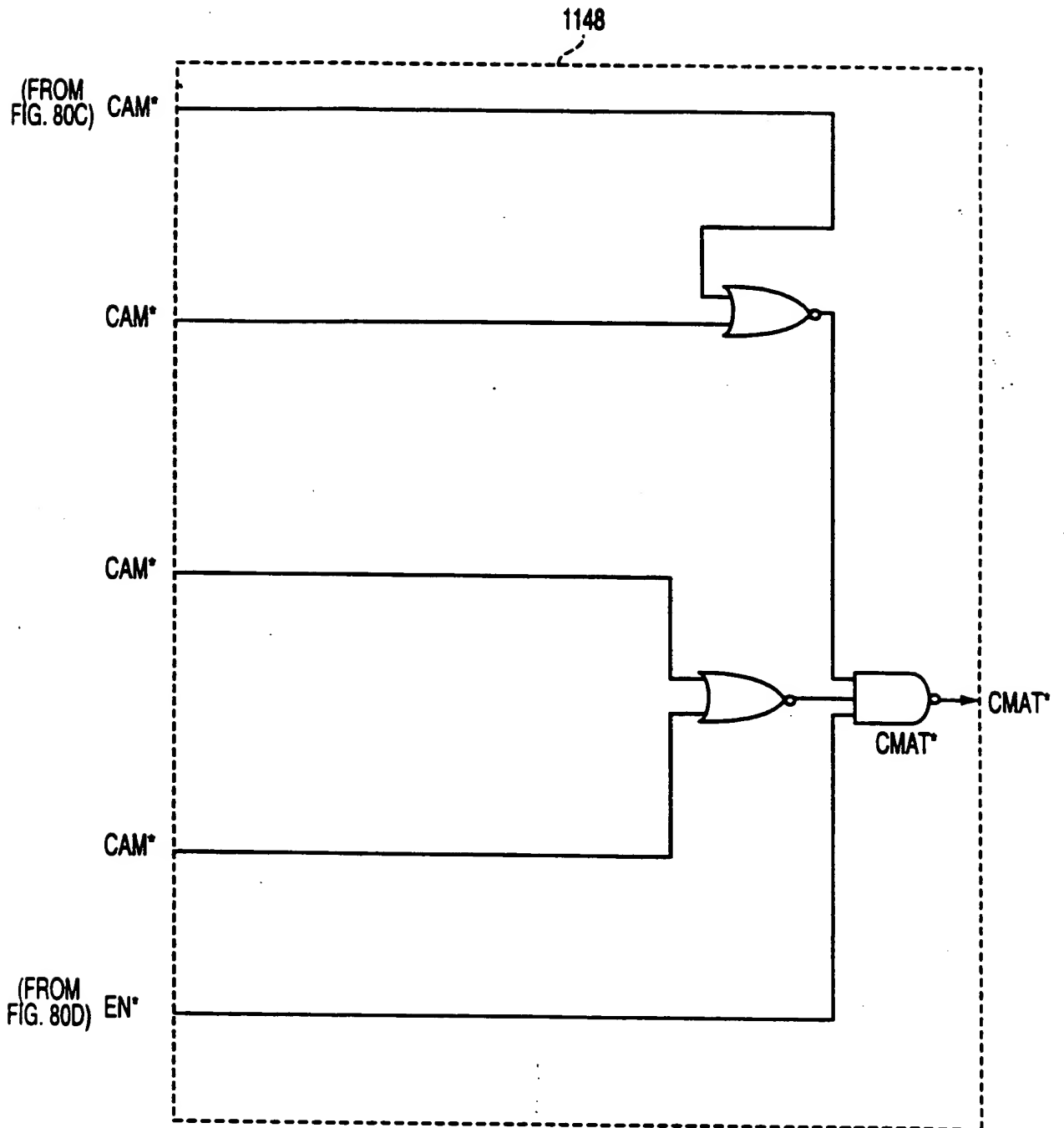


FIG. 80A

**FIG. 80B**

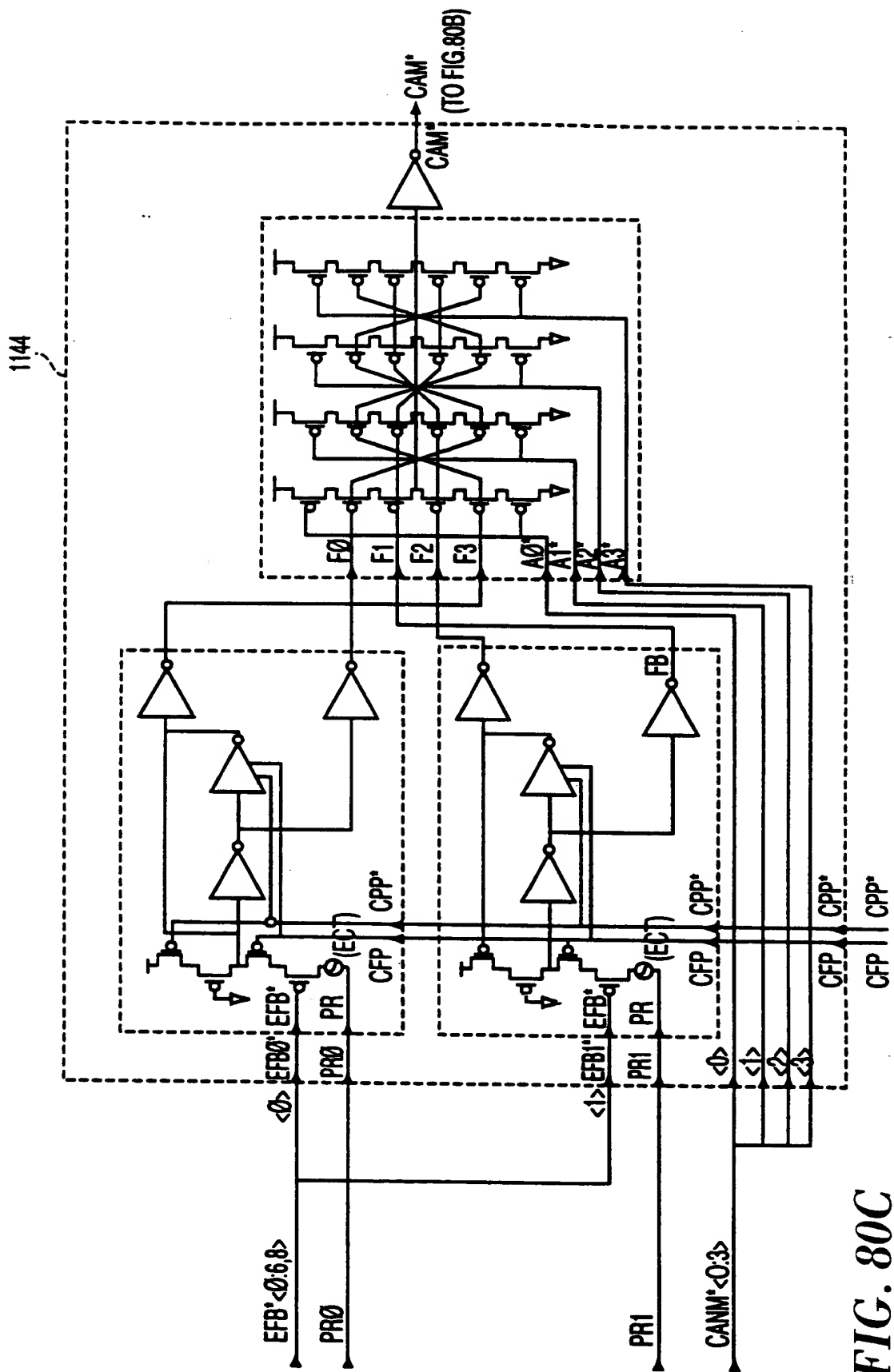


FIG. 80C

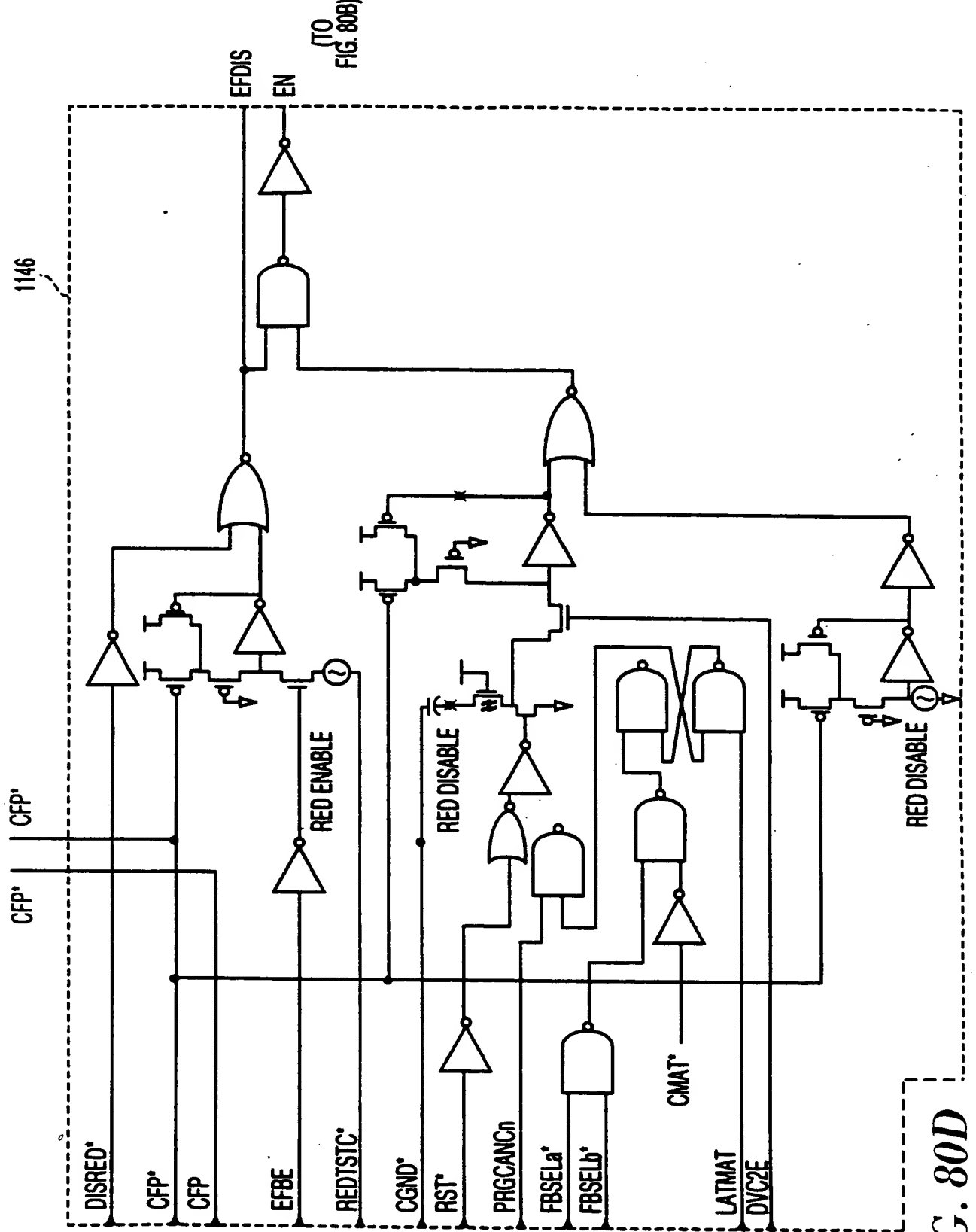


FIG. 80D

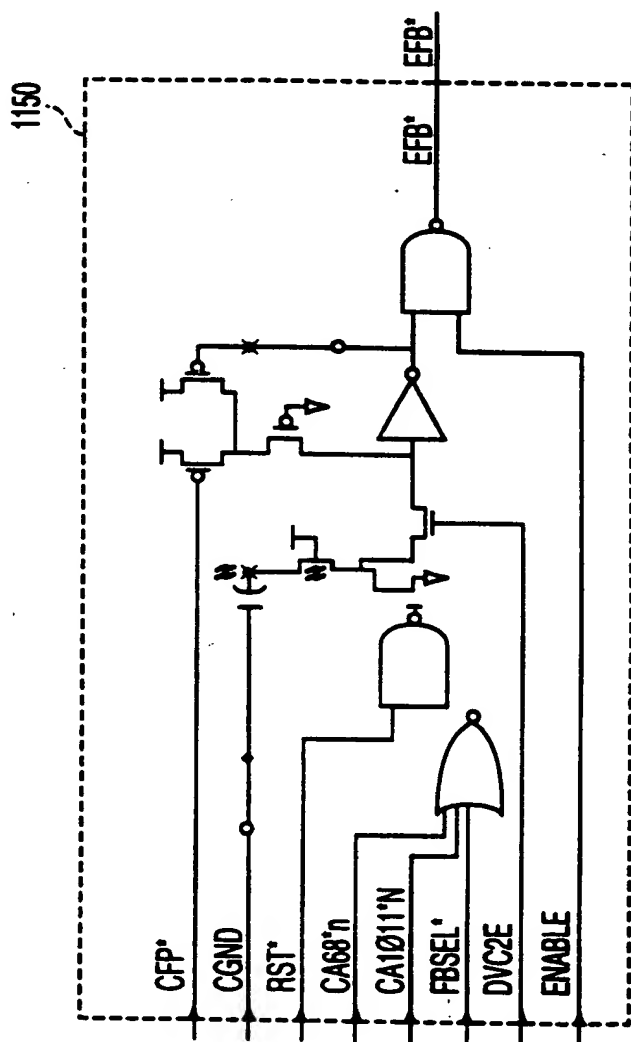


FIG. 81A

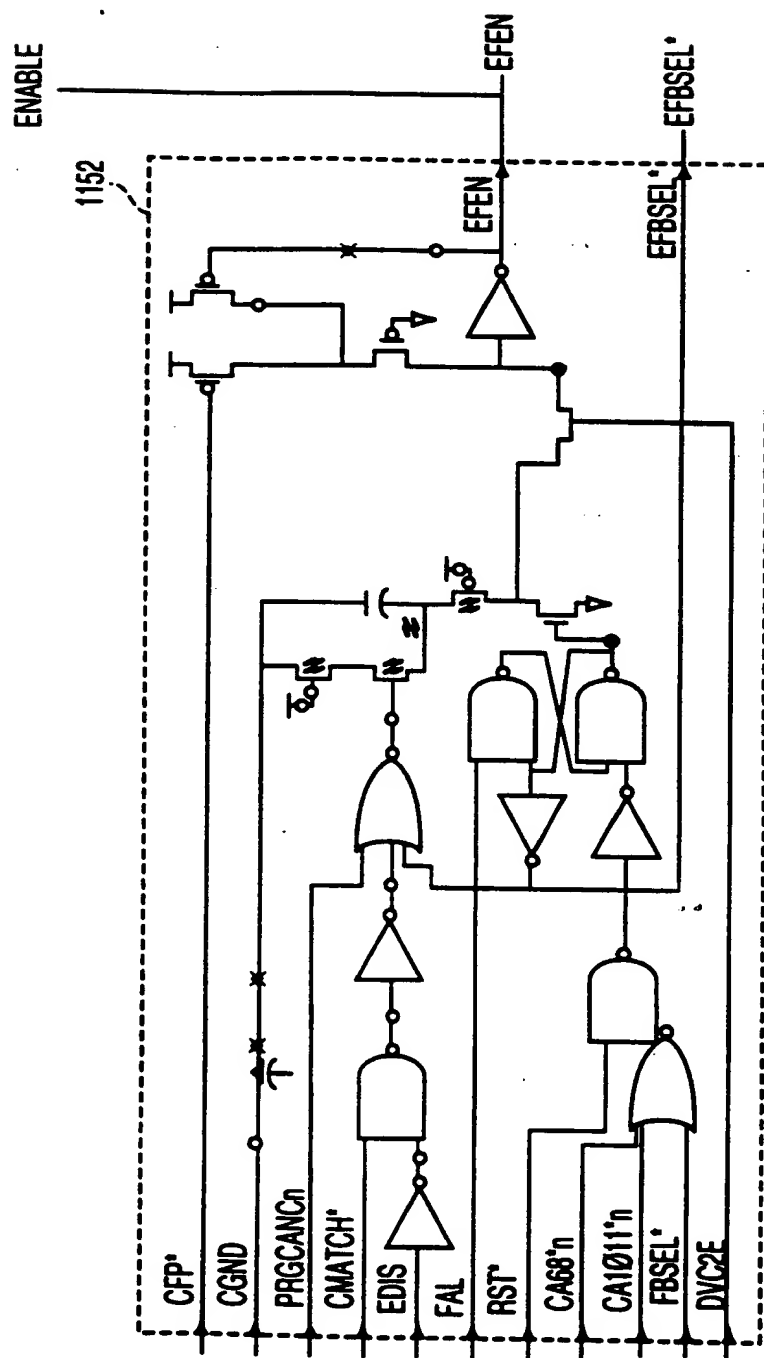


FIG. 81B

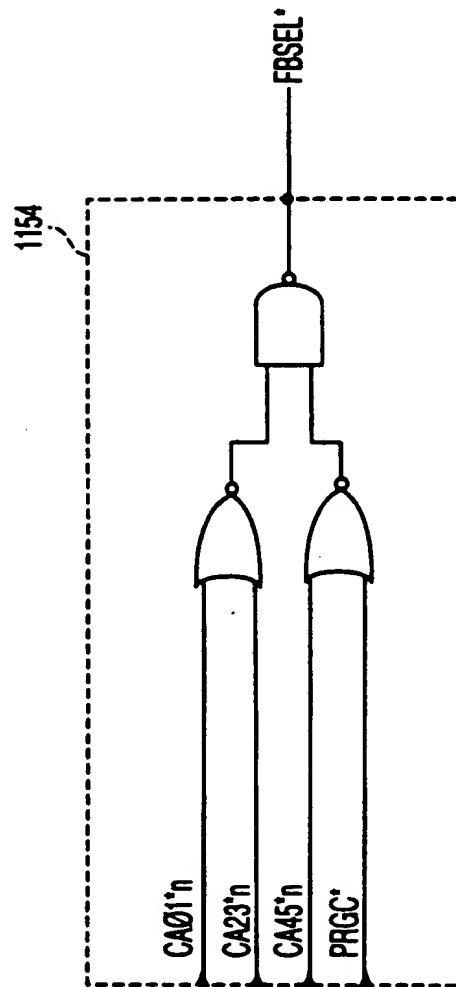


FIG. 81C

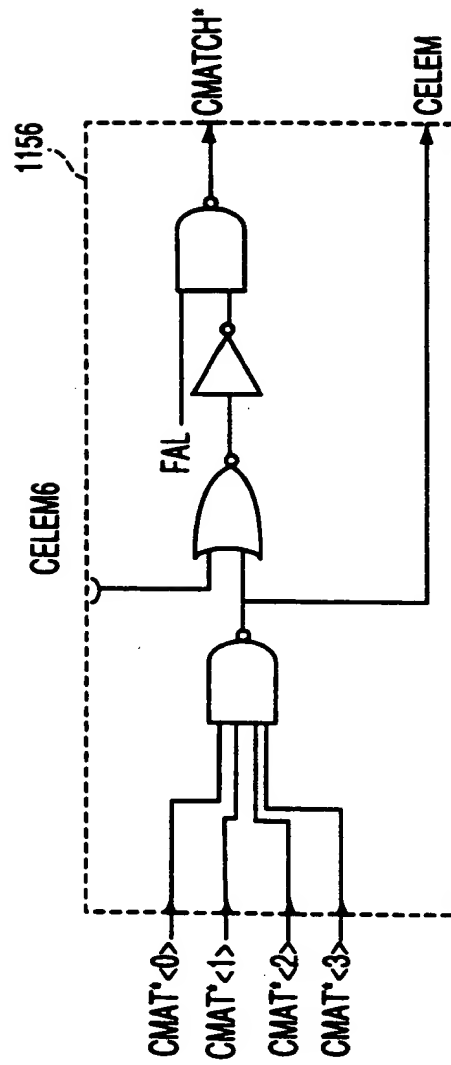


FIG. 81D

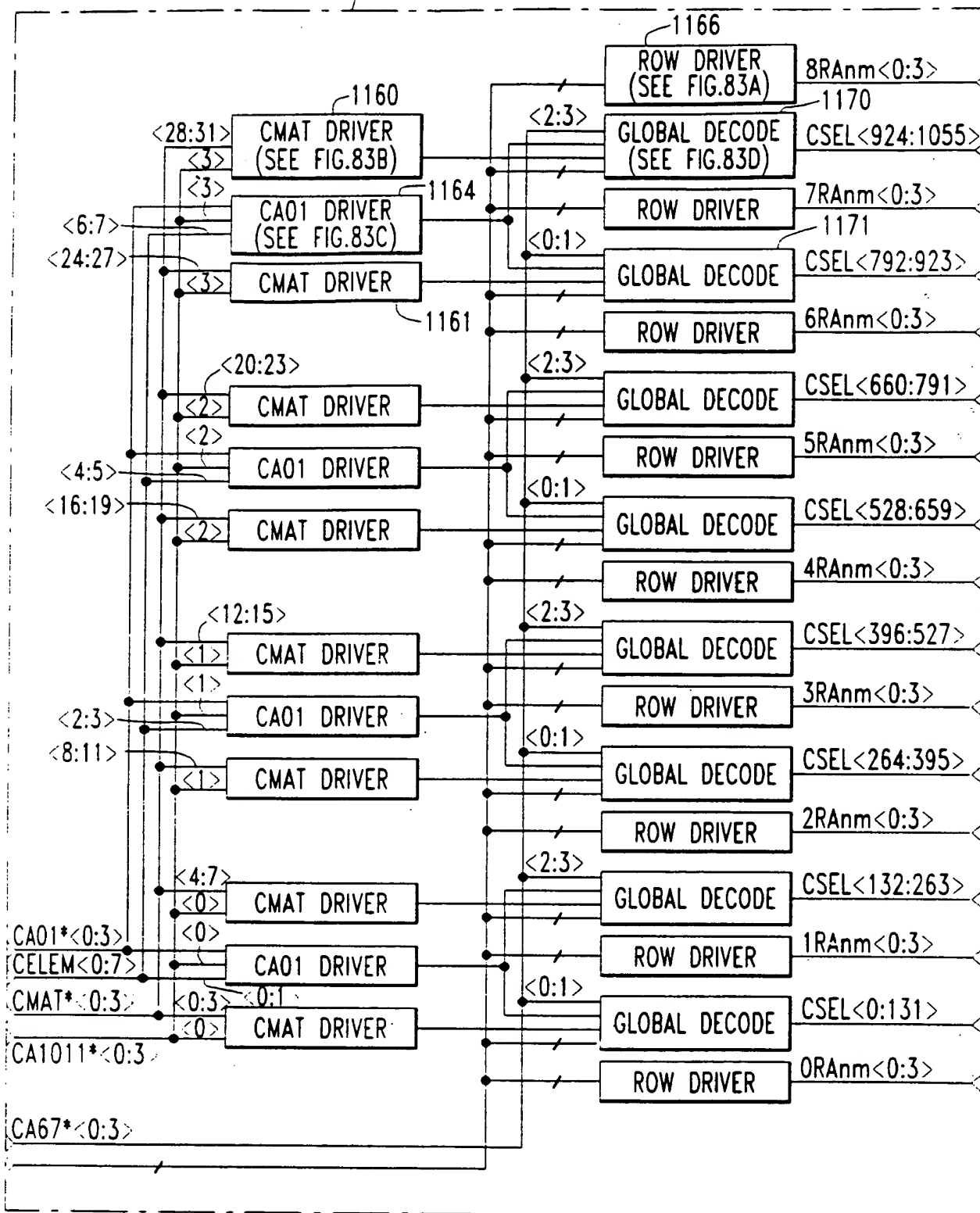


FIG. 82

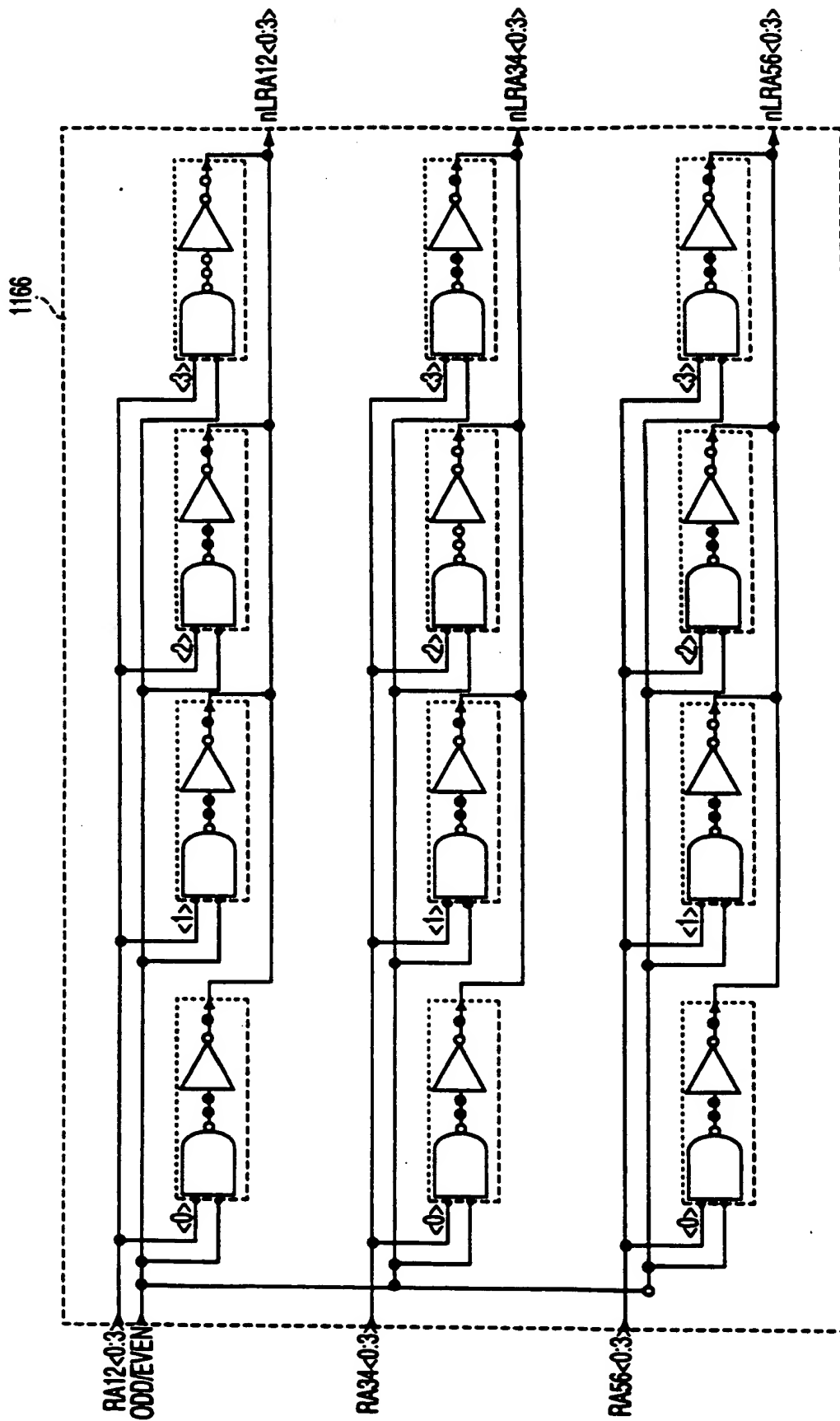


FIG. 83A

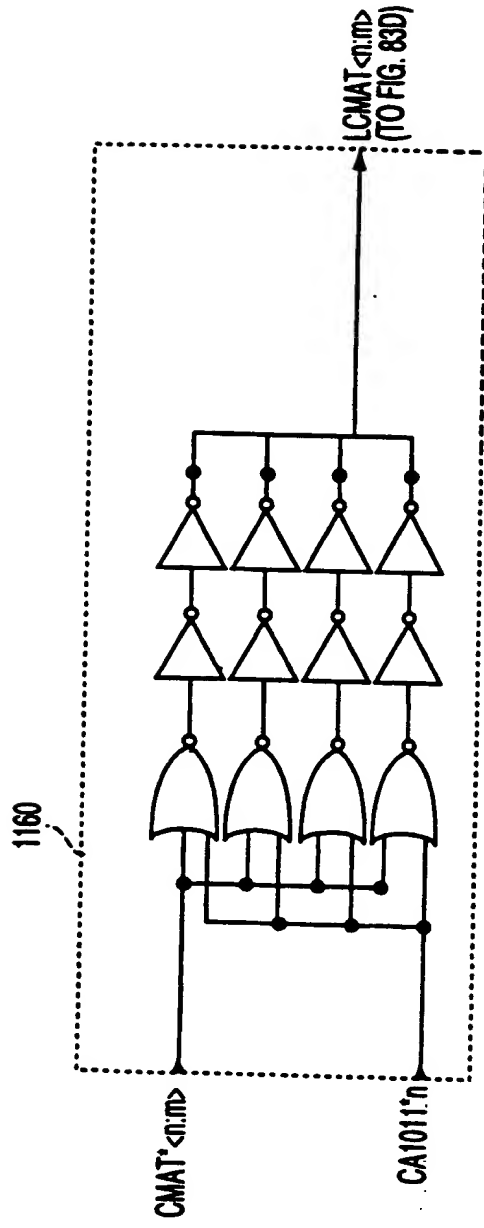


FIG. 83B

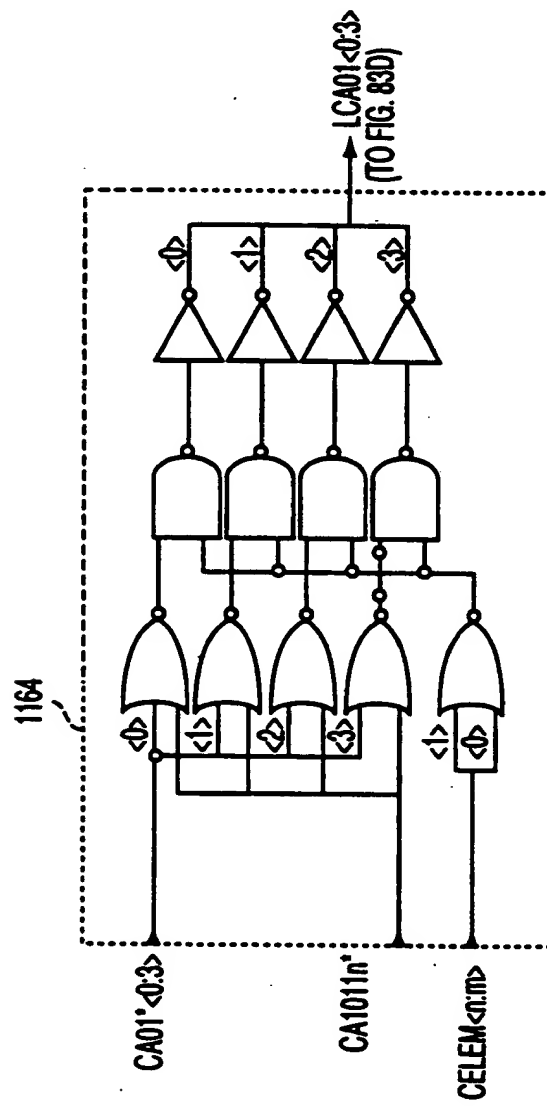


FIG. 83C

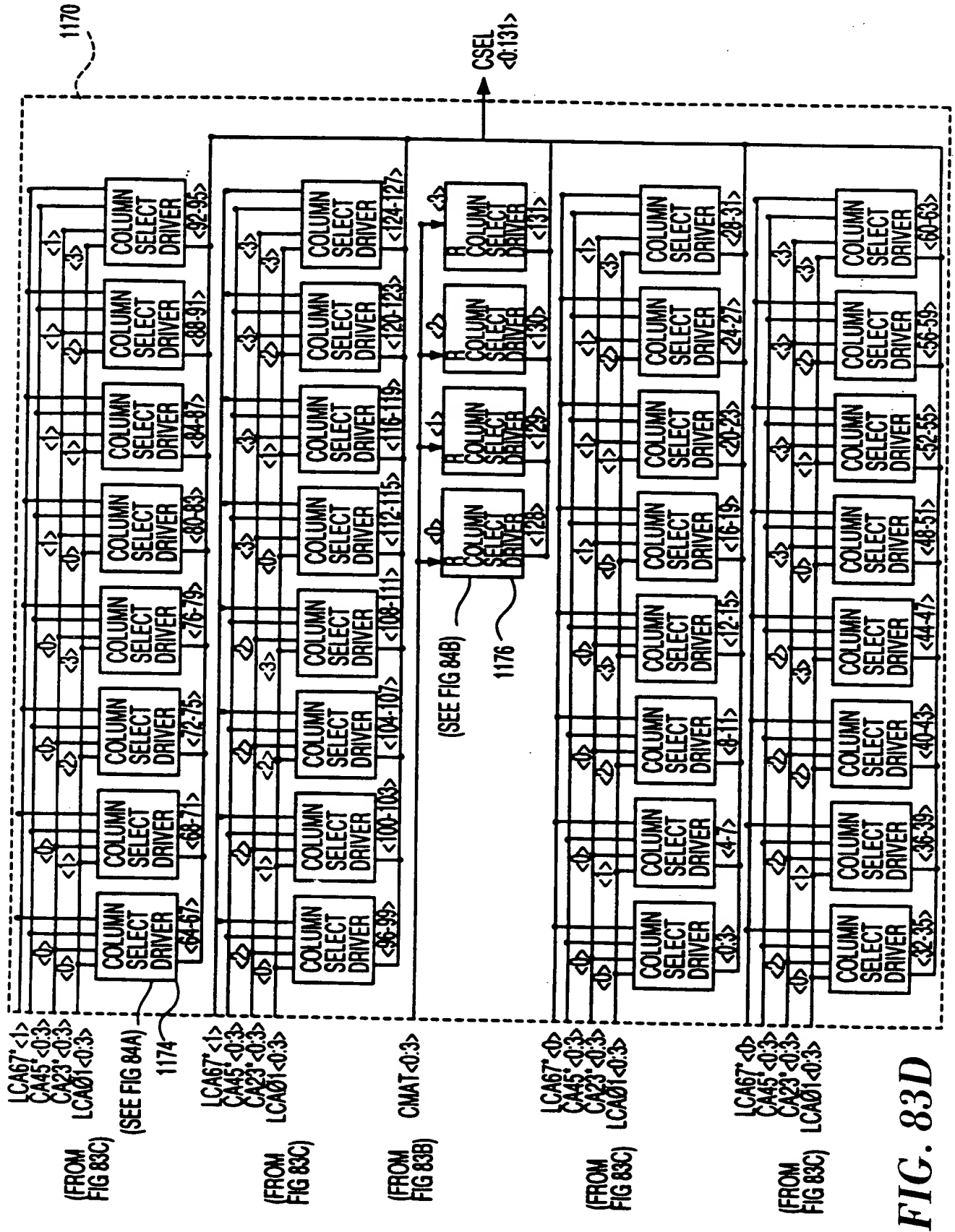


FIG. 83D

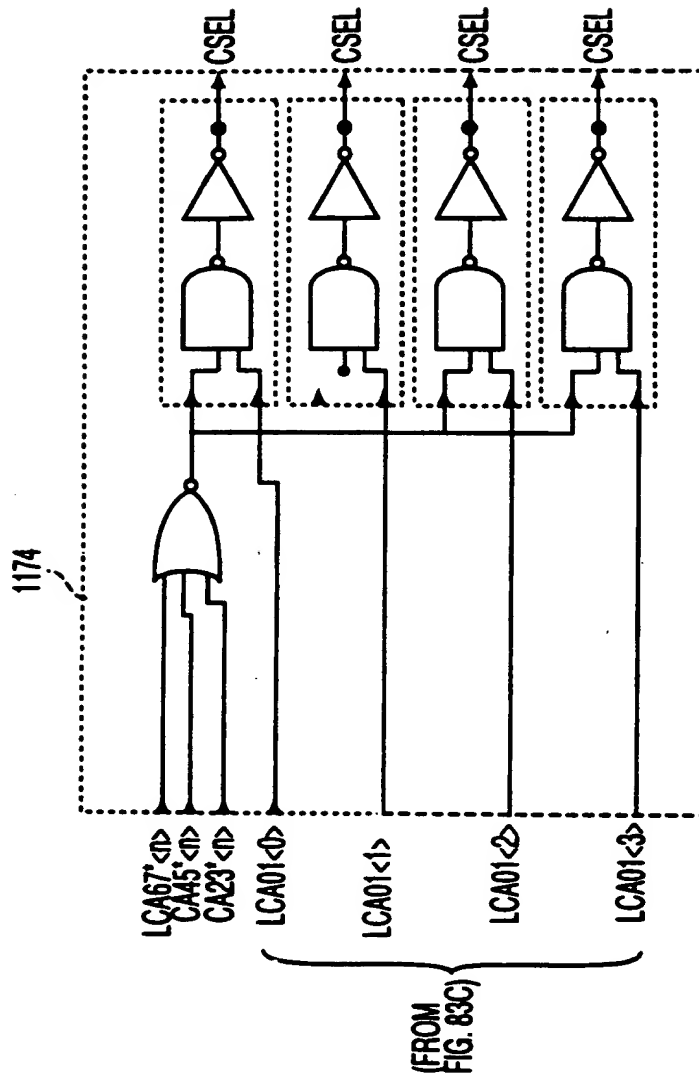
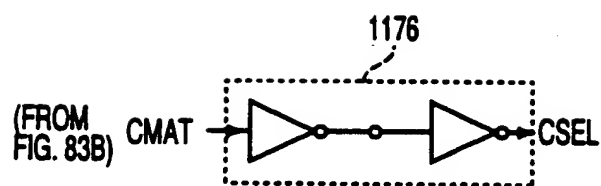


FIG. 84A

**FIG. 84B**

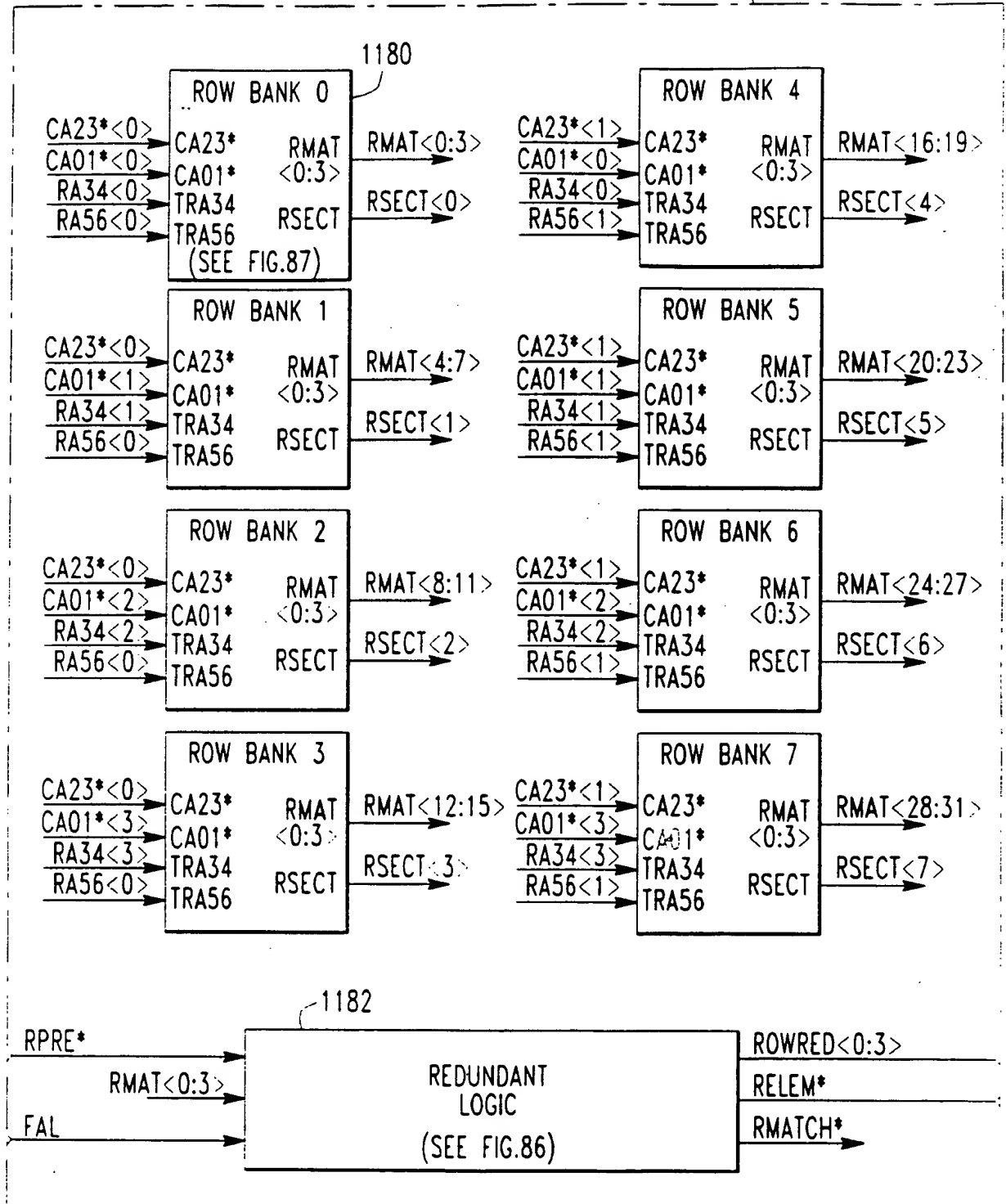


FIG. 85

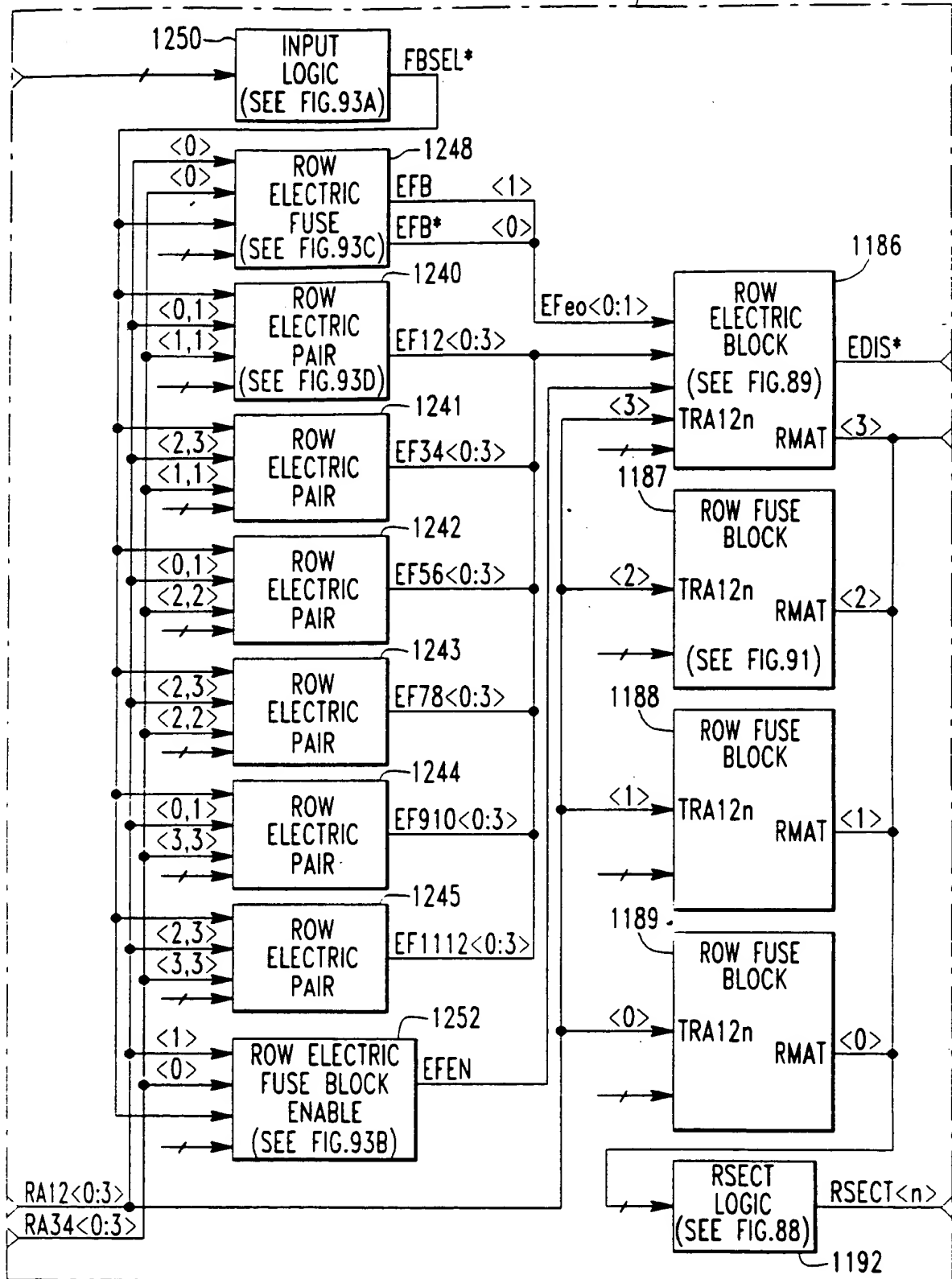
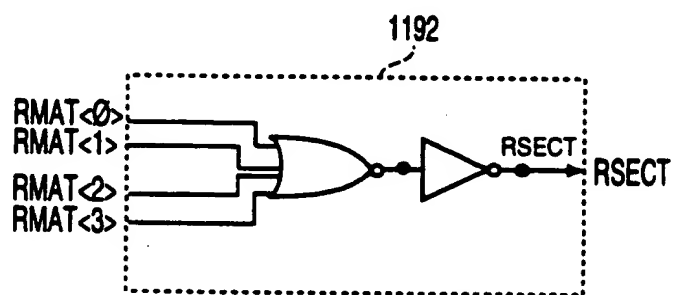


FIG. 87

**FIG. 88**

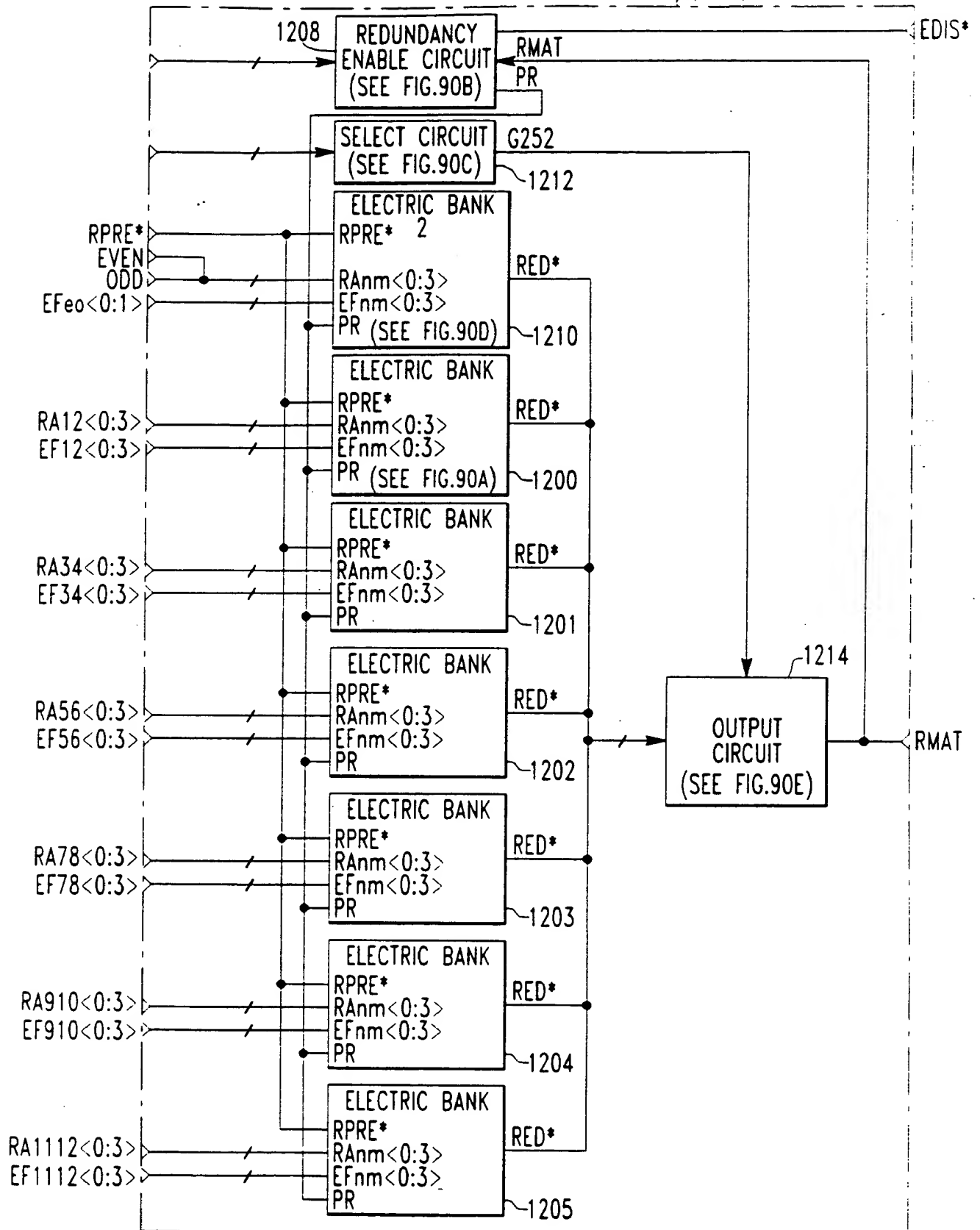


FIG. 89

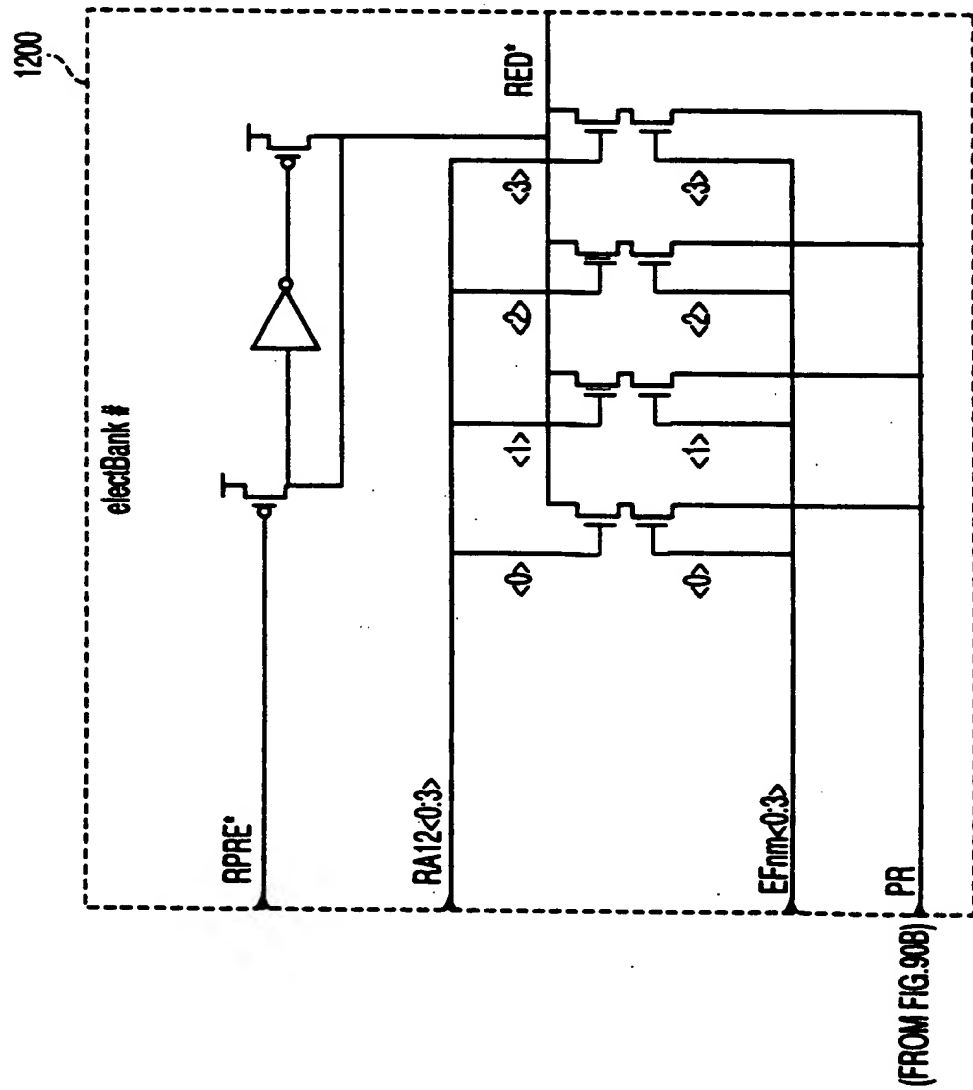


FIG. 90A

FIG. 90B

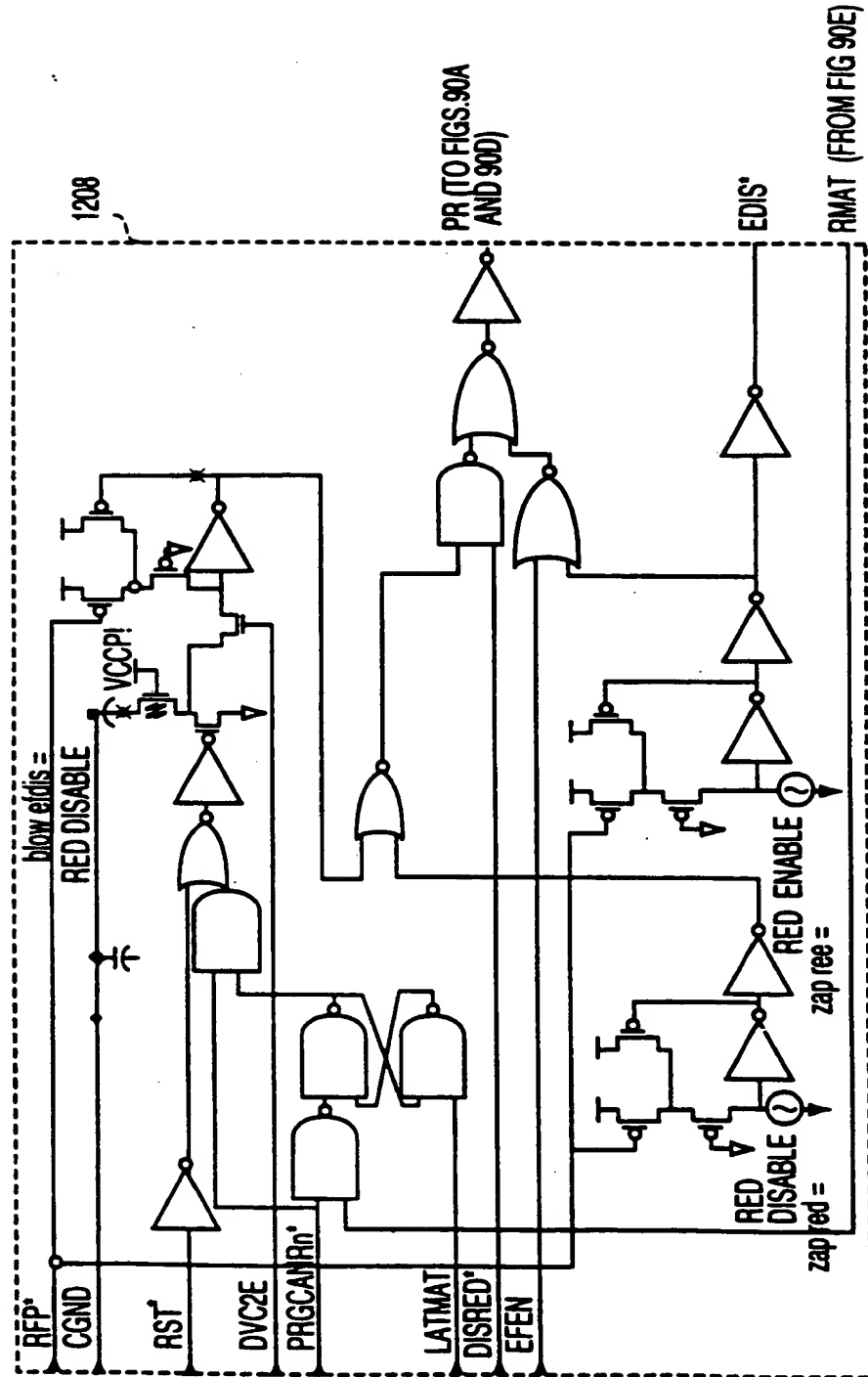


FIG. 90B

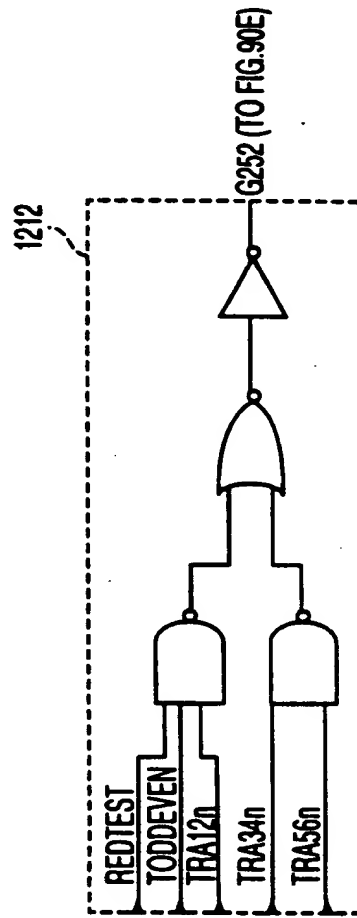


FIG. 90C

FIG. 90E

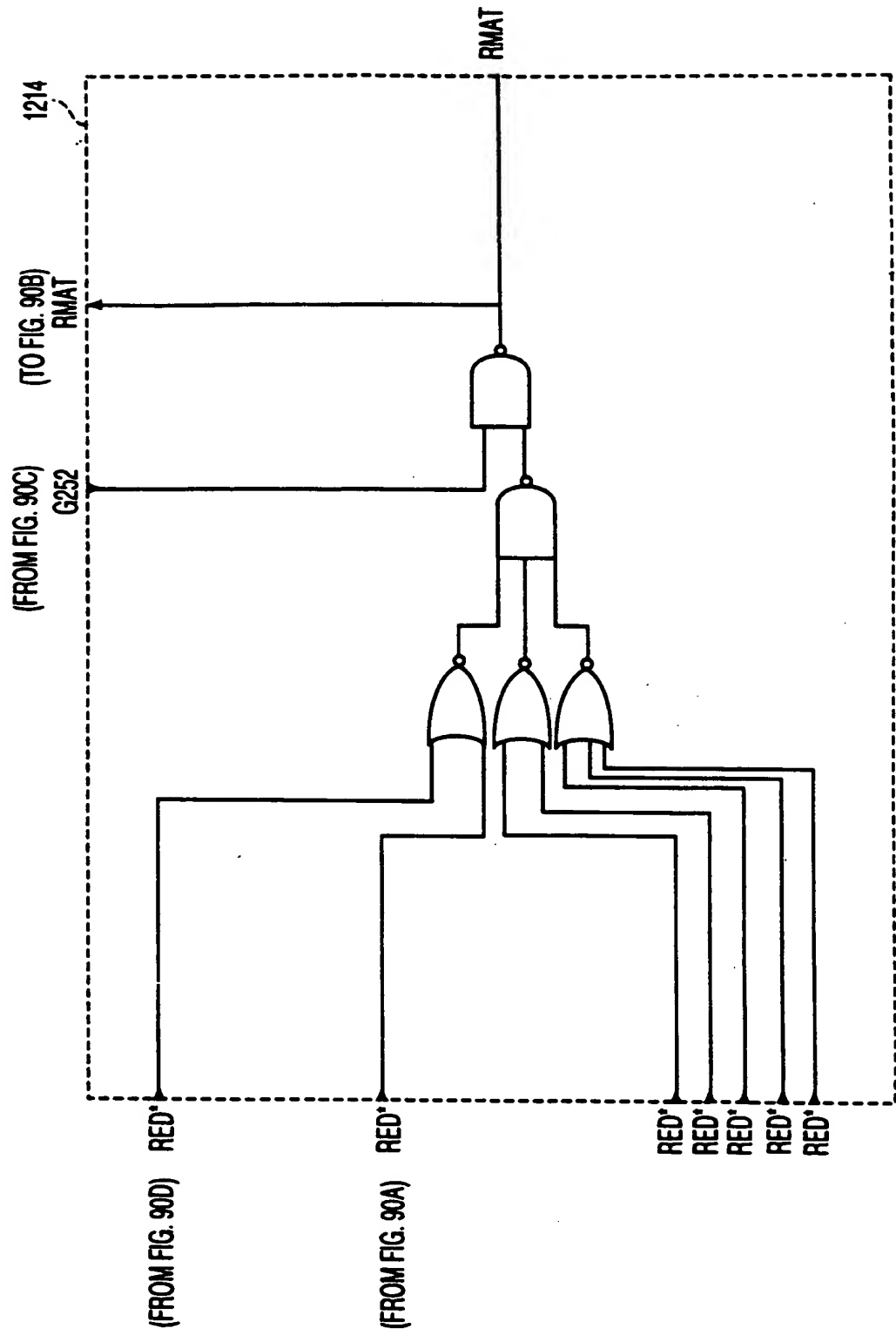


FIG. 90E

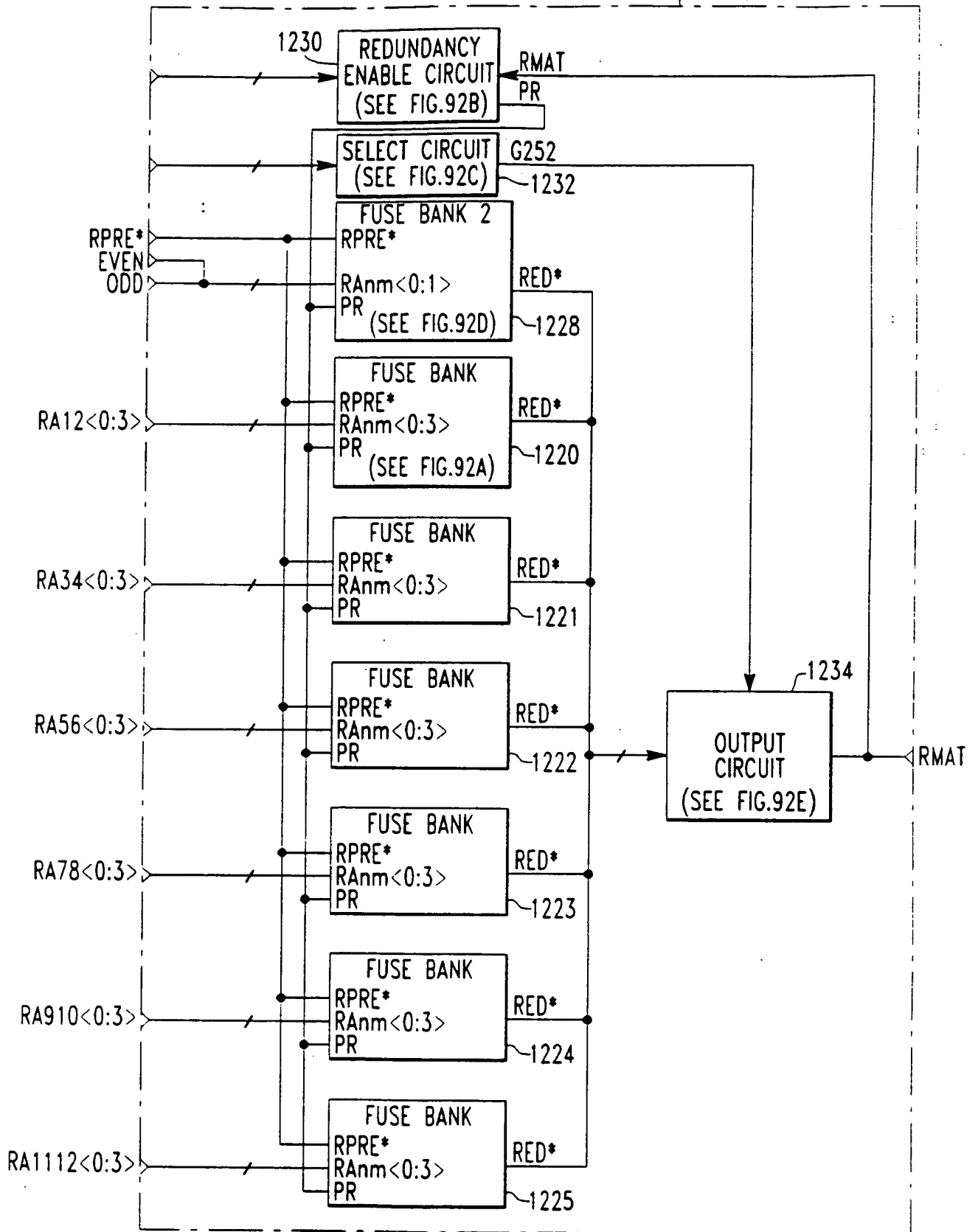


FIG. 91

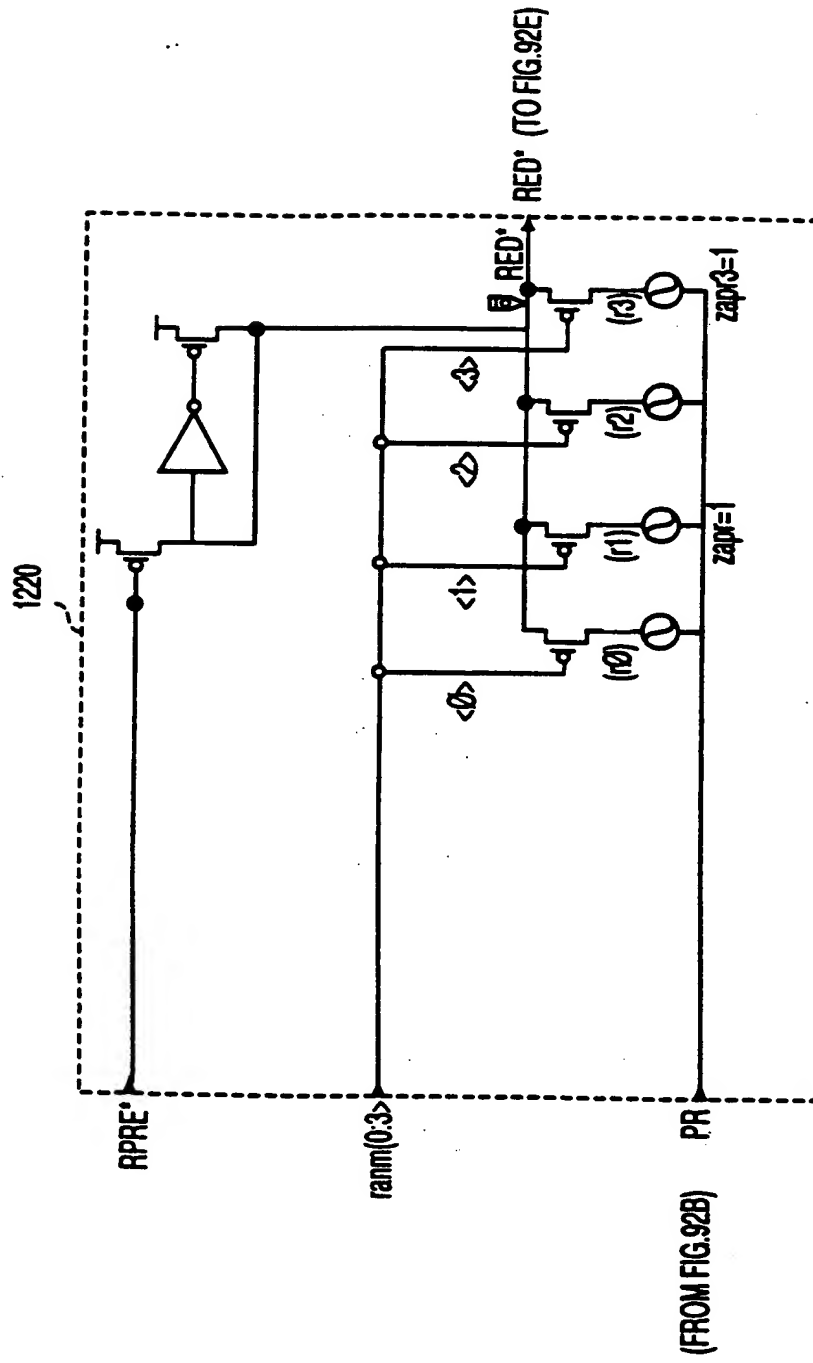


FIG. 92A

FIG. 92B

1230

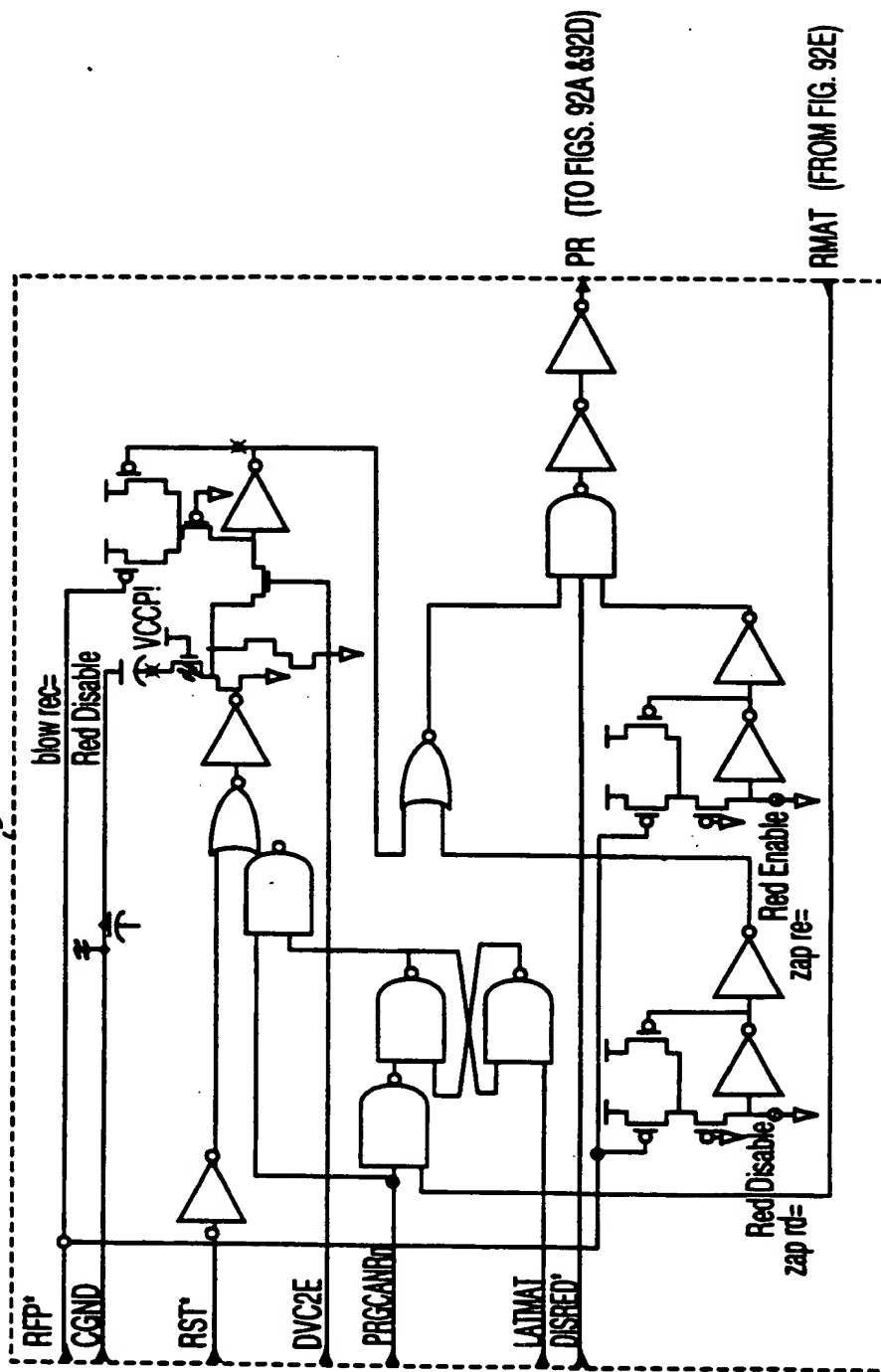


FIG. 92B

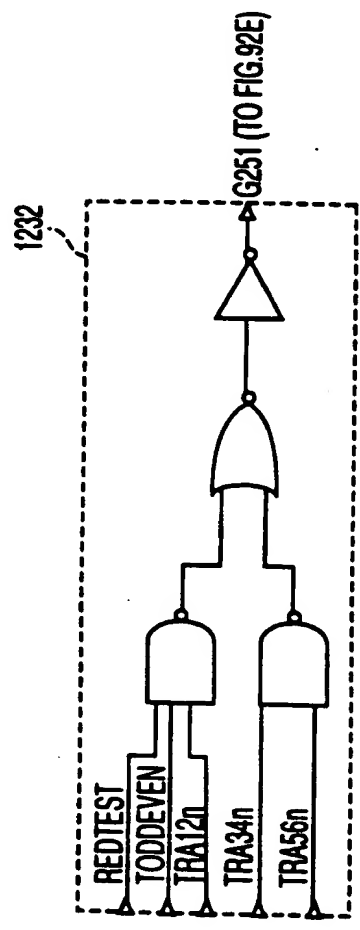


FIG. 92C

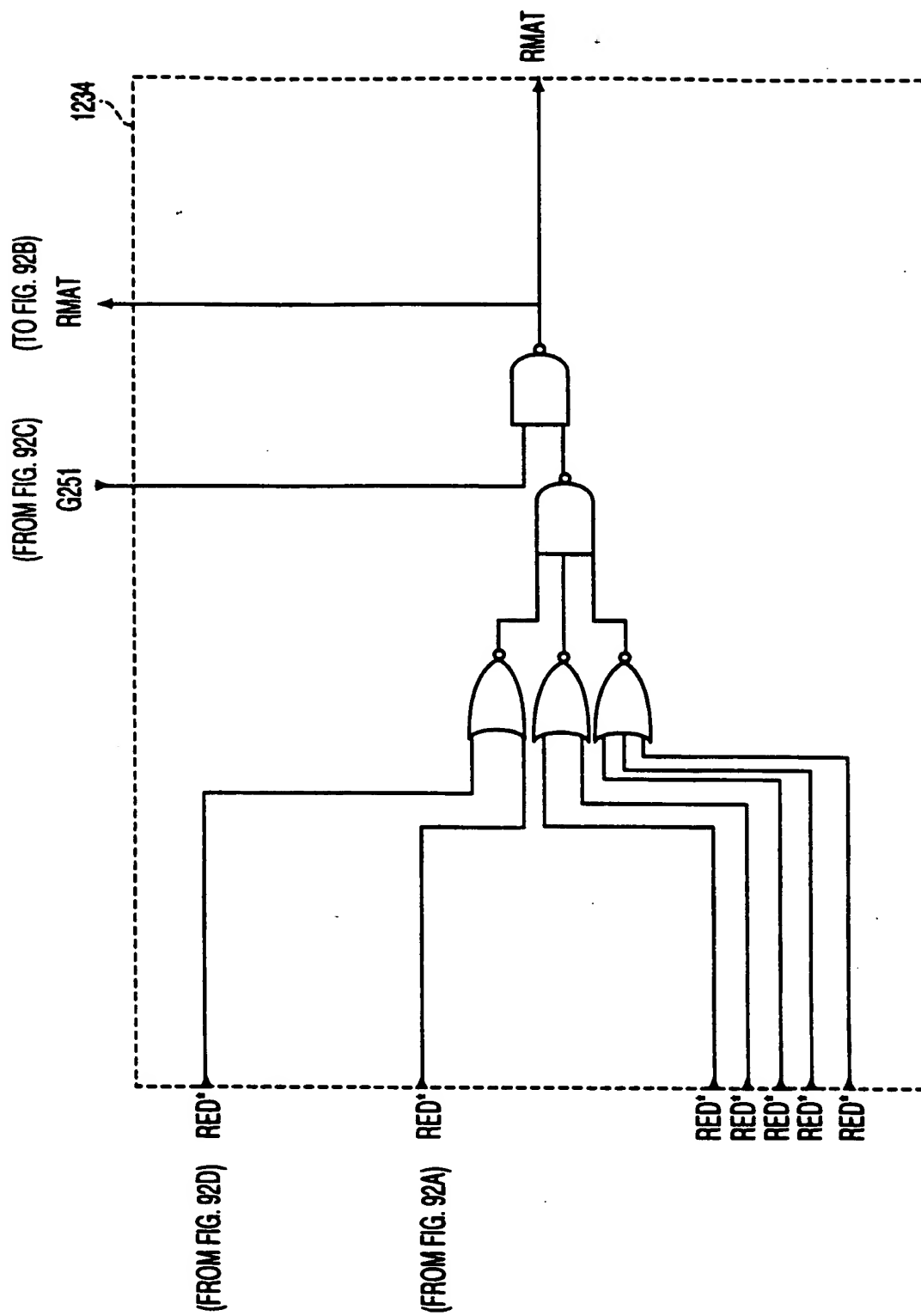


FIG. 92E

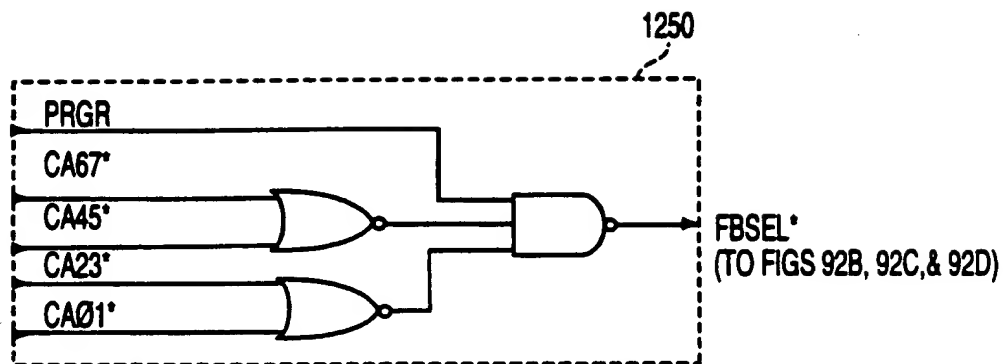


FIG. 93A

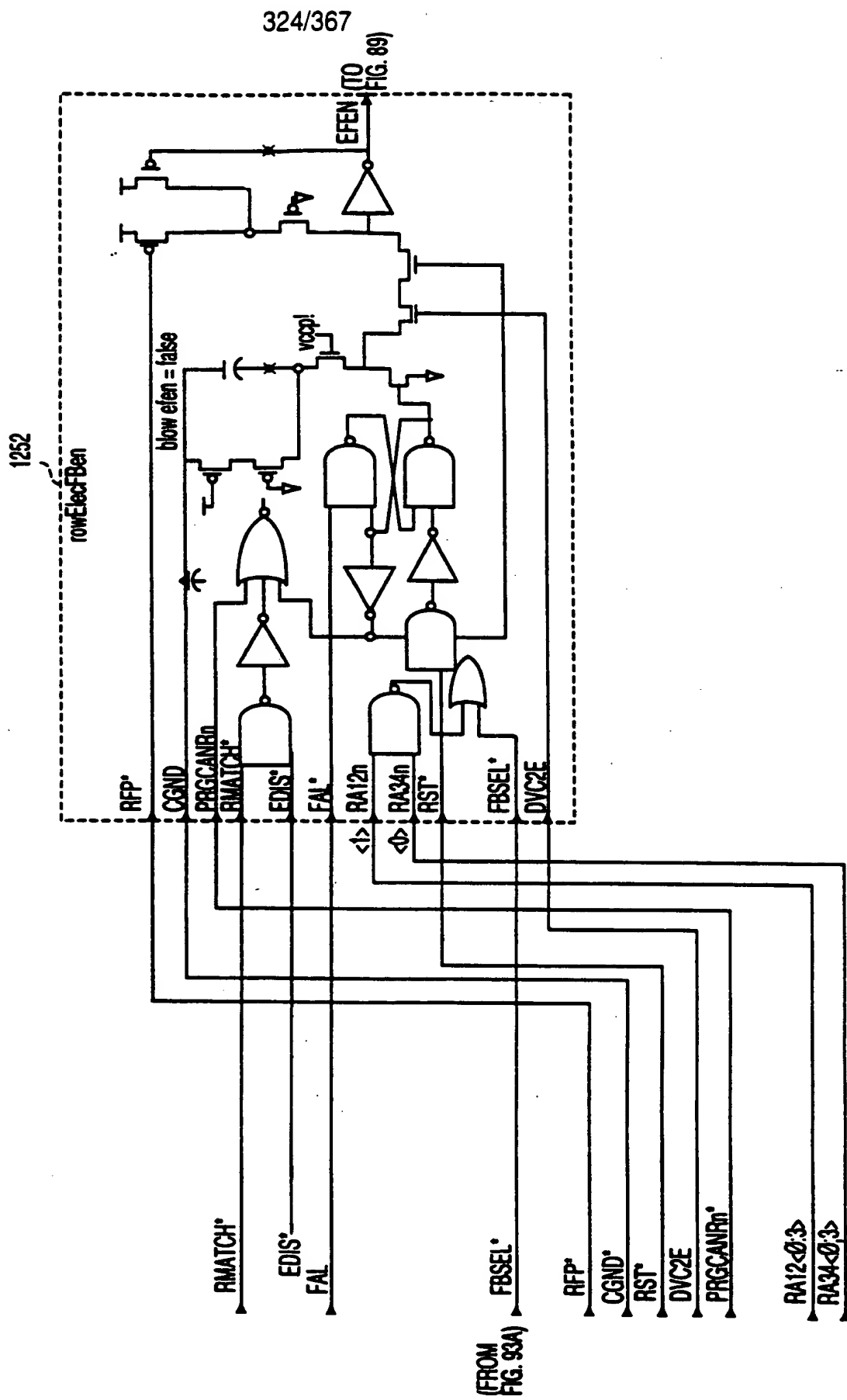


FIG. 93B

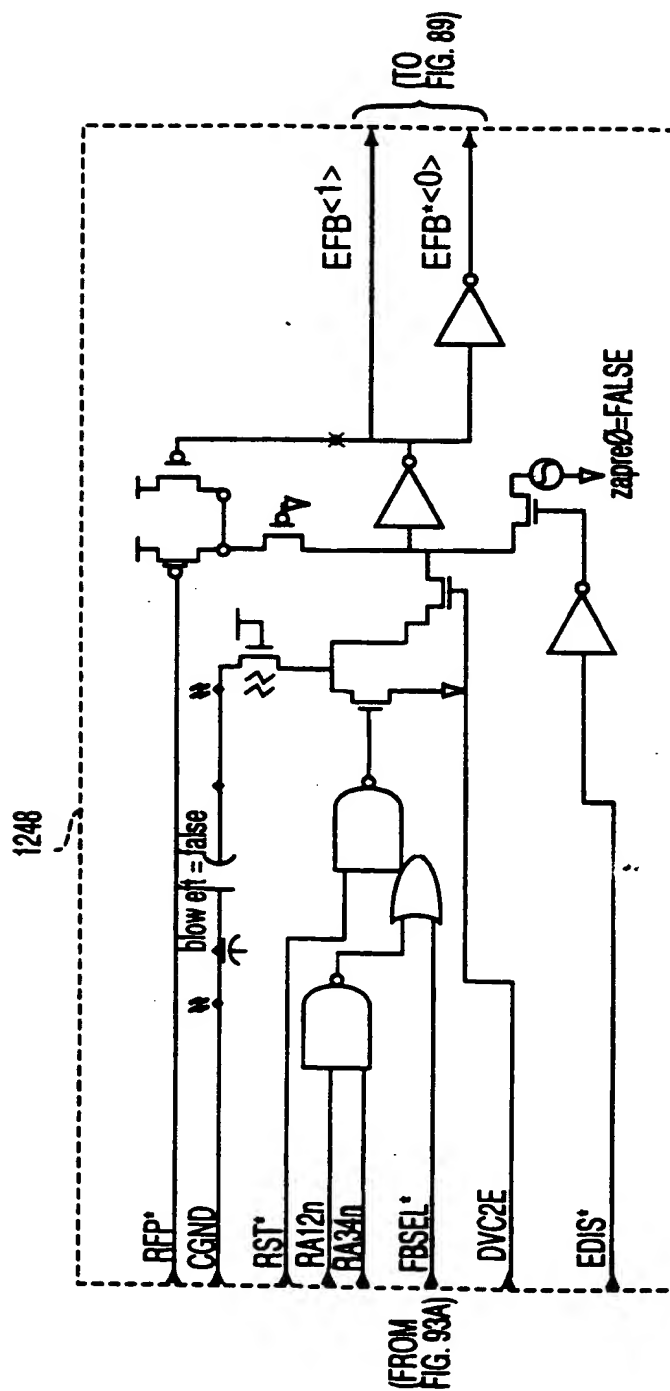
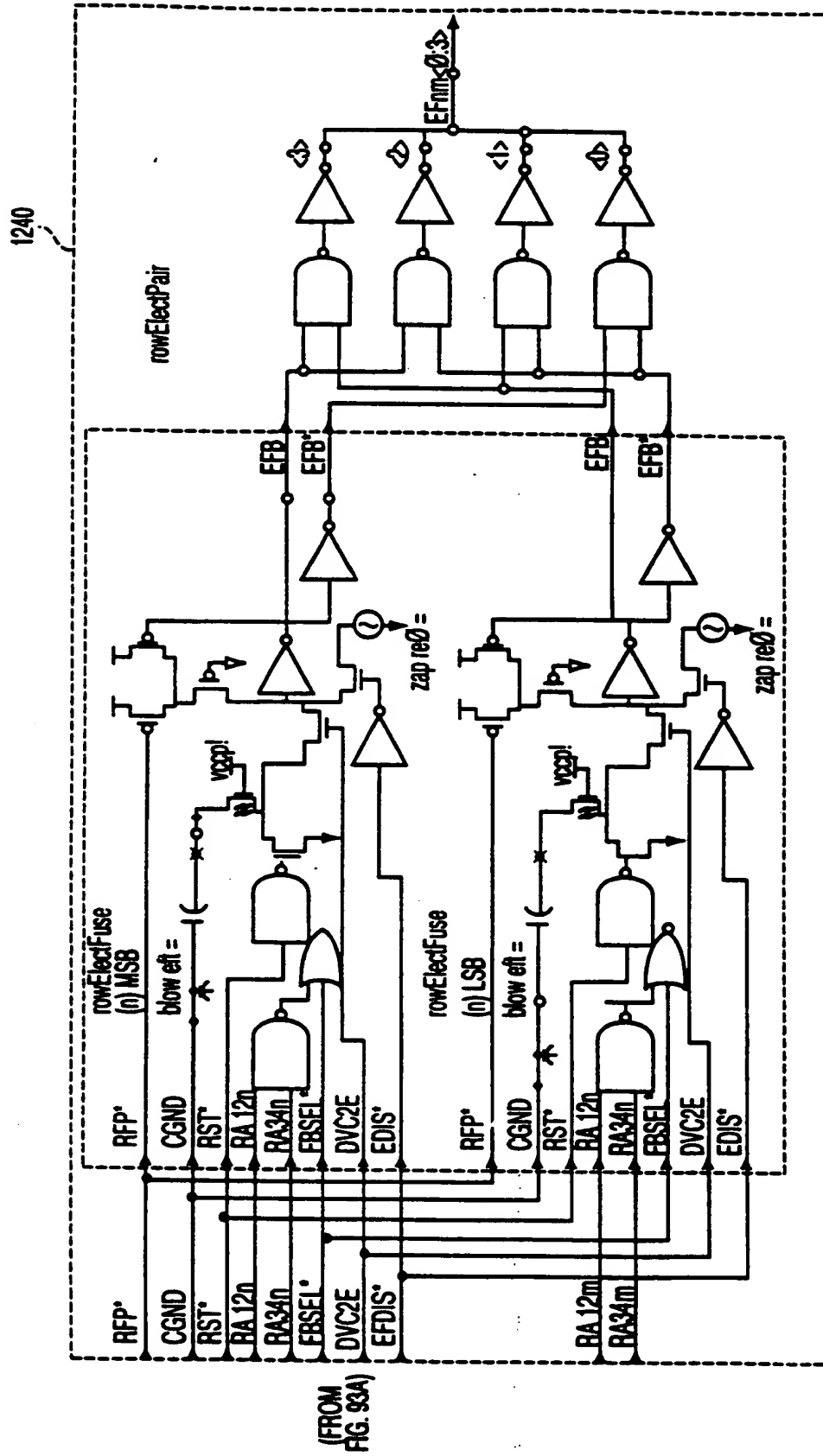
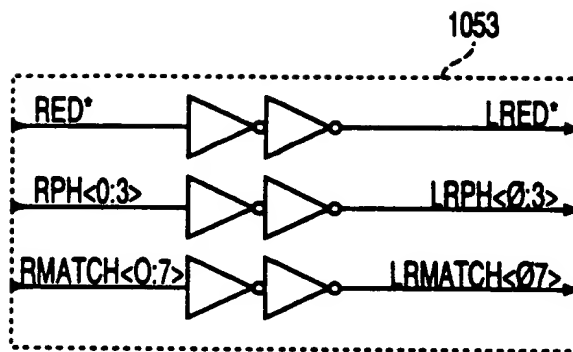


FIG. 93C



**FIG. 94**

1059

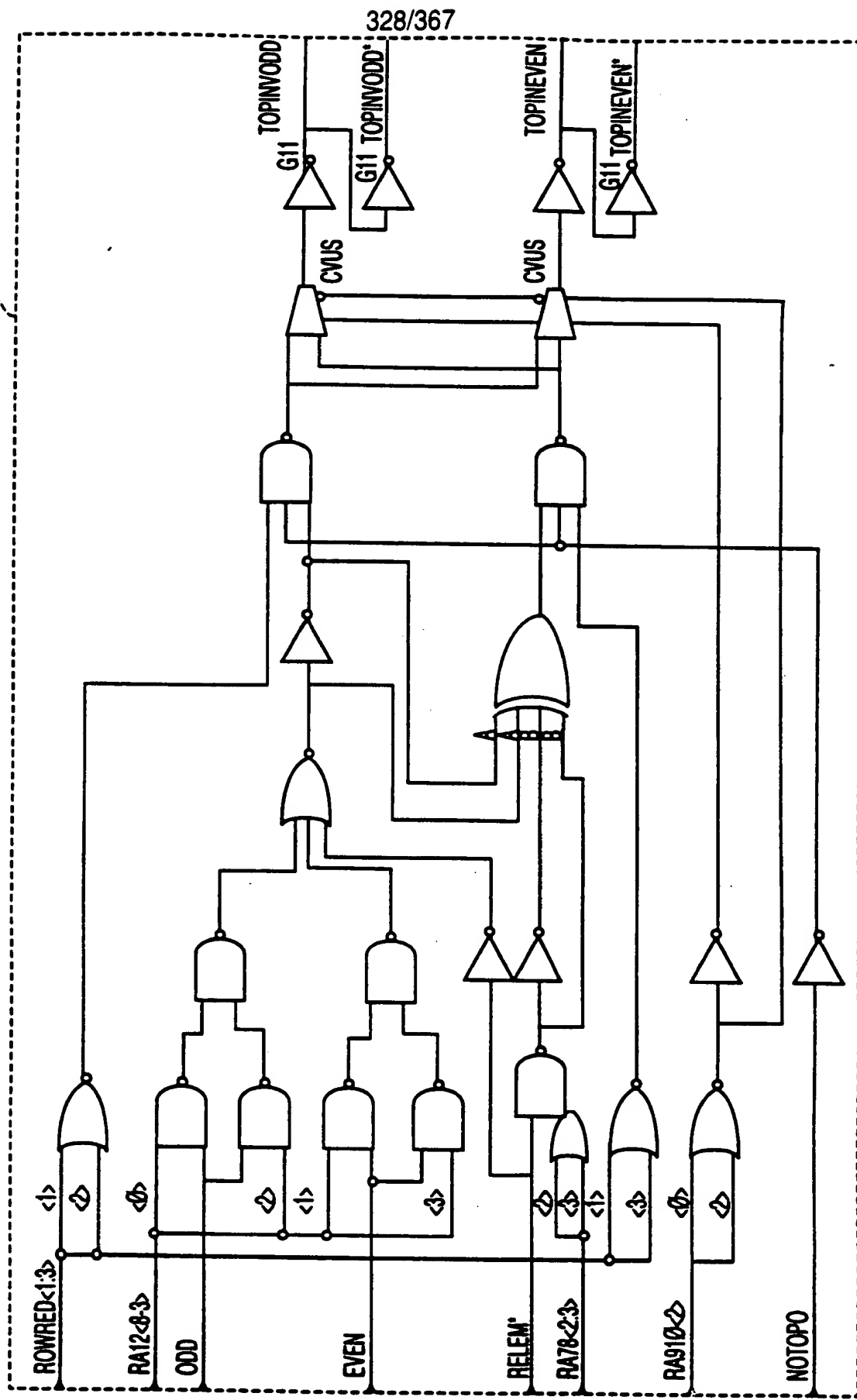


FIG. 95

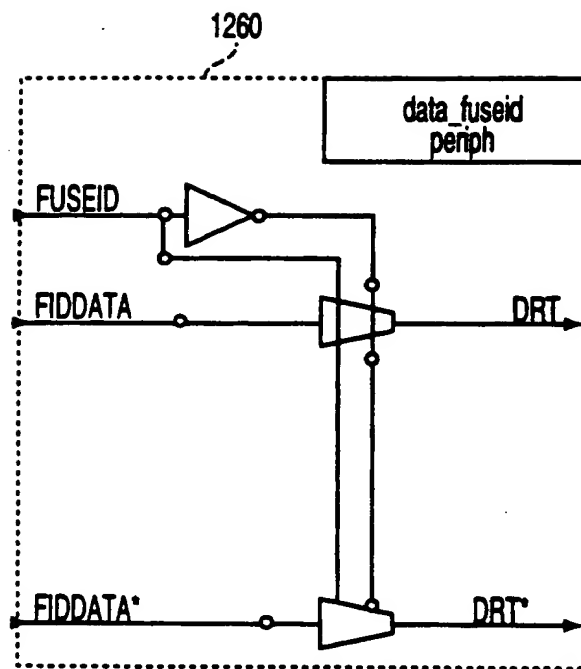
**FIG. 96**

FIG. 97-1

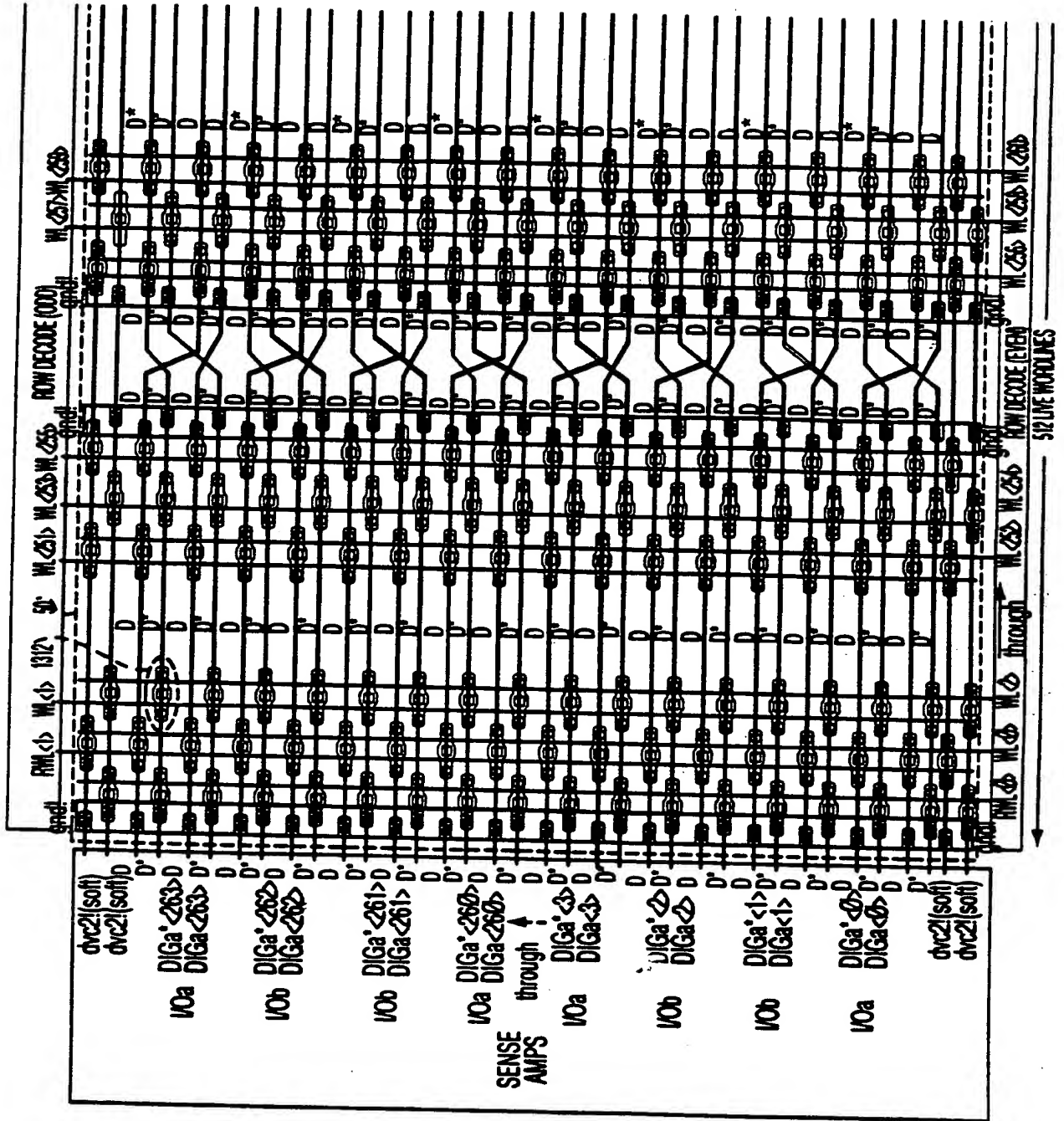


FIG. 97-1 FIG. 97-2

FIG. 97-1

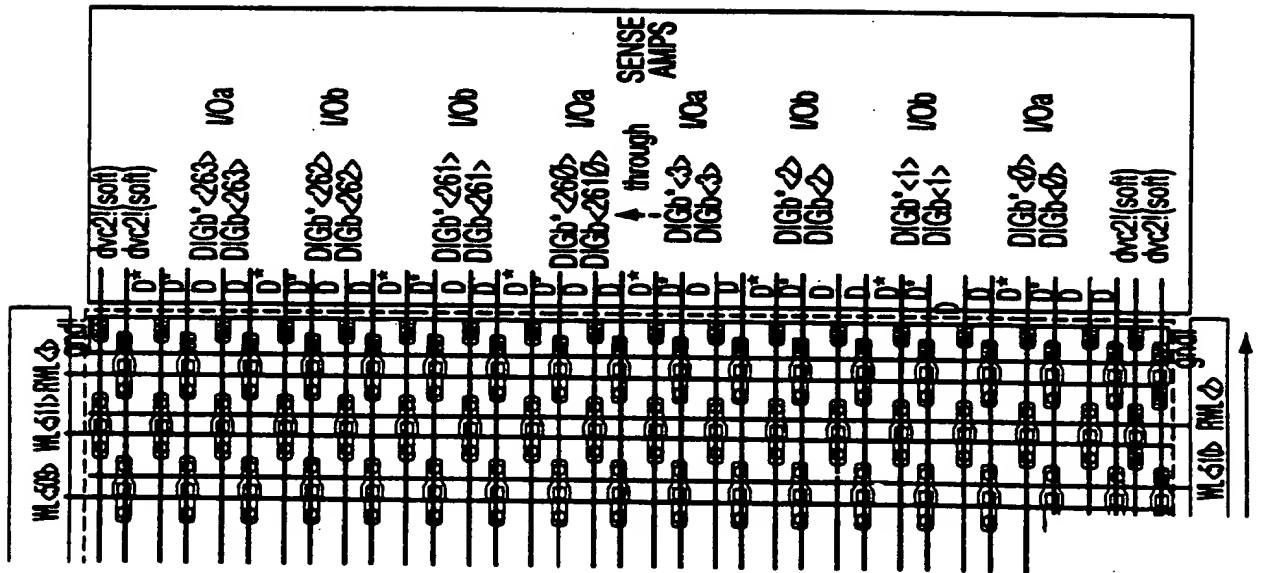
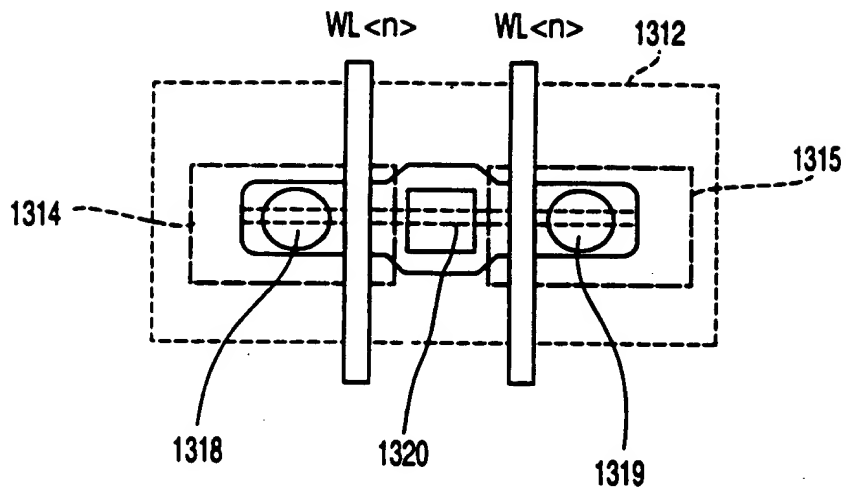


FIG. 97-1 | FIG. 97-2

FIG. 97-2

**FIG. 98**

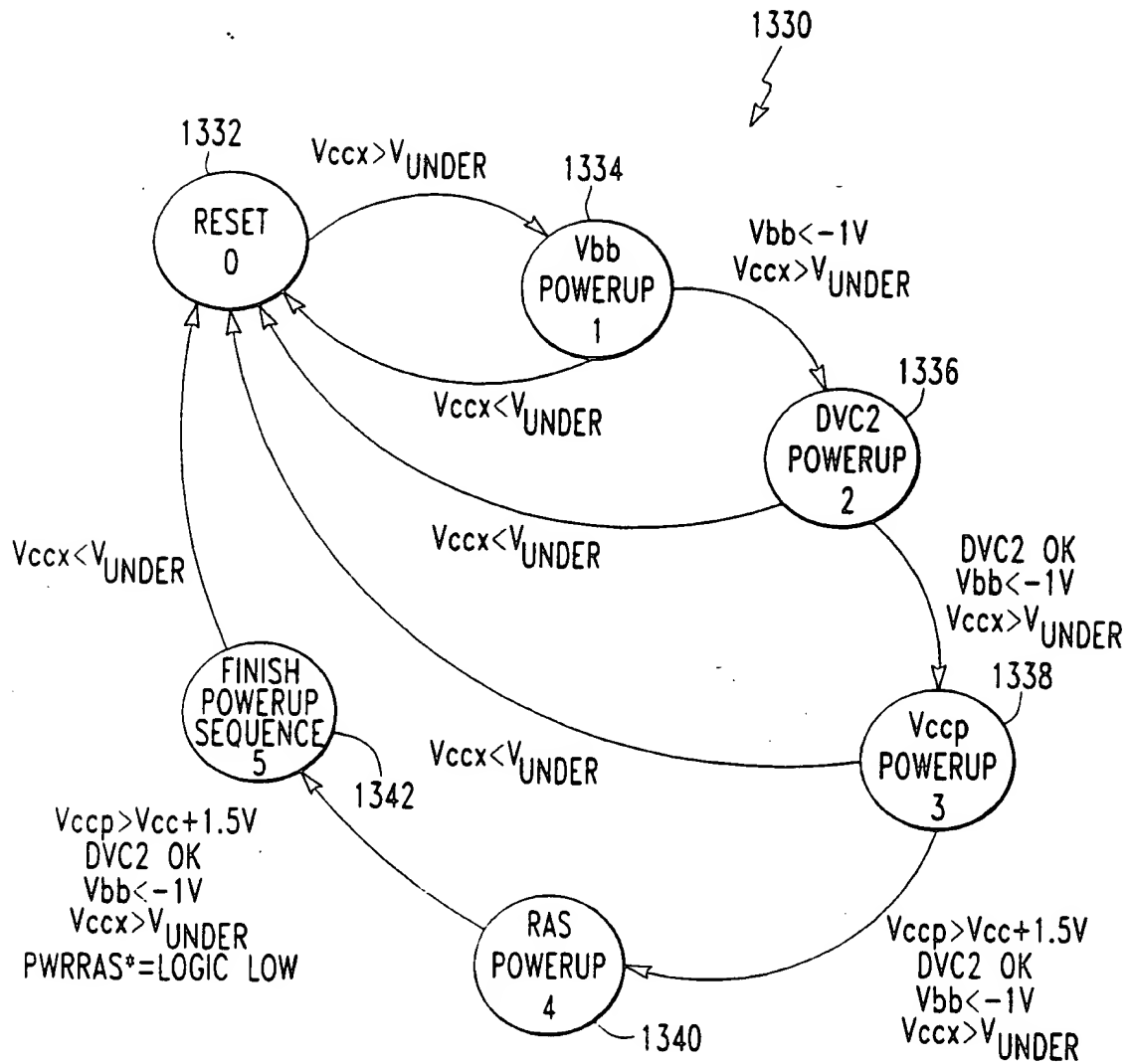


FIG. 99

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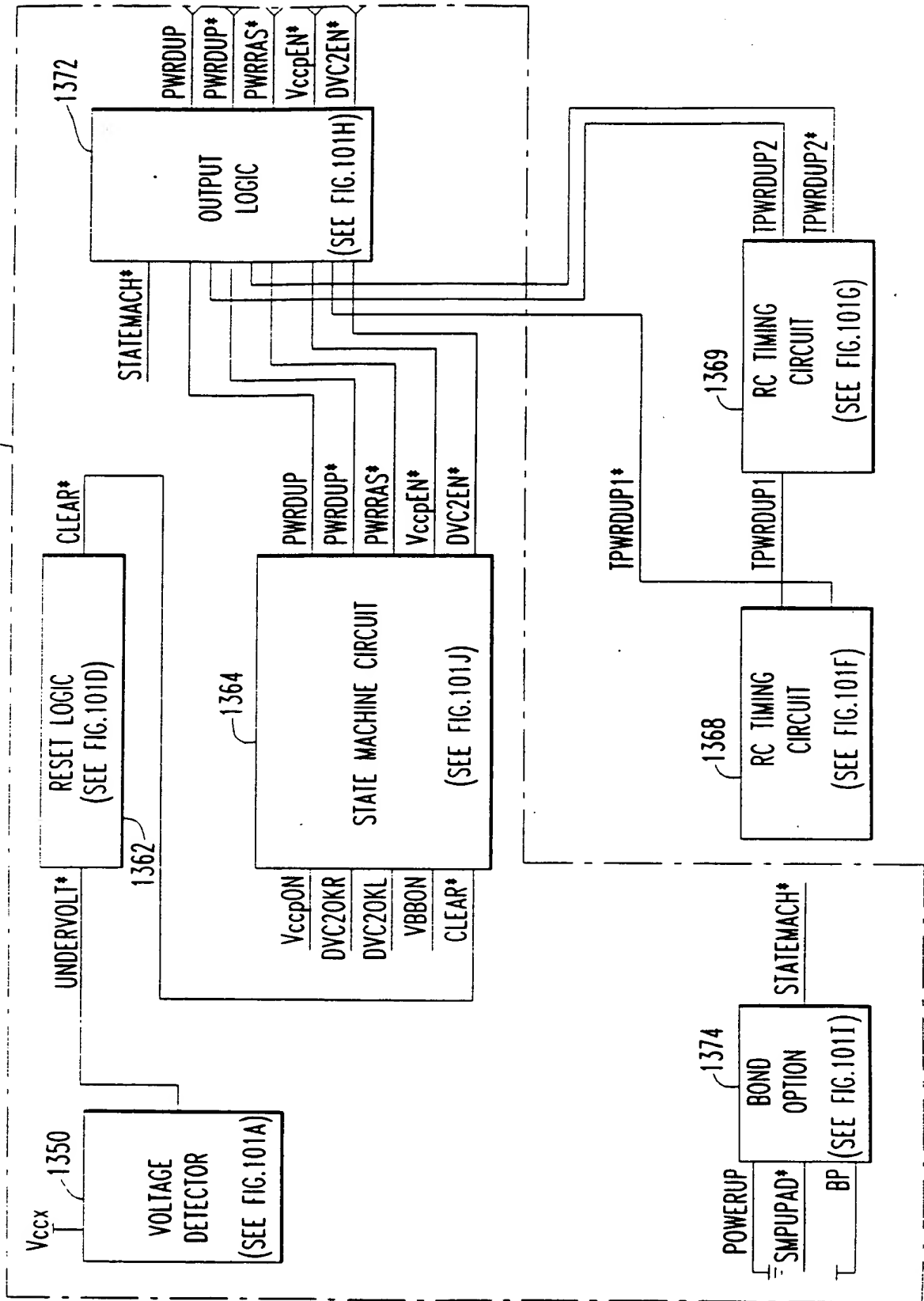


FIG. 100

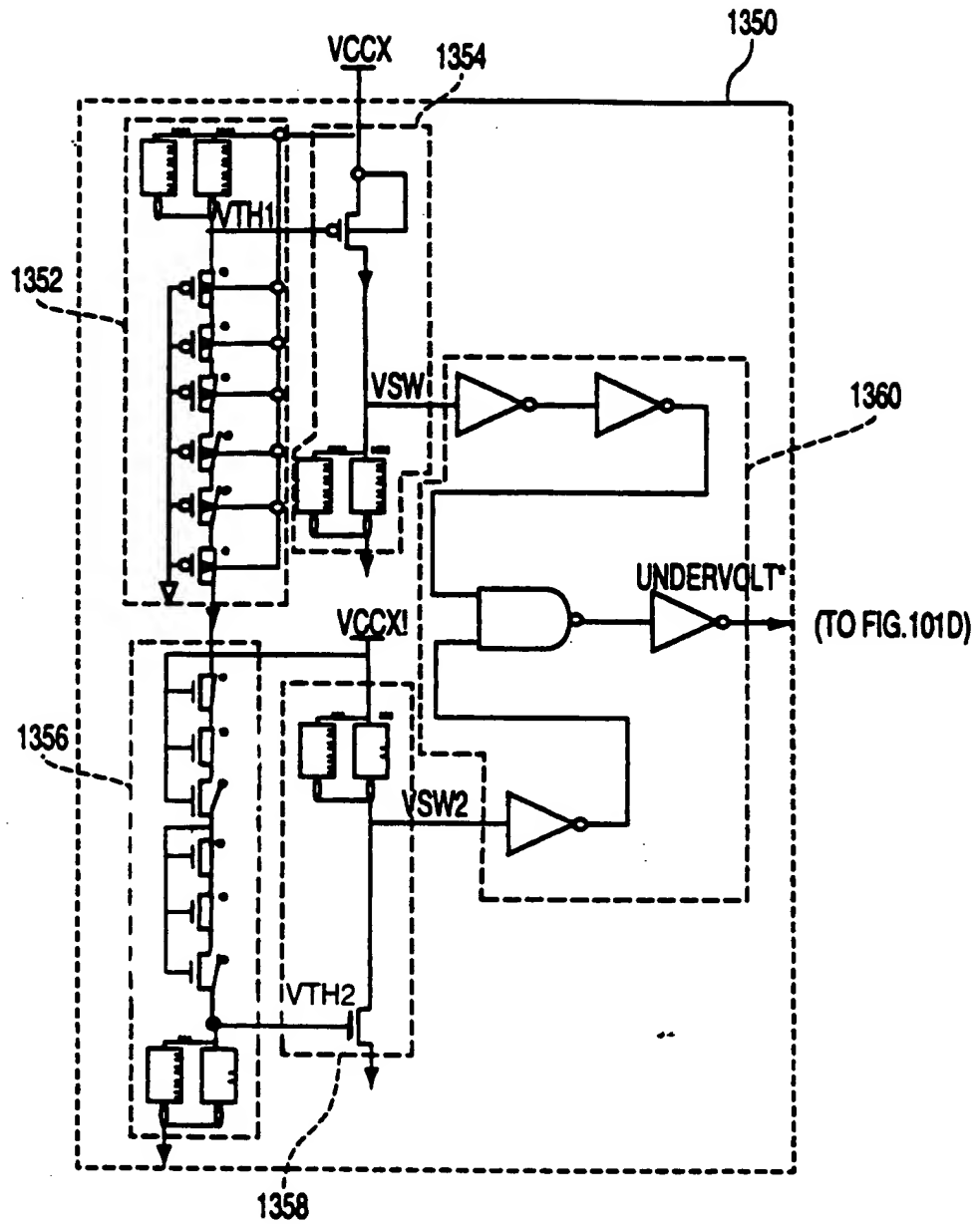
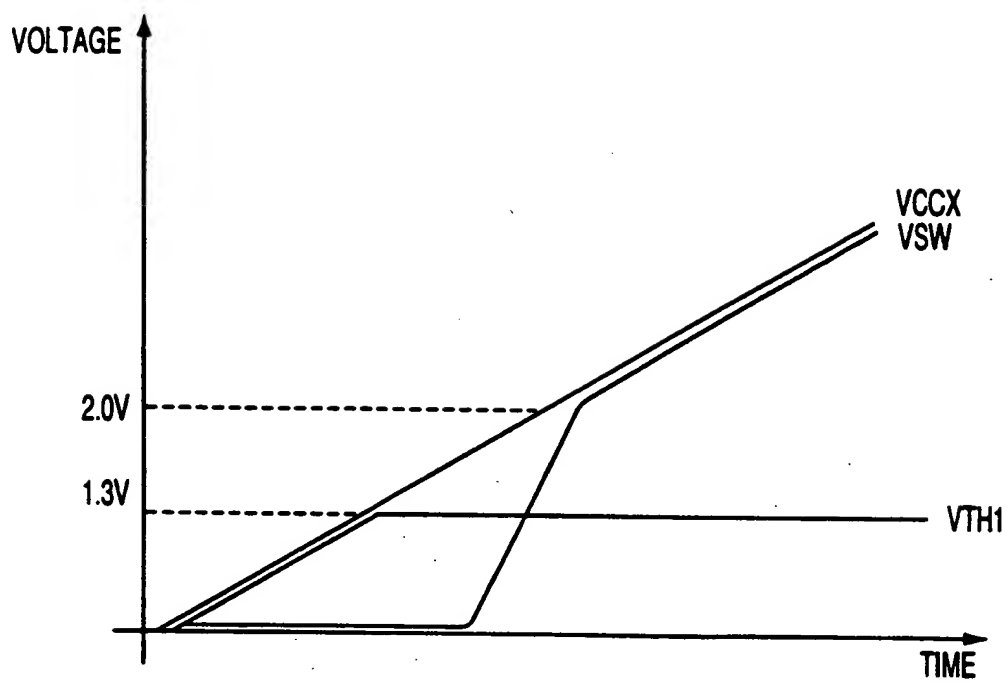
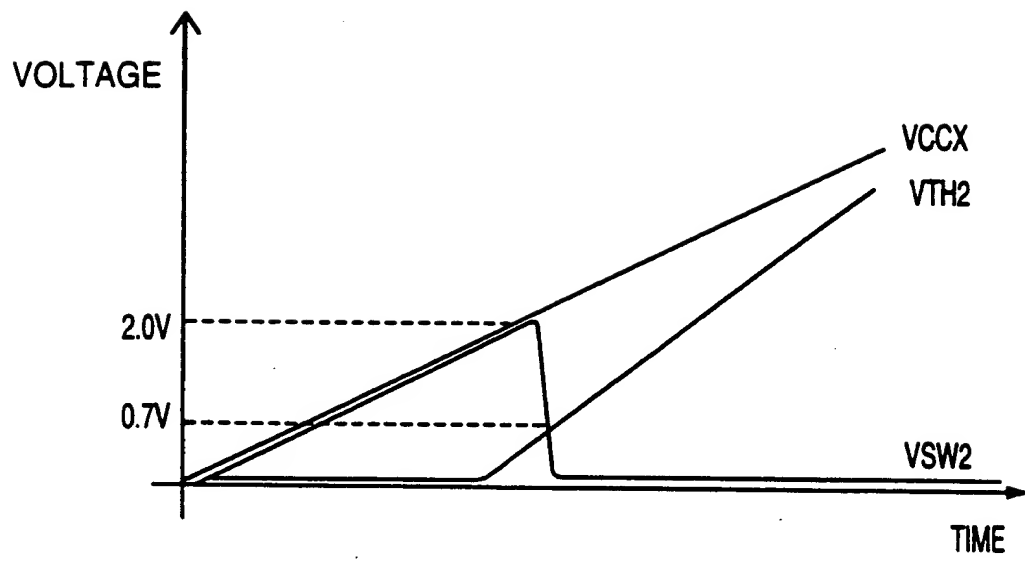


FIG. 101A

**FIG. 101B**

**FIG. 101C**

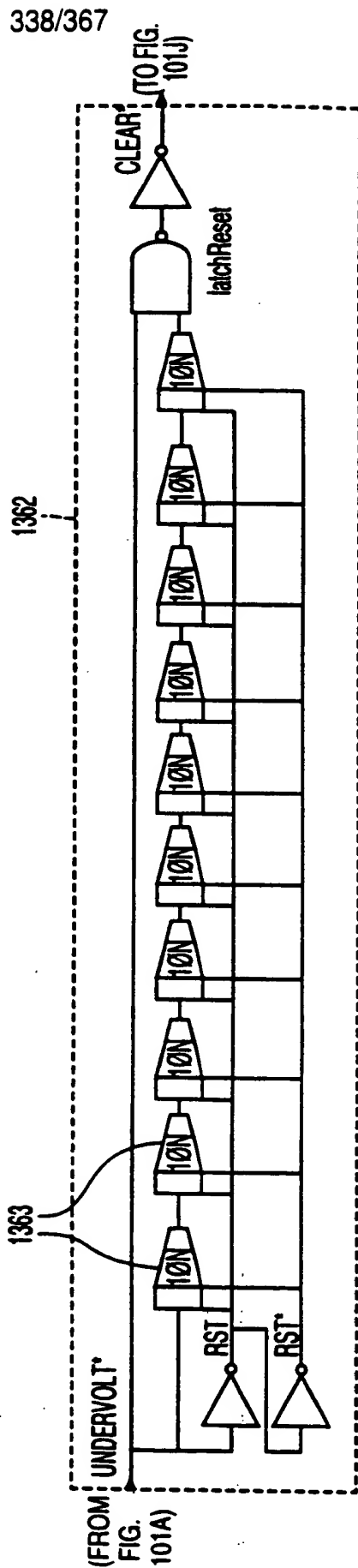


FIG. 101D

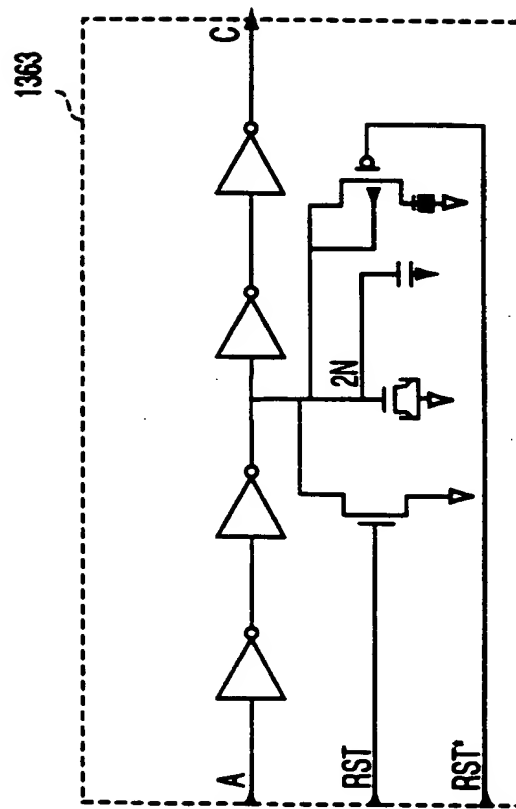


FIG. 101E

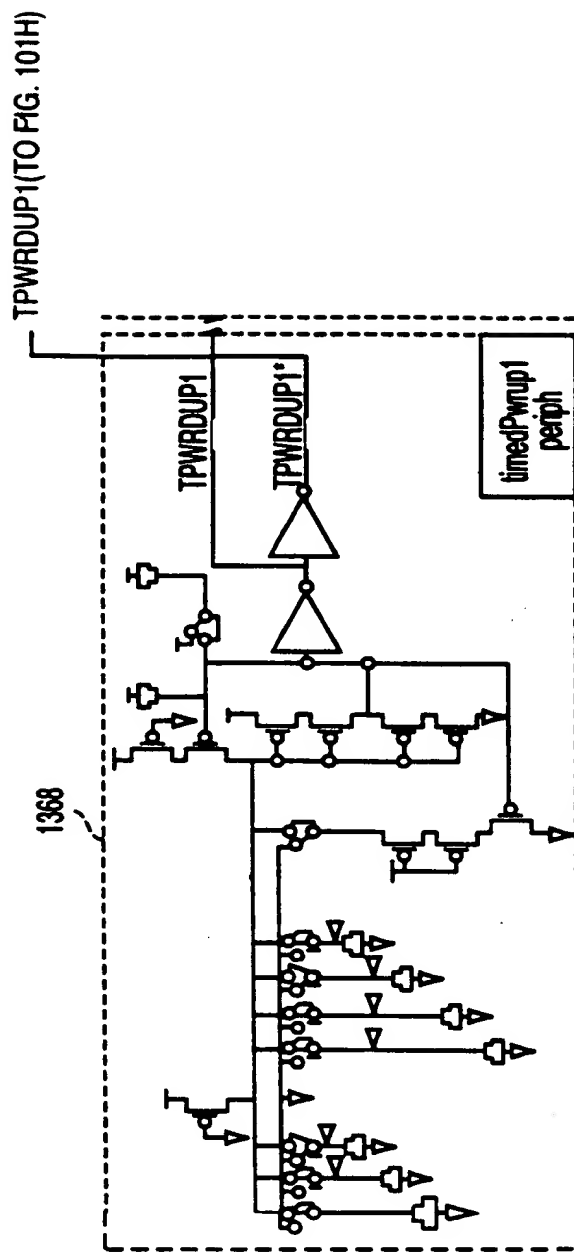


FIG. 101F

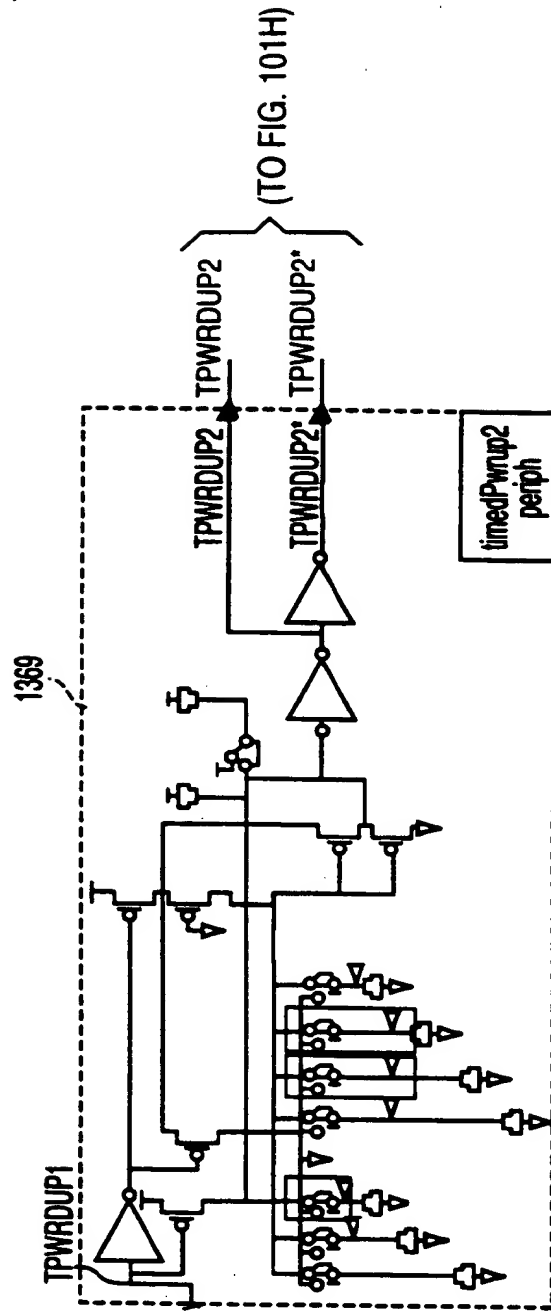


FIG. 101G

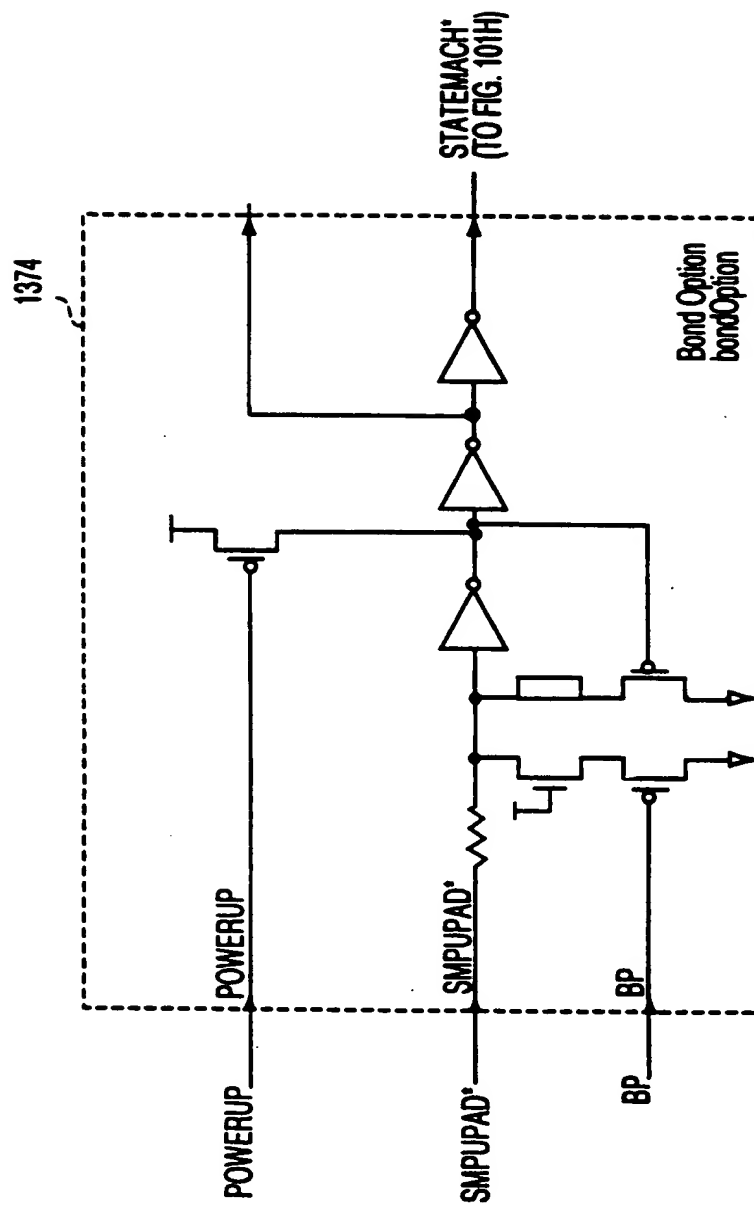


FIG. 101I

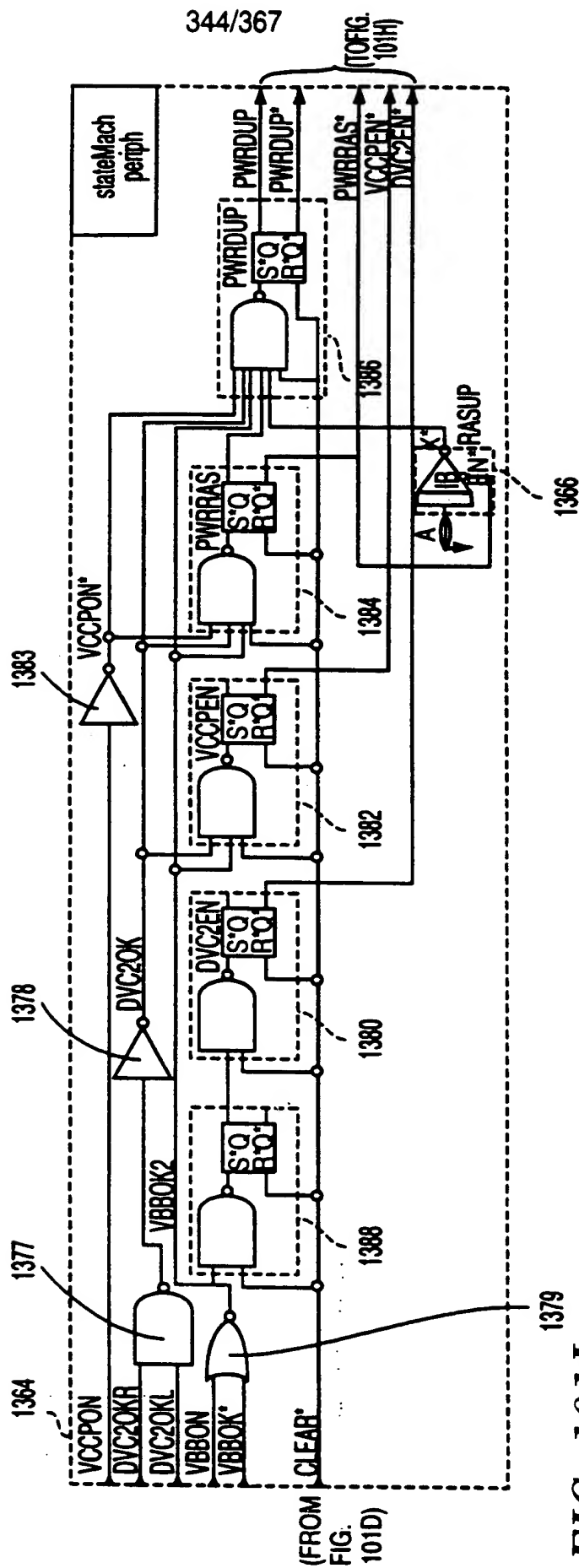
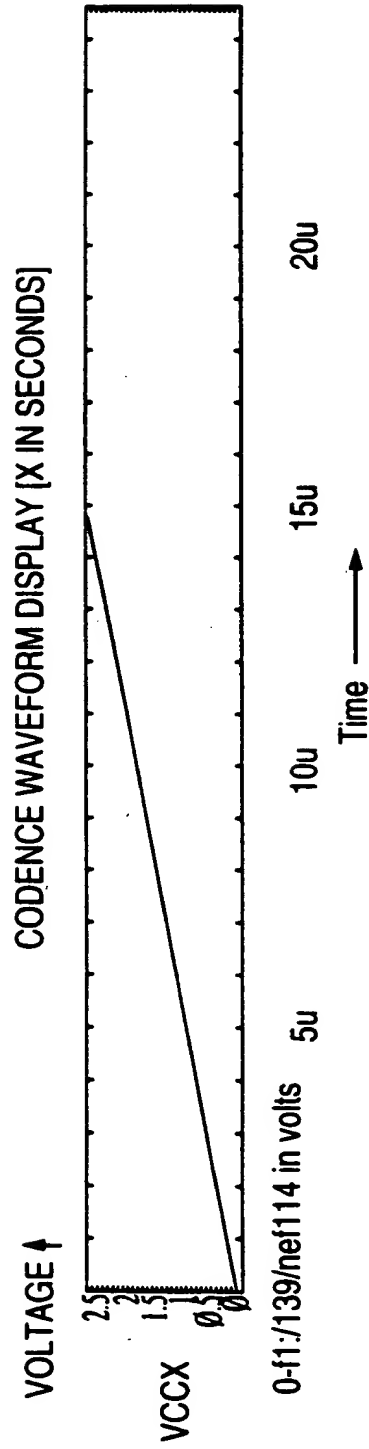


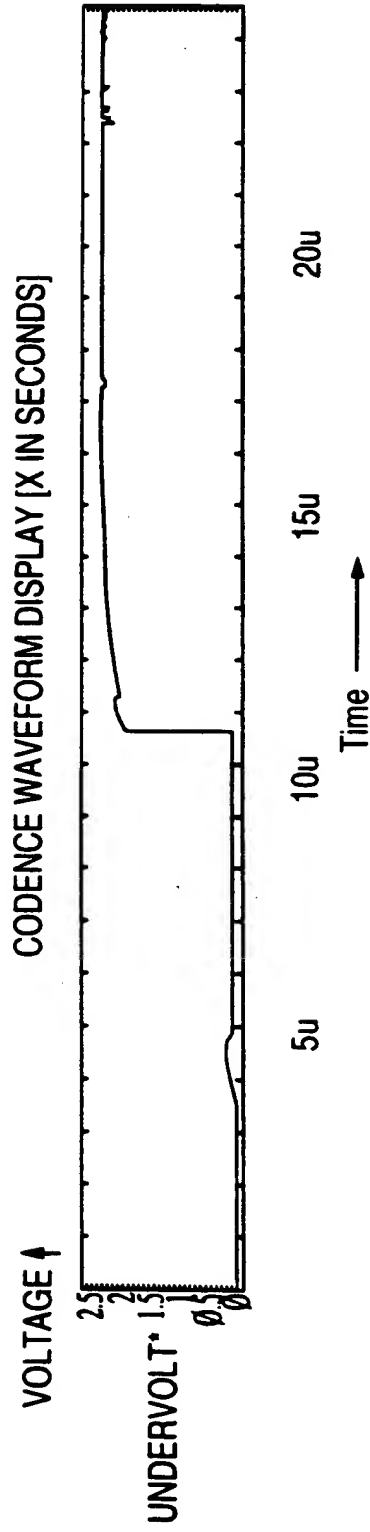
FIG. 101J



NOTE THAT PWRRAS* WAITED FOR DVC20KR

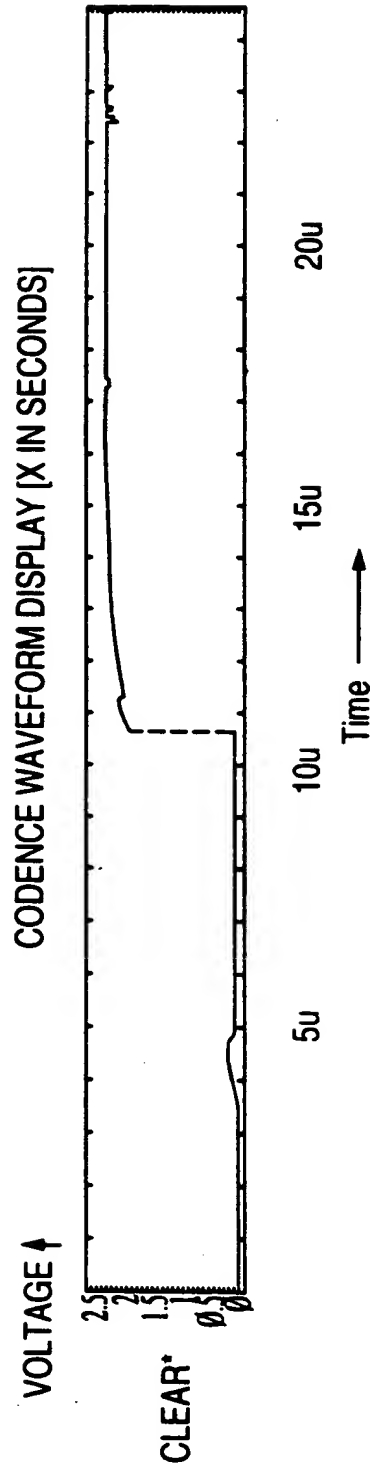
FIG. 102A

FIG. 102B



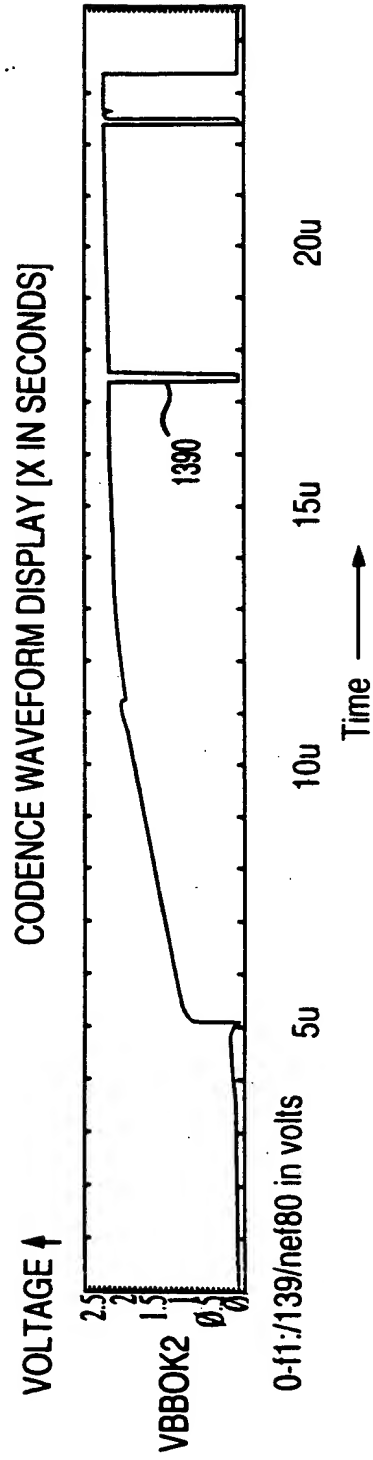
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102B



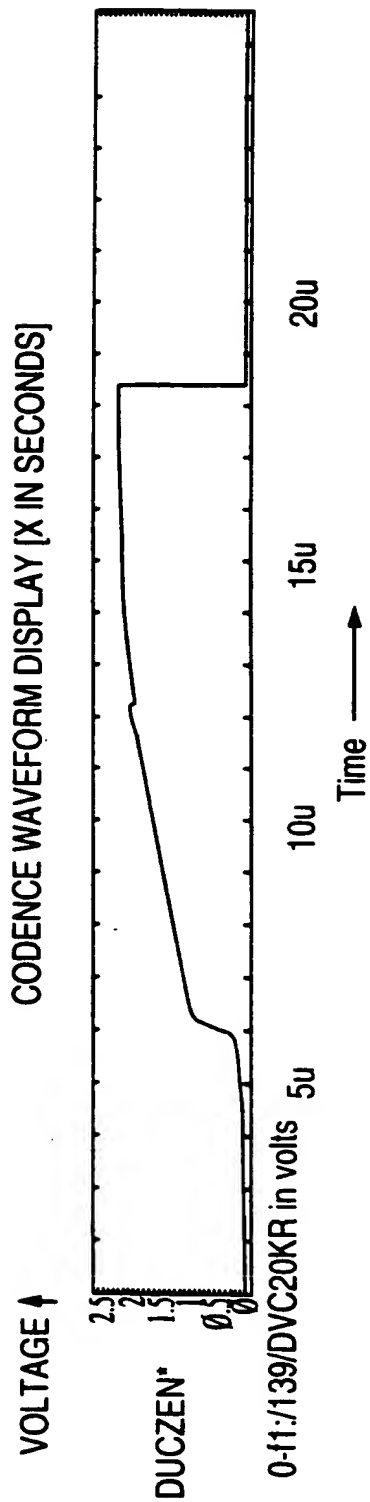
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102C



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102D



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102E

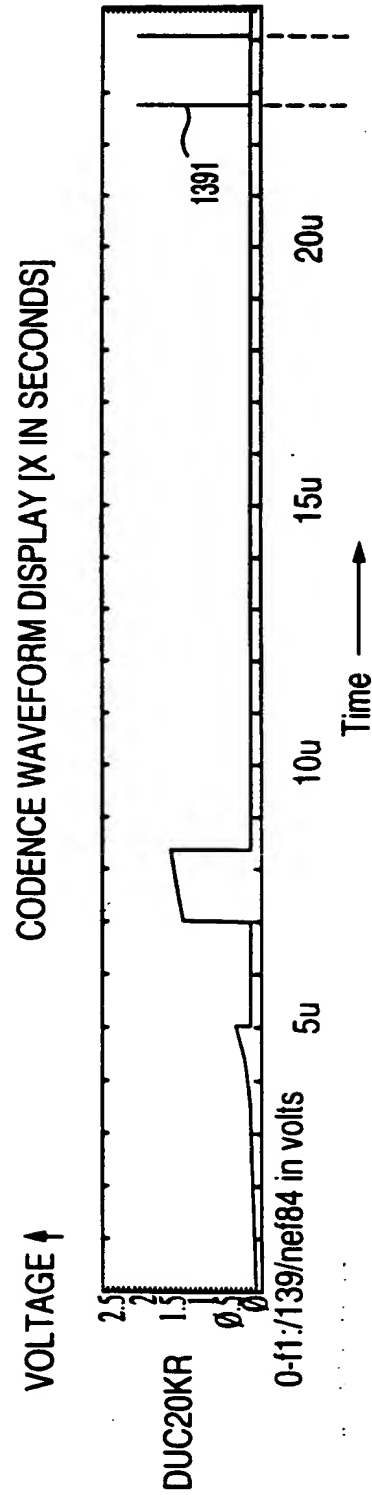
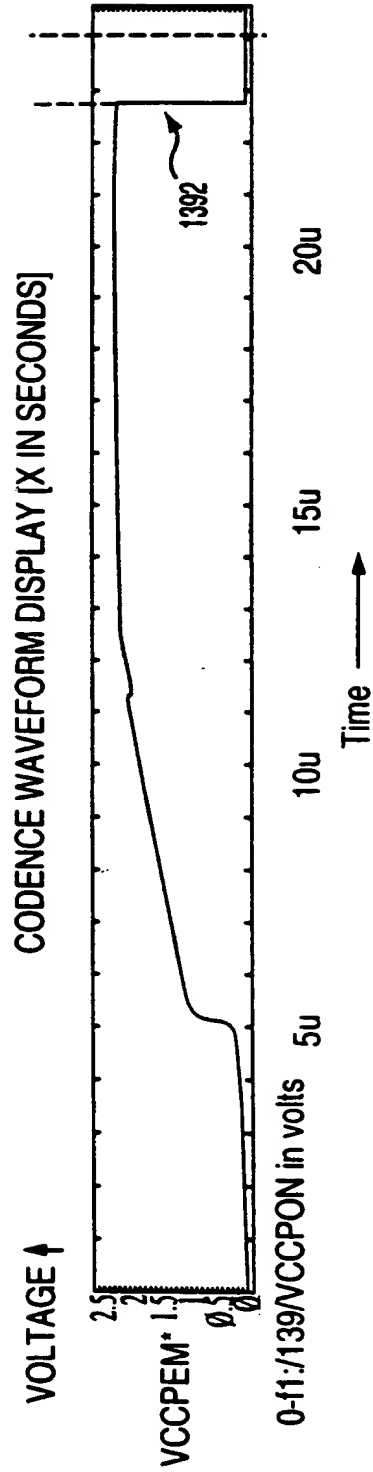


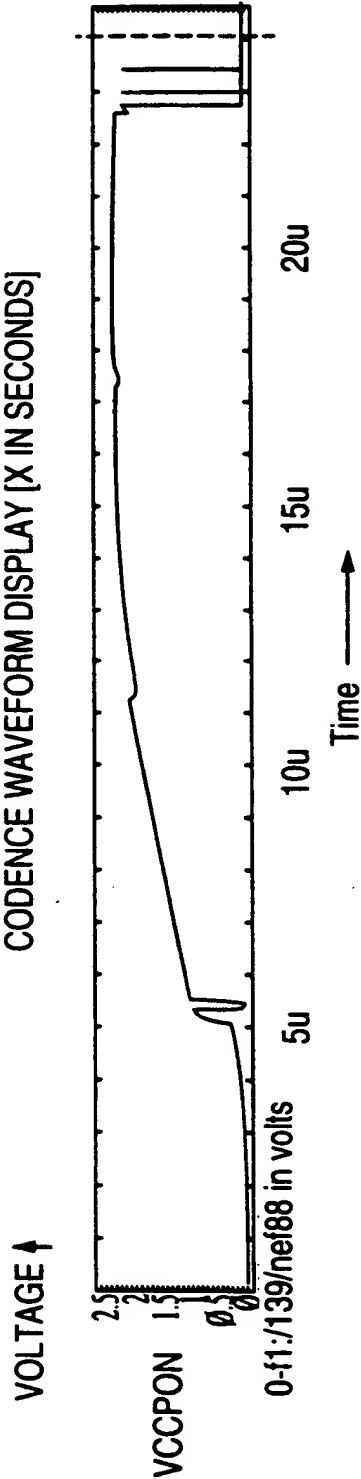
FIG. 102F

TOP SECRET



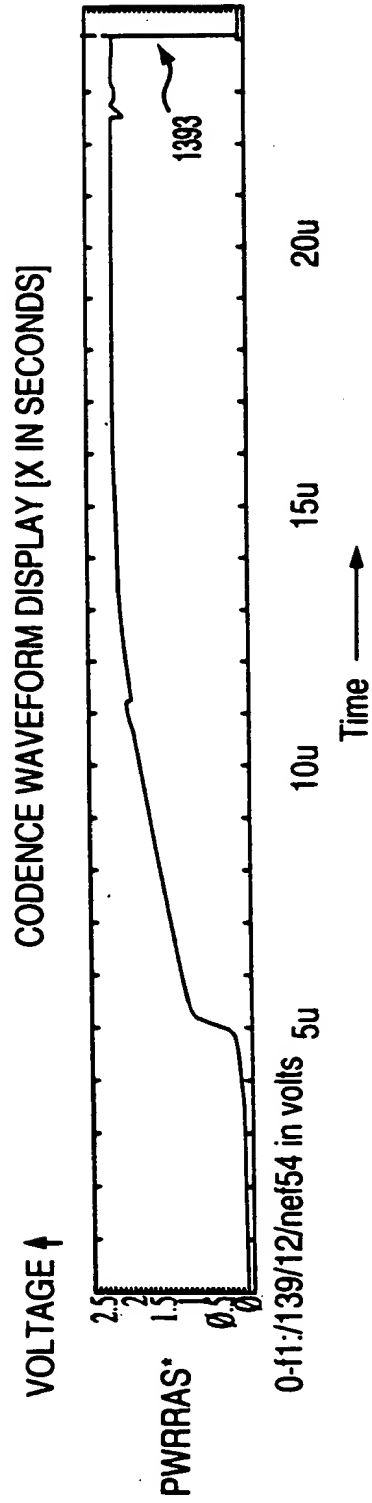
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102C



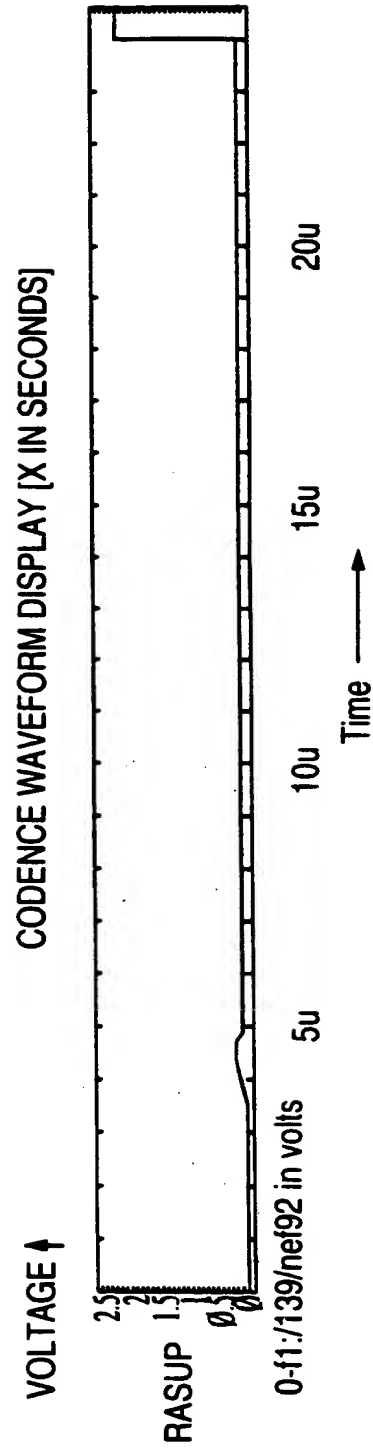
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102H



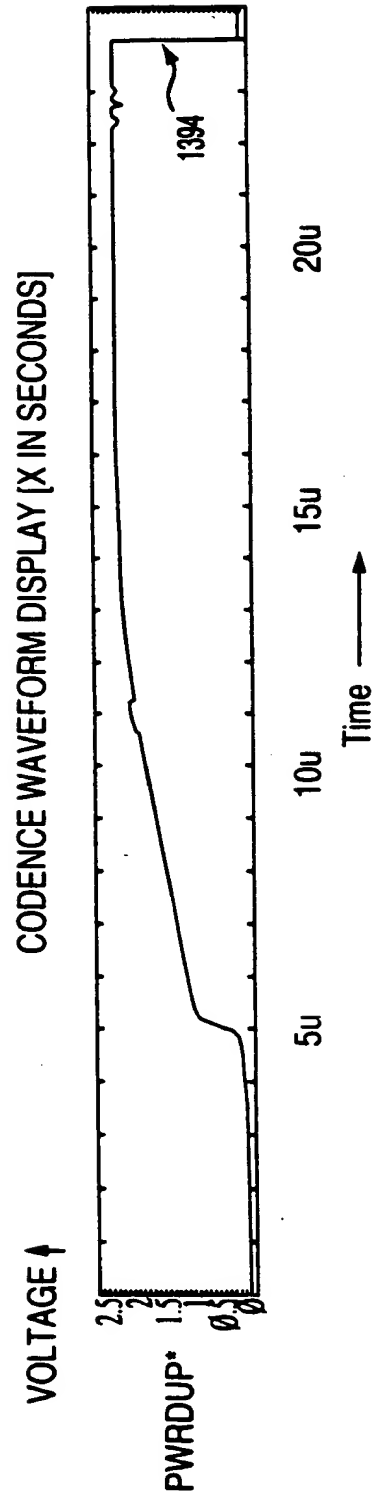
NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102I



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102J



NOTE THAT PWRRAS* WAITED FOR DVC20KR

FIG. 102K

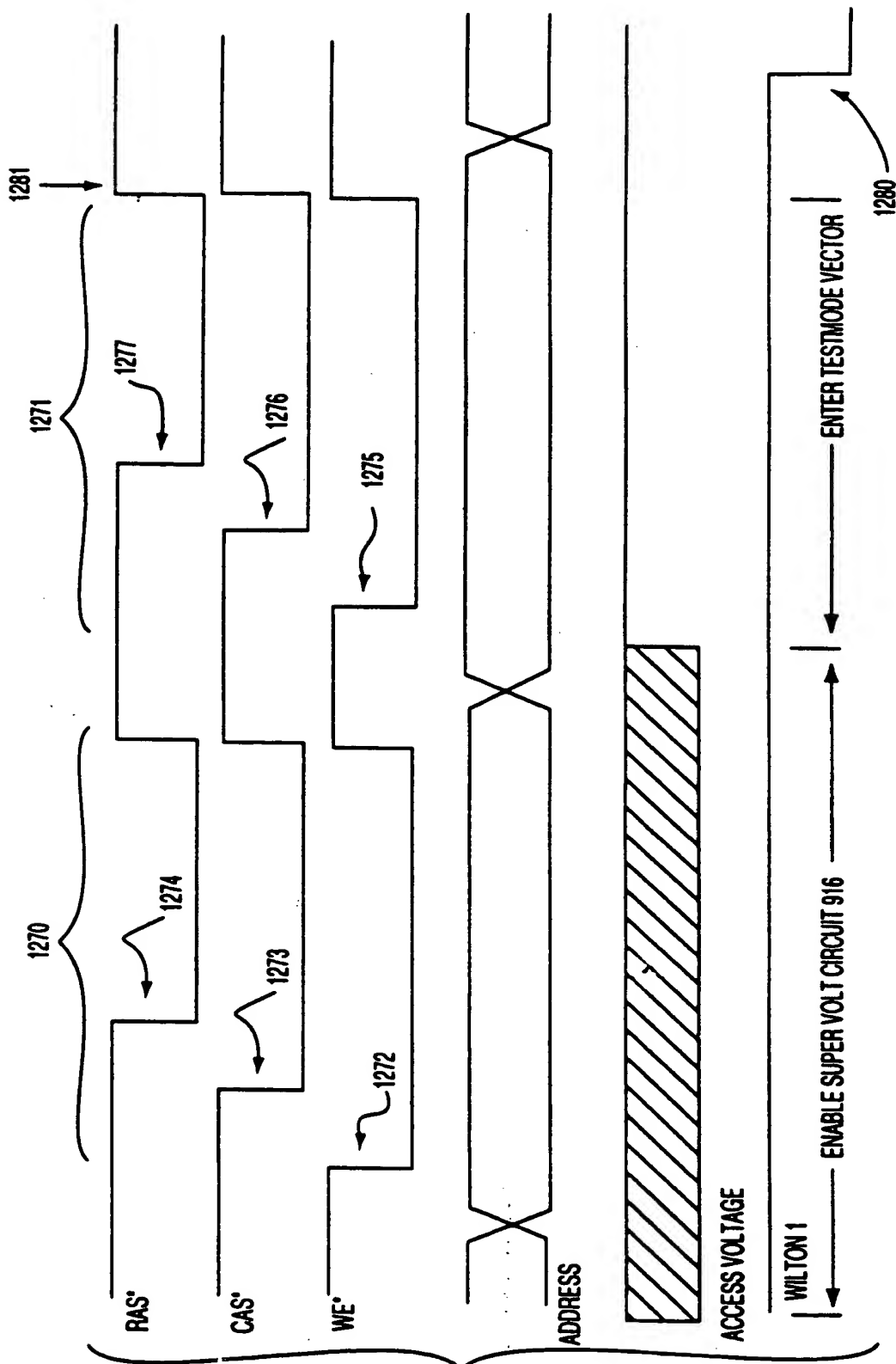


FIG.
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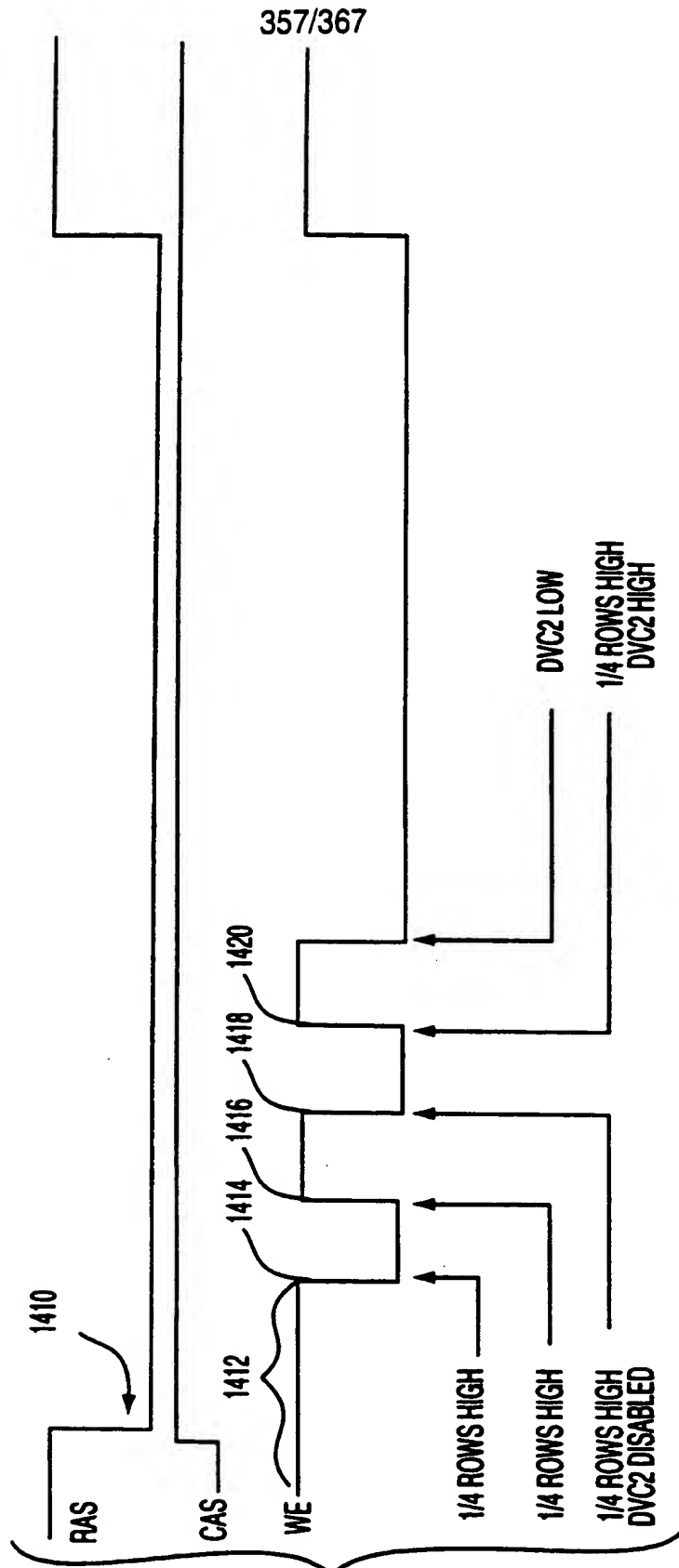


FIG.
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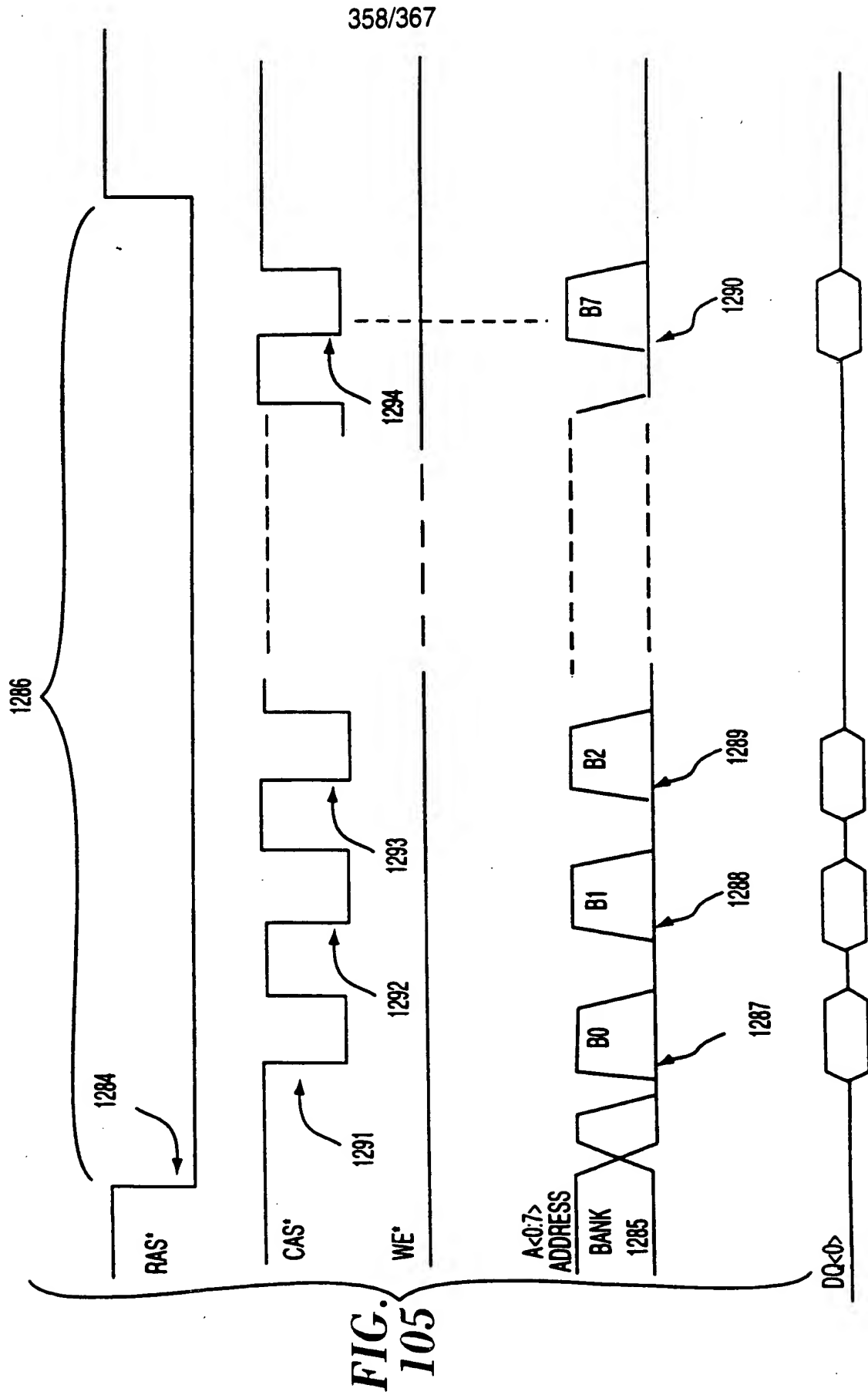
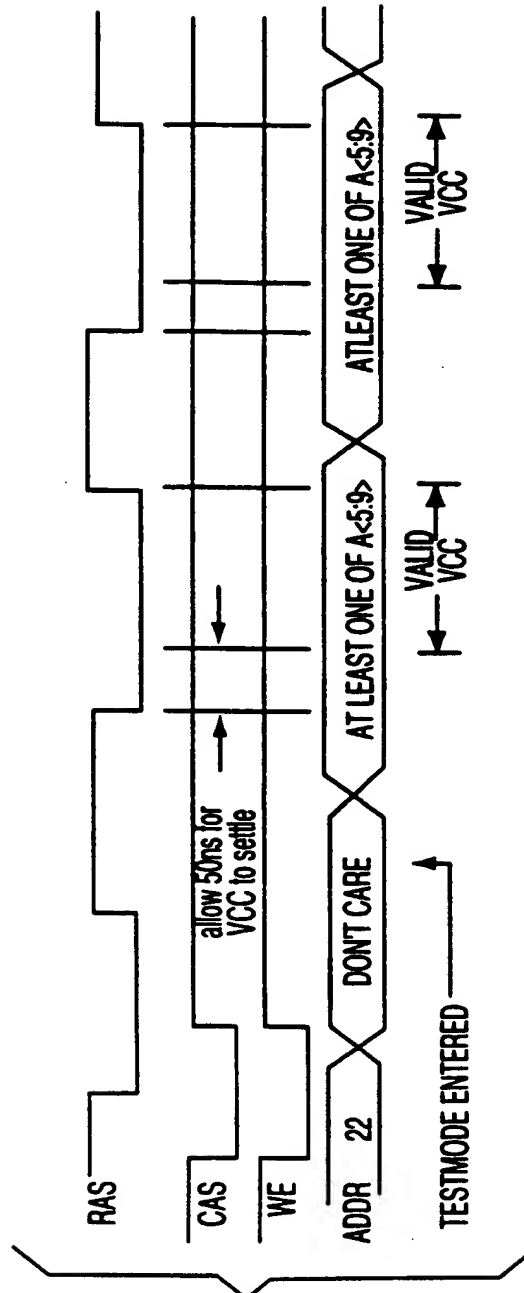


FIG. 106



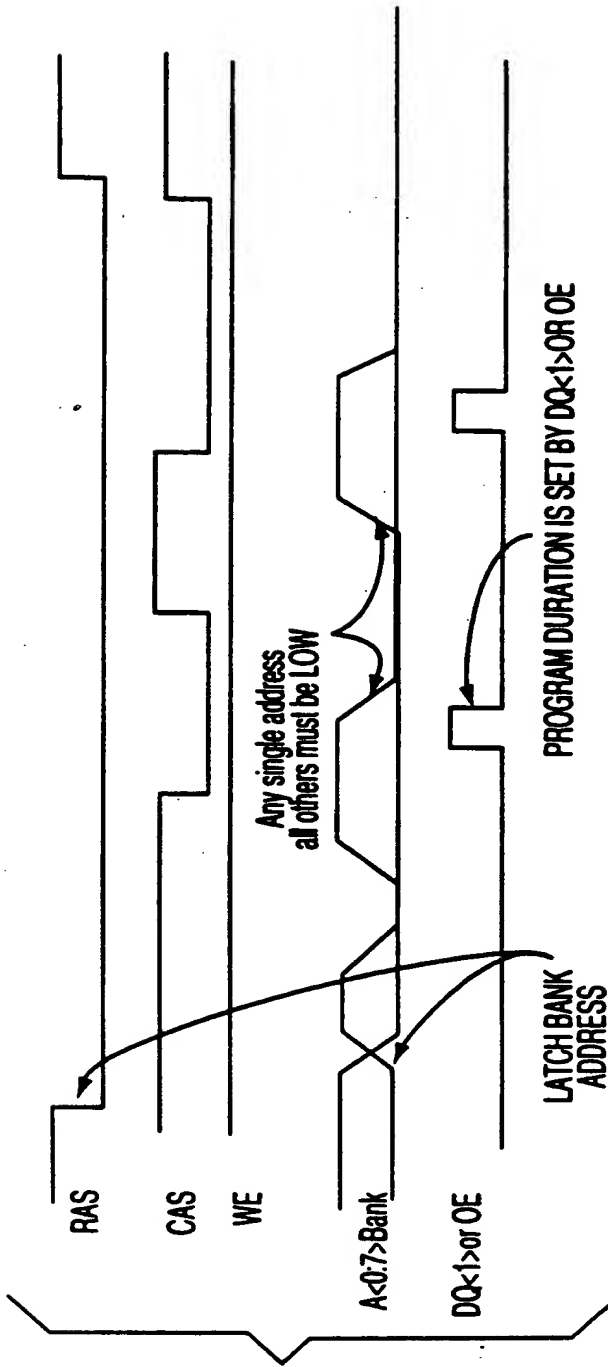


FIG. 107

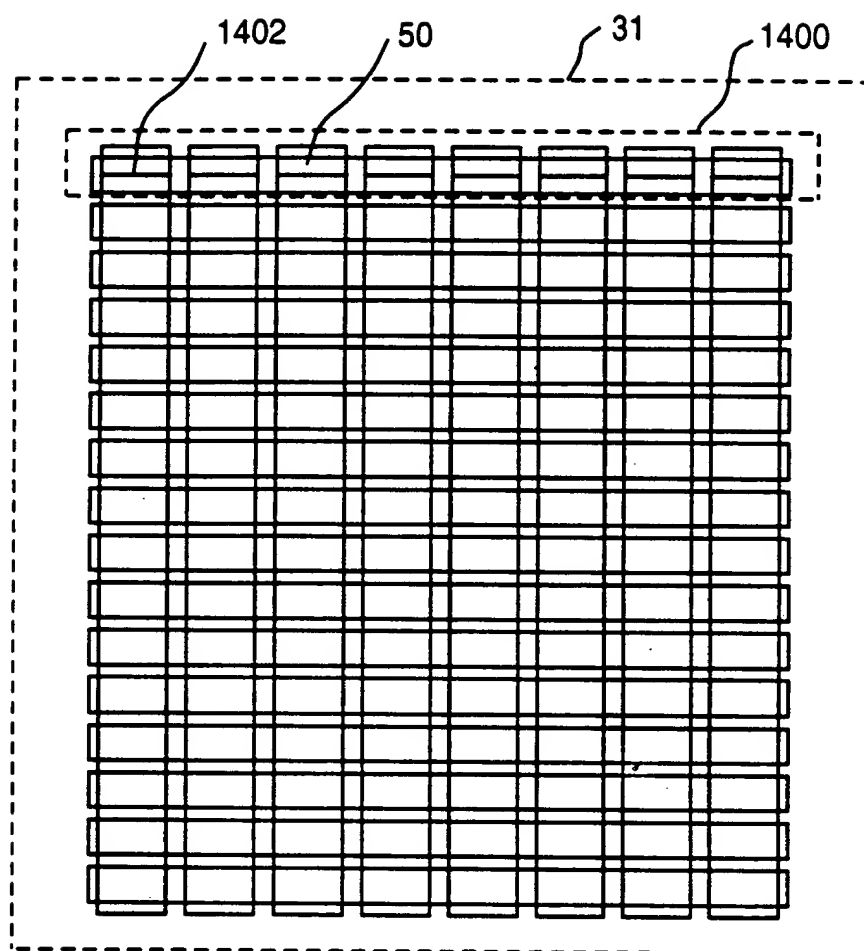
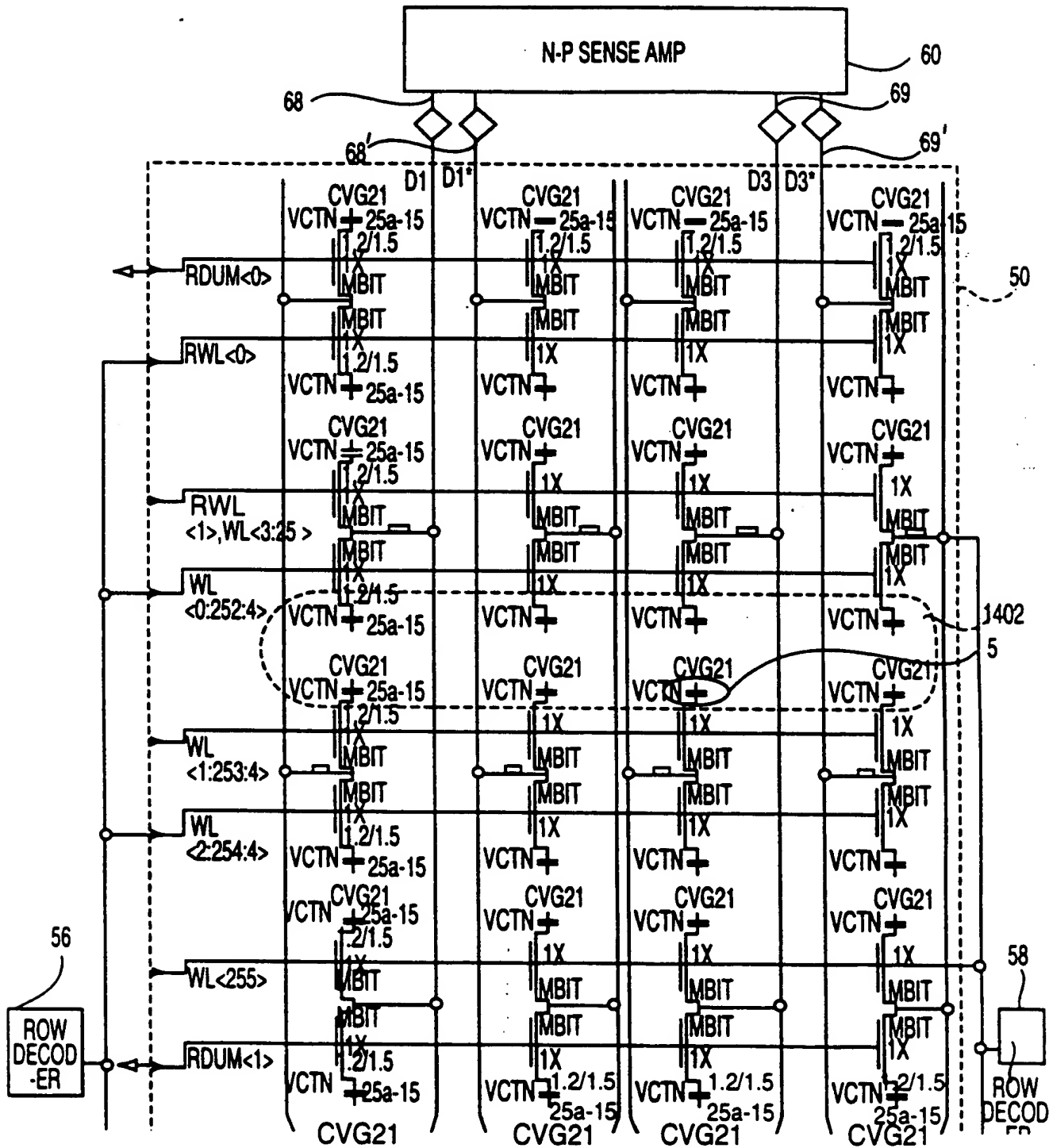
**FIG. 108**

FIG. 109-1

FIG. 109-1
FIG. 109-2



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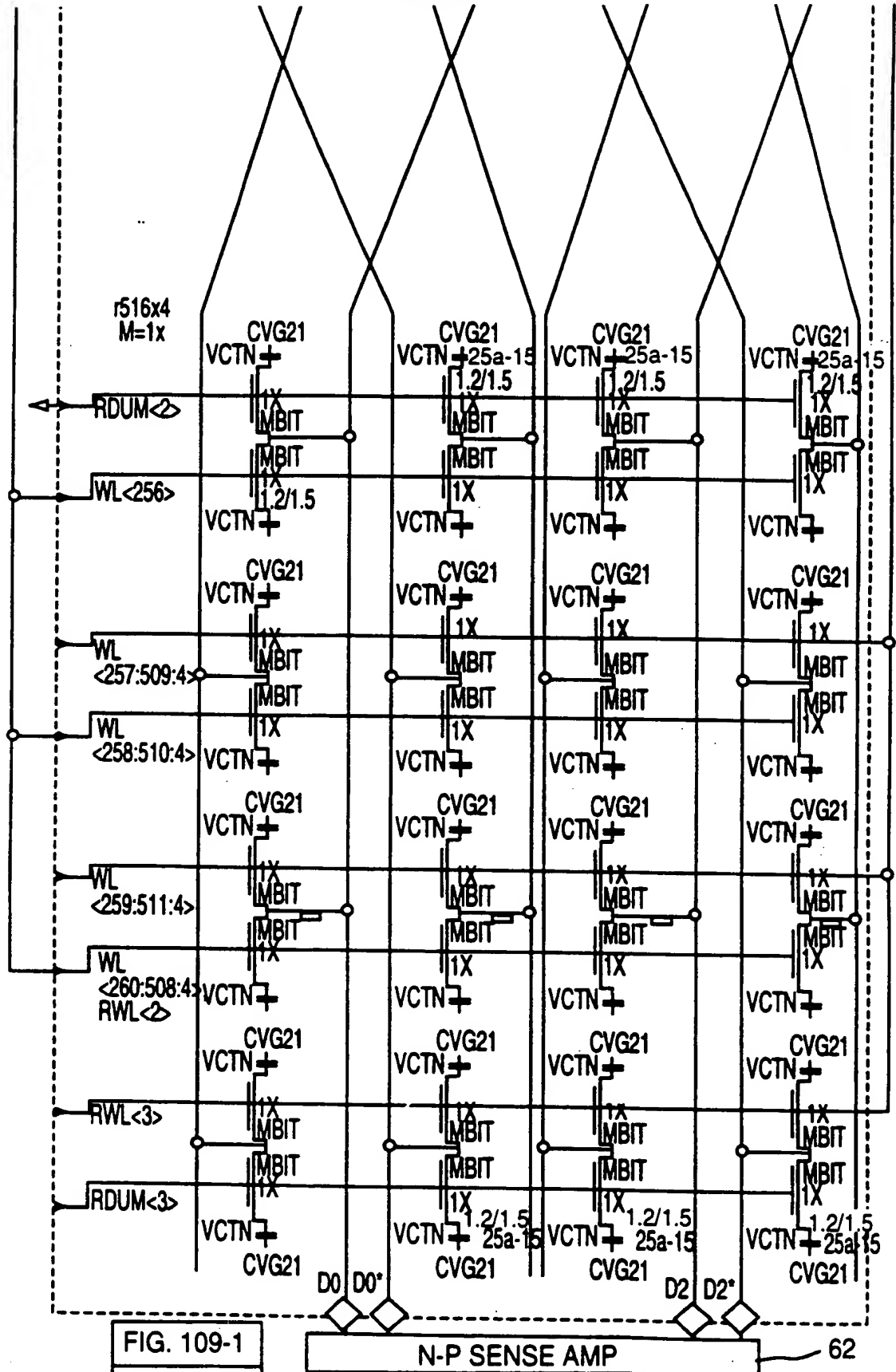


FIG. 109-2

Shrink Factor = 0.24
 Mbit = 2.5 x 5.4 um (drawn)
 0.600um Column Pitch
 0.648 um Row Pitch

10

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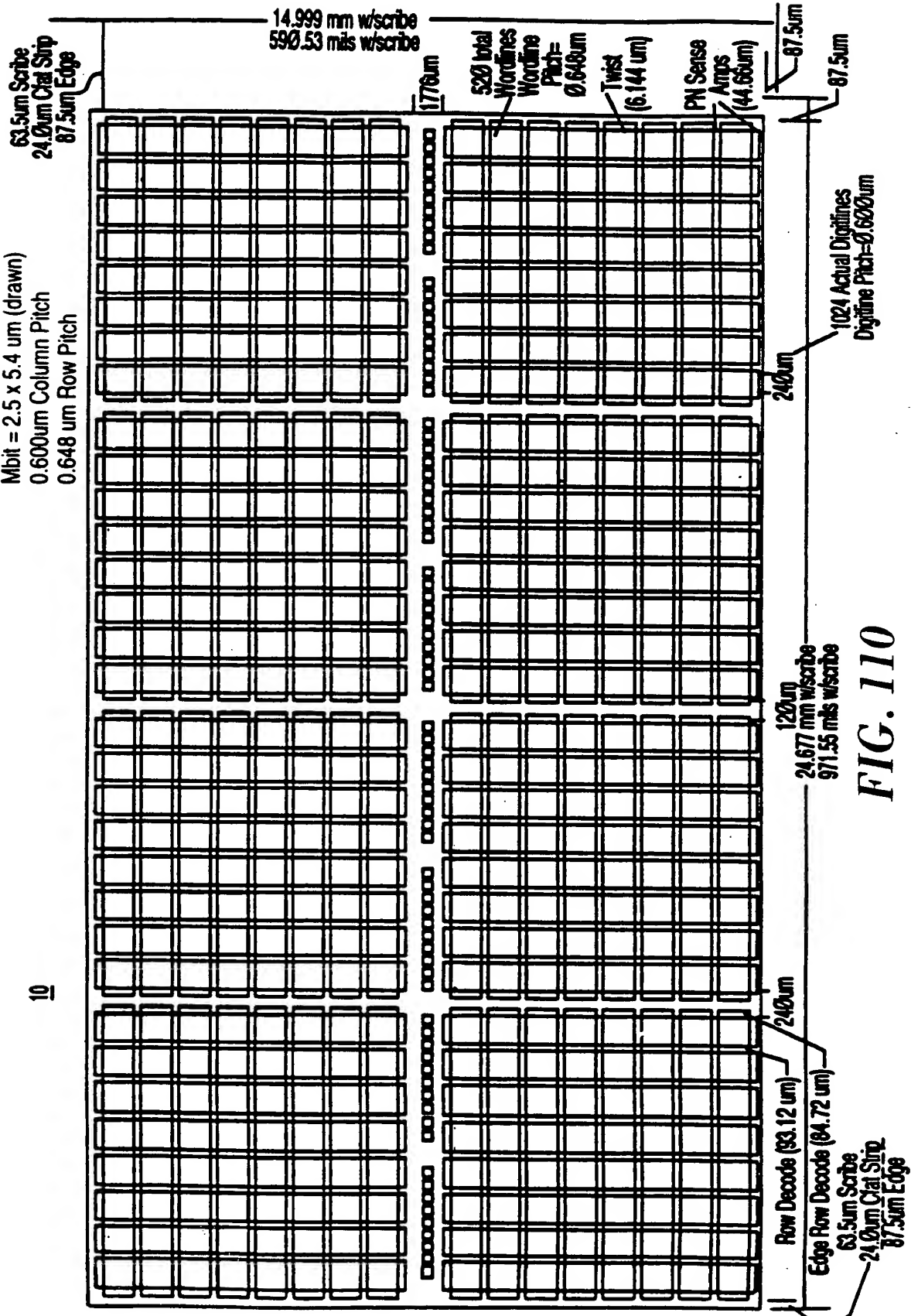
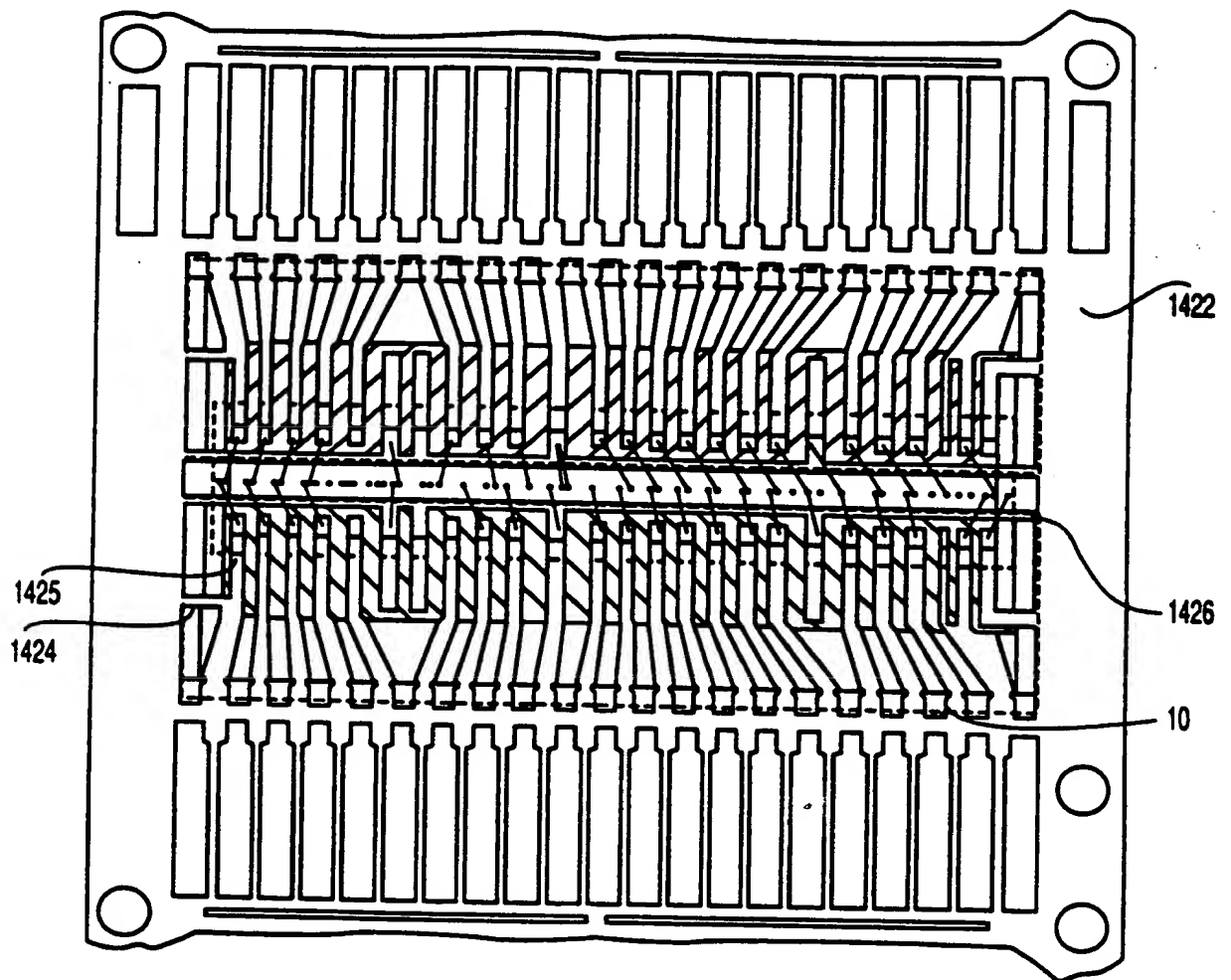


FIG. 110



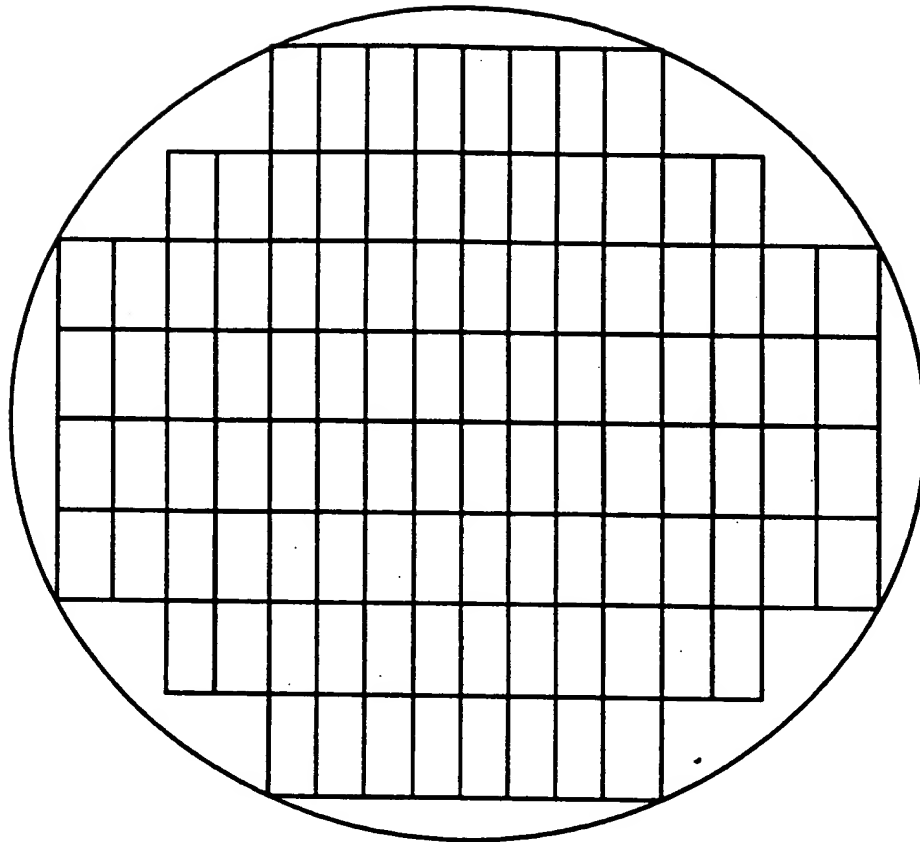
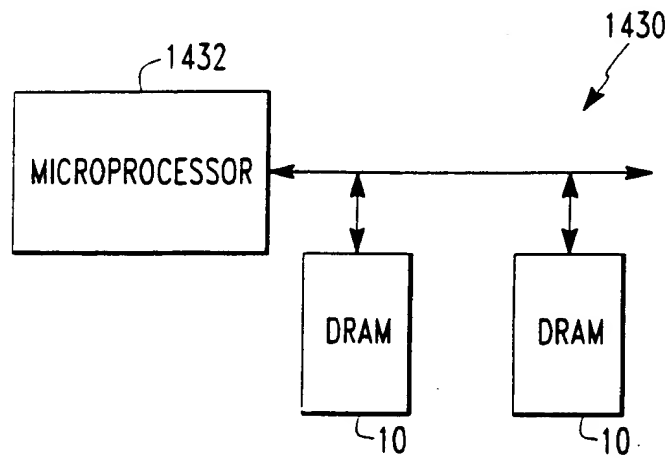


FIG. 112

*FIG. 113*